

05-29-2001



FORM PTO-1595

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

1-31-92

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HEET

TRADEMARKS ONLY

5.24.01

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Advanced Hardware Architectures Inc

Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies):
Name: **Silicon Valley Bank**
Internal Address: **HA155**

MAY 24 2001

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other _____

Execution Date: 04/23/01

Street Address: **3003 Tasman Drive**

City: **Santa Clara** State: **CA** ZIP: **95054**

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or trademark number(s):
If this document is being filed together with a new application, the execution date of the application is: _____
A. Trademark Application No.(s)
1,927,172
75/462,519
76/175,151
76/174,780
76/174,654
B. Trademark No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: **Silicon Valley Bank**

Internal Address: **Loan Documentation HA155**

Street Address: **3003 Tasman Dr.**

City: **Santa Clara** State: **Ca** ZIP: **95054**

6. Total number of applications and trademark involved: **X5**

7. Total fee (37 CFR 3.41):\$ 140.00
 Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:
(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Tracy Walton
Name of Person Signing

Signature

Date 5/15/01

Total number of pages comprising cover sheet: **6**

OMB No 0651-0011 (exp 4/94)

TRADEMARK
REEL: 002304 FRAME: 0013

ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated March 19 1999. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Advanced Hardware Architectures, Inc. ("Grantor"), with its principal office at 2365 N.E. Hopkins Court, Pullman, WA 99163, the owner of the intellectual property identified in the exhibit attached hereto, has granted to Silicon Valley Bank ("Bank"), with its principal office at 3003 Tasman Drive, Santa Clara, California, 95054, a security interest in the intellectual property described on the Exhibit(s) attached hereto, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Grantor has executed this Addendum to Intellectual Property Security Agreement as of April 23, 2001.

GRANTOR:

Advanced Hardware Architectures, Inc.

By: *E. E. Kinnelberg*

Name: *Eric E. Kinnelberg*

Title: *CFO & Secretary*

TRADEMARK

REEL: 002304 FRAME: 0014

**EXHIBIT A
COPYRIGHTS**

Description	Registration/ Application Number	Registration/ Application Date
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No Copyrights listed per search of the Copyright Office as of 5/2/01

EXHIBIT B PATENTS

Description	Registration/ Application Number	Registration/ Application Date
Single Clock Cycle Data Compressor/Decompressor with a String Reversal Mechanism	✓ 5,818,873	10/6/1998
Single Stack Implementation of Reed-Solomon Encoder/Decoder	✓ 5,396,502	3/7/1995
Adaptive Data Compression System With Systolic String Matching Logic	✓ 5,532,893	7/2/1996
Sliding Window With Big Gap Data Compression System	✓ 5,694,125	12/2/1997
Multi-port Ram for a Viterbi Decoder	5,822,341	10/13/1998
Six Transistor Dynamic Content Addressable memory Circuit	✓ 5,428,564	6/27/1995
Direct memory Access Controller In An Integrated Circuit	✓ 5,734,926	3/31/1998
Turbo Product Code Decoder	09/406,252	9/27/1999
Enhanced Turbo Product Codes	60/189,345	3/14/2000
A Method for Estimation and Removal of Co-Channel Interference in SDMA or PDMA Systems Utilizing OFDM Techniques	60/251,344	12/4/2000
Method to Improve the Performance of a Digital Modulation System	60/251,345	12/4/2000

EXHIBIT C TRADEMARKS

Description	Registration/ Application Number	Registration/ Application Date
Advanced Hardware Architectures Class 9: Computer hardware and software for use in the field of data and image compression, error correction and formatters and controllers	1,927,172	10/17/1995
Advanced Hardware Architectures & AHA Design Class 9: Computer hardware and software for use in the field of data and image compression, error correction and formatters and controllers	75/462,519	4/6/1998
AHA Class 9: Computer hardware for forward error correction, data compression and allocation and management of network and wireless communications	76/175,151	11/29/2000
AHA Class 9: Computer software for analysis of forward error correction and data compression in the field of network and wireless communication	76/174,780	11/29/2000
AHA & Design Class 9: Computer hardware for forward error correction, data compression and allocation and management of network and wireless communications	76/174,654	11/29/2000
AHA & Design Class 9: Computer software for analysis of forward error correction and data compression in the field of network and wireless communication	76/174,654	11/29/2000
AHA Benelux	663753	6/2/1999
AHA European Union	64816	4/1/1996
AHA France	94534453	8/31/1994
AHA Germany	57 677/9 Wz	8/30/1994
AHA Japan	3282040	4/18/1997
AHA United Kingdom	1583596	3/4/1994

**Exhibit "D" attached to that certain Addendum to Intellectual Property
Security Agreement dated April 23, 2001.**

EXHIBIT D

ITEM NUMBER	DESCRIPTION
01-49068-01	MO ENDEC/FORMATTER
1140-PROTO-A	FORMATTER CONSTOLLER W/SCSI
3101A-020 PQC	DATA COMPRESSION IC
3210A-020 PQC	DATA COMPRESSION IC
3210B-020 PQC	DATA COMPRESSION IC
3370A-020 PQC	IMAGE COMPRESSION IC
3371A-020 PQC	IMAGE COMPRESSION IC
3410B 025 PQC	DATA COMPRESSION IC
3410C-025 PQC	DATA COMPRESSION IC
3431A-040 PQC	DATA COMPRESSION IC
3520A-040 PQC	DATA COMPRESSION IC
3530A-040 PTC	DATA COMPRESSION IC
3540A-040 PTC	DATA COMPRESSION IC
3710A-006 PTC	DATA COMPRESSION IC
4010A-010 PJC	ERROR CORRECTION IC
4011A-040 PJC	R-S ECC COPROCESSOR
4011B-040 PJC	R-S ECC COPROCESSOR
4011C-040 PJC	R-S ECC COPROCESSOR
4210A-062 PJC	RSVP ERROR CORRECTION
4501A-050 PQC	TPC ENCODER/DECODER
4510A-015 PJC	ERROR CORRECTION IC
4600A-010 PJC	ERROR CORRECTION IC SET
5101A-060 PQC	TAPE FORMATTER CONTROLLER
5102A-060 PQC	TAPE FORMATTER CONTROLLER
5121 - PROTO	5121 Proto
5130A-120 PBC	DATA FORMATTER CONTROLLER
5140A-040 PQC	DATA FORMATTER CONTROLLER
5210A-032 PQC	MO FORMATTER CONTROLLER
AHA3212-PROTO	DATA COMPRESSION IC
AHA4522A-PROTO	TPC ENCODER/DECODER

Inventory listing.xls