

08-14-2002

FORM PTO-1594
(Rev. 03/01)
OMB No. 0651-0027 (exp. 05/31/2002)



102189970

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Sequence Design, Inc.
08/12/02
 Individual(s) Association
 General Partnership Limited Partnership
 Corporation-State
 Other
Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies):
Name: **Silicon Valley Bank**
Internal Address: HA155
Street Address: 3003 Tasman Drive
City: Santa Clara State: CA ZIP: 95054

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other _____
Execution Date: 4/17/01

Individual(s) citizenship
 Association
 General Partnership
 Limited Partnership
 Corporation-State-California
 Other
If assignee is not domiciled in the United States, a domestic representative designation is attached: Yes No
Additional name(s) & address(es) attached? Yes No

OFFICE OF PATENT & TRADEMARKS
2002 AUG 12 AM 11:04
FINANCE SECTION

4. Application number(s) or registration number(s):
A. Trademark Application No.(s)
2,306,075
2,262,053
2,252,594
2,162,591
78,066,865
78,066,862

B. Trademark No.(s)
78,060,579
78,060,004
78,060,000

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: **Silicon Valley Bank**
Internal Address: Loan Documentation HA155
Street Address: 3003 Tasman Dr.
City: Santa Clara State: Ca ZIP: 95054

6. Total number of applications and registrations involved: 9
7. Total fee (37 CFR 3.41): \$240.00
 Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:
(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

08/13/2002 DBYRME 00000165 2306075

01 FC: 481 40.00 OP
02 FC: 482 Pia A. Pena 200.00 OP

Pia A. Pena
Signature

Name of Person Signing

8/1/02
Date

Total number of pages including cover sheet, attachments, and document: 8

Mail documents to be recorded with required cover sheet information to:

TRADEMARK
REEL: 002562 FRAME: 0199

ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated April 17, 2001. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that SEQUENCE DESIGN, INC. ("Assignor"), with its principal office at 469 El Camino Real , Santa Clara, CA 95050, the owner of the intellectual property identified in the exhibits attached hereto, has granted to SILICON VALLEY BANK ("Assignee"), with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054, a security interest in the intellectual property, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Assignor has executed this Addendum to Intellectual Property Security Agreement as of May 9, 2002.

SEQUENCE DESIGN, INC.

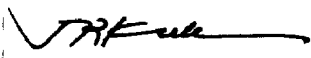
By: 
Name: VIC KULKARNI
Title: President & CEO

Exhibit "A" attached to that certain Addendum to Intellectual Property Security Agreement dated May 9, 2002.

EXHIBIT "A"
COPYRIGHTS

SCHEDULE A - ISSUED COPYRIGHTS

<u>COPYRIGHT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
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SCHEDULE B - PENDING COPYRIGHT APPLICATIONS

<u>COPYRIGHT DESCRIPTION</u>	<u>APPLICATION NUMBER</u>	<u>DATE OF FILING</u>	<u>DATE OF CREATION</u>	<u>FIRST DATE OF PUBLIC DISTRIBUTION</u>
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SCHEDULE C - UNREGISTERED COPYRIGHTS (Where No Copyright Application is Pending)

<u>DESCRIPTION</u>	<u>CREATION</u>	<u>DISTRIBUTION FROM ASSIGNOR</u>	<u>ASSIGNOR</u>
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Exhibit "B" attached to that certain Addendum to Intellectual Property Security Agreement dated May 9, 2002.

EXHIBIT "B"
PATENTS

<u>PATENT</u> <u>DESCRIPTION</u>	<u>DOCKET NO.</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>FILING DATE</u>	<u>STATUS</u>
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See attached

Exhibit "B" attached to that certain Addendum to Intellectual Property Security Agreement dated May 9, 2002.

<i>Patent Description</i>	<i>Docket Number</i>	<i>Country</i>	<i>Serial Number</i>	<i>Patent Number</i>	<i>Filing Date</i>	<i>Status</i>
Methods for Determining on-chip Interconnect Process Parameters	M-4833 US	United States	08/937393	6057171	9/25/97	Granted 5/2/00
Methods for Determining on-chip Interconnect Process Parameters	M-4833 TW	Taiwan	87115892	136000	9/24/98	Granted 11/1/01
Methods for Determining on-chip Interconnect Process Parameters	M-4833-ID US	United States	09/245812	6312963	2/4/99	Granted 11/6/01
Methods for determining on-chip Interconnect Process Parameters	M-4833-2D US	United States	09/244616	6291254	2/4/99	Granted 9/18/01
System and Method for Extracting Parasitic Impedance from an Integrated Circuit	M-4310 US	United States	08/804524	5901063	2/21/97	Granted 5/4/99
Method and System for Extraction of Parasitic Interconnect Impedance Including Inductance	M-7493 US	United States	09/350966	6381730	7/9/99	Granted 4/30/02
Method for Modeling a Conductive Semiconductor Substrate	M-7781 US	United States	09/405510	6311312	9/23/99	Granted 10/30/01

EXHIBIT "C"
TRADEMARKS

<u>TRADEMARK DESCRIPTION</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
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See Attached

EXHIBIT C
Trademarks

<i>Description</i>	<i>Registration/Application Number</i>	<i>Registration Application Date</i>
Sente	2,306,075	1/4/00
Sente	2,262,053	7/20/99
Wattwatcher	2,252,594	6/15/99
Columbus	2,162,591	5/24/96
ExtractionStage	78,066,865	6/1/01
PowerTheater	78,066,862	6/1/01
FullContext	78,066,579	5/31/01
Physical Studio	78,060,004	4/24/01
Showtime	78,060,000	4/24/01
Nanocool	TM-2000US	6/24/02

EXHIBIT "D"
SERVICE MARKS

<u>SERVICE MARK</u>	<u>DESCRIPTION</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
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