

8/13/03

08-21-2003



REGISTRATION 102530522 TRADEMARKS ONLY

U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office

Form PTO-1594 (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005) Tab settings

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies): Zeevo, Inc. Individual(s) Association General Partnership Limited Partnership Corporation-State Other Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies) Name: Comdisco Ventures, Inc. Internal Address: Legal Department Street Address: 6111 North River Road City: Rosemont State: IL Zip: 60018 Individual(s) citizenship Association General Partnership Limited Partnership Corporation-State DE Other If assignee is not domiciled in the United States, a domestic representative designation is attached: Yes No (Designations must be a separate document from assignment) Additional name(s) & address(es) attached? Yes No

TRADEMARK FEE PROCESS RECEIVED 2003 AUG 13 P 3:21

3. Nature of conveyance: Assignment Merger Security Agreement Change of Name Other Execution Date: 07/16/2003

4. Application number(s) or registration number(s): A. Trademark Application No.(s) 76/167,150 76/167,619 78/030,222 78/016,453 B. Trademark Registration No.(s) Additional number(s) attached Yes No

5. Name and address of party to whom correspondence concerning document should be mailed: Name: Gem Mil Langit Internal Address: Winston & Strawn Street Address: 101 California Street Suite 3900 City: San Francisco State: CA Zip: 94111

6. Total number of applications and registrations involved: 4 7. Total fee (37 CFR 3.41): \$ 115.00 Enclosed Authorized to be charged to deposit account 8. Deposit account number: 20-0052

DO NOT USE THIS SPACE

9. Signature. Christina E Wilson Gem Mil Langit Name of Person Signing Signature Date 08/11/03

Total number of pages including cover sheet, attachments, and document: 6

06/20/2003 ECDOPER 00000049 200052 76167150

Mail documents to be recorded with required cover sheet information to: Commissioner of Patent & Trademarks, Box Assignments Washington, D.C. 20231

01 FC:0521 40.00 BA 02 FC:0522 75.00 BA

TRADEMARK REEL: 002805 FRAME: 0759

REAFFIRMATION AND GRANT OF SECURITY INTEREST
IN PATENTS AND TRADEMARKS

THIS REAFFIRMATION AND GRANT OF SECURITY INTEREST, dated as of July 16, 2003, is executed by ZEEVO, INC., a Delaware corporation ("*Debtor*"), in favor of COMDISCO VENTURES, INC. ("*Secured Party*.")

- A. Debtor and Secured Party are parties to a certain Collateral Grant of Security Interest of Patents and Trademarks dated as of November 16, 2000 granting a security interest in Debtor's trademarks and patents then in existence and thereafter acquired.
- B. Pursuant to a Restructuring Agreement dated as of July 16, 2003 (as amended from time to time in writing, the "*Restructuring Agreement*") between Debtor and the Secured Party, Secured Party agreed to reschedule certain payments due under the Master Lease and to consent to a certain Bridge Financing, as defined in the Restructuring Agreement upon the terms and subject to the conditions set forth therein.
- C. Debtor owns the trademarks and/or applications for trademarks, more particularly described on Schedule 1-A annexed hereto as part hereof (collectively, the "*Trademarks*").
- D. Debtor owns the letters patent and/or applications for letters patent, of the United States, more particularly described on Schedule 1-B annexed hereto as part hereof (collectively, the "*Patents*").
- E. Pursuant to the Restructuring Agreement, Debtor has reaffirmed its prior grant of security interest in the Patents and Trademarks, and further granted to Secured Party a security interest in all right, title and interest of Debtor in and to in and to the Trademarks and the Patents, together with any reissue, continuation, continuation-in-part or extension thereof, and all proceeds thereof, including any and all causes of action which may exist by reason of infringement thereof for the full term of the Trademarks and the Patents (collectively, the "*Collateral*"), to secure the prompt payment, performance and observance of the Secured Obligations (as defined in the Restructuring Agreement).

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, Debtor does hereby reaffirm and further grant to Secured Party upon the terms of the Restructuring Agreement, a security interest in the Collateral to secure the prompt payment, performance and observance of the Secured Obligations as defined in the Restructuring Agreement.

Debtor does hereby further acknowledge and affirm that the rights and remedies of Secured Party with respect to the security interest in the Collateral granted hereby are more fully set forth in the Restructuring Agreement, the terms and provisions of which are hereby incorporated herein by reference as if fully set forth herein.

IN WITNESS WHEREOF, Debtor has caused this instrument to be executed as of the day and year first written above.

ZEEVO, INC.

By: Scott A Malcomber
Name: SCOTT A MALCOMBER
Title: CEO

TRADEMARKS AND TRADEMARKS APPLICATIONS

TRADEMARK APPLICATIONS			
Name	Date Filed	Serial Number	Status
ZEEVO	November 17, 2000	76/167,150	
ZEEVO	November 17, 2000	76/167,619	
BLUE SHARK	October 11, 2000	78/030,222	
[ENABLING PERVASIVE CONNECTIVITY]	July 12, 2000	78/016,453	

SCHEDULE 1-B TO REAFFIRMATION AND GRANT OF SECURITY INTEREST

PATENTS AND PATENT APPLICATIONS

PATENTS		
Patent Name	Status and Date Issued	Patent Number
Automatic Gain Control Circuit for Signal with Diverse Power Level Range	7/16/2002	6420934
Area Efficient Bond Pad Placement	12/03/2002	6489688
Oscillator Circuit with Signal Buffering & Start-Up Circuitry	9/03/2002	6445258
Seal Ring Structure for IC Containing Integrated Digital/RF/Analog Circuits & Functions	12/10/2002	6492716
Efficient AC Coupled CMOS RF Amplifier	5/27/03	6570450

PATENT APPLICATIONS		
Name	Status and Date Filed	Application Number
Method for Designing Mixed Signal Integrated Circuits and Configurable Synchronous Digital Noise Emulator Circuit	7/19/2000	09/618971
Reduced Instruction Set Baseband Controller	3/18/2002	10/101396
System for Maintaining Synchronization Between Multiple Asynchronous Communication Links	3/19/2002	10/101676
Split Source RF Mosfet Device	3/18/2002	10/101405
DC Offset Correction Technique, Applicable for use in an Enhanced Direct Conversion Radio Architecture	3/20/2001	09/813420
DC Offset Calibration for a Radio Transceiver Mixer	5/15/2001	09/858812

Name	Status and Date Filed	Application Number
Structure & Method to Increase Density of MIM Capacitors in Damascene Process	4/26/2001	09/844293
Logarithmic IF Amplifier with Dynamic Large Signal Bias Circuit	4/16/2001	09/836624
Low Power Large Signal RF Tuned Buffer Amplifier	4/13/2001	09/835021
CMOS Frequency Conversion Using Dual Mixers for Sideband Suppression	4/2/2001	09/825250
Transconductance Device Employing Native MOS Transistors	4/18/2001	09/837897
Method and Apparatus for Co-Location of Two Radio Frequency Devices	8/29/2002	10/233237
Co-Location of Two RF Devices	12/28/2001	60/346315
[Reducing size of highly integrated device packaged on LTCC (temp. title)]	new	
Analog Memory Cell in a Low Power Oscillator	7/26/2002	10/206256
Analog Hold Cell in a CMOS Process	7/02/2002	10/190129
Single Ended Tuning of a Differential Power Amplifier Output	9/23/2002	10/253460
Package Filter and Combiner Network	9/30/2002	10/262336
Package Filter Combiner Network	5/15/2002	60/381387
Solder Pad Structure for Low Temperature Co-Fired Ceramic Package and Method for Making the Same	9/4/2002	10/235762
[Fully Differential, High-Q, On-Chip, Impedance Matching Section]	New	
[Three-Line Balun with power Amplifier bias (temp.title)]	New	