Patent and Trademark Office

FORM PTO-1594

(Rev 5-93)

2501487-24

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To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof. 2. Name and address of receiving party(ies): 1. Name of conveying party(ies): SONICS, INC. Name: SILICON VALLEY BANK Address: 3003 TASMAN DRIVE City: SANTA CLARA Zip: 95054-1191 Individual(s) citizenship: Association: Individual(s) citizenship: General Partnership: Association: Limited Partnership: General Partnership: Corporation - State: DELAWARE Limited Partnership: Other: Corporation - State: Other: unknown Additional name(s) of conveying party(ies) attached? [] Yes [X] No 3. Nature of Conveyance: If assignee is not domiciled in the United States, a domestic representative designation is attached: [] Yes [] No] Assignment Merger (Designations must be a separate document from assignment)] Change of Name [X] Security Agreement [] Other Additional name(s) & address(es) attached? [] Yes . [x] No Execution Date: February 27, 2004 4. Application number(s) or trademark number(s): A. Trademark Application No.(s) B. Trademark Registration No.(s) 2,712,27 76/417,583 76/385,374 2,342,271 2,618,548 76/412.887 76/418.875 76/421.329 76/421,333 76/434,499 78/168,826 Additional numbers attached? [] Yes [x] No 5. Name and address of party to whom correspondence concerning document 6 Total number of applications and registrations involved: 12 Erin O'Brien Name: Internal Address: GRAY CARY WARE & FREIDENRICH 4365 Executive Drive, Suite 1100 San Diego, California 92121-2133 7. Total fee (37 CFR 3.41) \$315.00 [X] Enclosed [] Authorized to be charged to deposit account 8. Deposit account number: (Attach duplicate copy of this page if paying by deposit account) DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Erin O'Brien Name of Person Signing Total number of pages comprising cover sheet: [8] Mail Documents to be recorded with required cover sheet information to: US. Patent and Trademark Office, Office of Public Records 1213 Jefferson Davis Highway, 3rd Floor 03/10/20#4 LMUELLER 00000049 75732079 Arlington, VA 22202 01 FC:8521 02 FC:8522 40.00 DP 275.00 pp Gray Cary\PA\10347262.1

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement is entered into as of February 27, 2004, by and between SILICON VALLEY BANK ("Bank") and Sonies, Inc. ("Grantor").

RECITALS

- A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in that certain Loan and Security Agreement by and between Bank and Grantor, dated as of the date hereof (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works to secure the obligations of Grantor under the Loan Agreement.
- B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including, without limitation those Copyrights, Patents, Trademarks and Mask Works registered in the United States and listed on Schedules A. B. C. and D hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

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03/08/2004 MON 09:26 [TX/RX NO 5213] 2002

Attn: Loan Services

IN WITNESS WHEREOF, the parties have cause this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

GRANTOR:

Address of Grantor: SONICS, INC.

2440 W. El Camino Real, Suite 600

Mountain View, CA 94040

By: Start Screen

Aun: Chief Financial Officer

Title: | TRESIDENT 4 CILC

Aun: Chief Financial Officer

BANK:

Address of Bank: SILICON VALLEY BANK

3003 Tasman Drive
Santa Clara, CA 95054-1191
Title: 5 VP

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2

03/08/2004 MON 09:26 [TX/RX NO 5213] 2003

Schedule A

United States Copyrights

3

Description

Registration/ Application Number Registration/ Application Date

Gray Cary\PA\10344456.6

03/08/2004 MON 09:26 [TX/RX NO 5213] 2004

Schedule B

United States Patents and Patent Applications

Description	Registration/	Registration
Оезопрядон	Application Number	Application <u>Date</u>
A Fully Pipelined Fixed-Latency Communications System with a Real-Time Dynamic Bandwidth Allocation	5,948,089	9/7/1999
Communications System and Methods with Multi-Level Connection Identification	6,182,183	1/30/2001
Method and Apparatus for Communication Using a Distributed Multiplexed Bus	6,683,474 B2	1/27/2004
Communication System and Method for Different Quality of Service Guarantees For Different Data Flows	6,330,225	12/11/2001
Method and Apparatus for Scheduling Requests Using Ordered Stages of Scheduling Criteria	6,578,117	6/10/2003
Communications System and Methods with Multi-Level Connection Identification	09/721,044	11/21/2000
Logic System with Configurable Interface	09/634,045	8/8/2000
Communication System and Method with non-Blocking Shared Interface	09/802,405	3/29/2001
Method and Apparatus for Response Modes in a Pipelined Environment	09/872,126	6/1/2001
Method and Apparatus for Evaluation an Integrated Circuit Model	10/163,192	6/4/2001
Method and Apparatus for Scheduling A Resource to Meet Quality of Service Restrictions	09/977,602	10/12/2001
Method and Apparatus for Scheduling of Requests to A Dynamic Random Access Memory Device	09/977,510	10/12/2001
Method and Apparatus for Scheduling Requests to a Resource Using a Configurable Threshold	09/977,600	10/12/2001
Scalable Low Bandwidth Multicast Handling in Mixed Core Systems	10/144,073	5/10/2002
Communication System and Method with Configurable Posting Points	10/138,284	5/3/2002
Composing On-Chip Interconnects with Configurable Interface	10/138,946	5/3/2002
A Method and Apparatus for Optimizing Distributed Multiplexed Bus Interconnects	10/146,989	5/15/2002
On-Chip Inter-Network Performance Optimization Using Configurable Performance Parameters	10/138,772	5/3/2002

Gray Cary\PA\10344456.6

4

03/08/2004 MON 09:26 [TX/RX NO 5213] 20005

Method and Apparatus for Error Handling in Networks	10/285,987	10/1/2002
Method and Apparatus for Automatic Configuration of Multiple On-Chip Interconnects	10/330,591	12/27/2002
Methods and Apparatus for Speculative Response Arbitration to Improve System Latency	10/286,721	11/1/2002
Methods and Apparatus for Configurable Address Mapping and Protection Architecture and Hardware for On-chip Systems	10/288,973	11/5/2002
A Method and Apparatus for Decomposing and Verifying Configurable Hardware	10/293,734	10/12/2002
A Method and Apparatus for Round Robin Resource Arbitration	10/418,624	4/18/2003
Various Methods and Apparatuses for Arbitration Among Blocks of Functionality	10/418,370	4/18/2003
Various Method and Apparatuses for Interfacing of a Protocol Monitor to Protocol Checkers and Functional Checkers	Application Number Not Yet Assigned	7/24/2003
Low Power Shared Link Arbitration	Application Number Not Yet Assigned	10/3/2003
Method and Apparatus for Establishing a Quality of Service Model	Application Number Not Yet Assigned	10/31/2003

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03/08/2004 MON 09:26 [TX/RX NO 5213] 2006

Schedule C

United StatesTrademarks

Description	Registration/ Application <u>Number</u>	Registration Application <u>Date</u>
Sonics, Inc. and Design	2,342,271	4/18/2000
CoreCreator	2,618,548	9/10/2002
SocCreator	2,712,271	4/29/2003
SocBuilder	75/732,079	6/18/1999
Sonics, Inc.	76/417,583	6/5/2002
MemMax	76/385,374	3/21/2002
Sonics	76/412,887	5/28/2002
Box Logo Design	76/418,875	6/12/2002
Sonics Smart Interconnect IP and Design	76/421,329	6/12/2002
Sonics Smart Interconnect IP	76/421,333	6/12/2002
Soc Works	76/434,499	7/24/2002
Synapse	78/168,826	9/27/2002

Gray Cary\PA\10344456.6 2501487-24

03/08/2004 MON 09:26 [TX/RX NO 5213] 2007

Schedule D

United States Mask Works

Description

Registration/ Application Number Registration/ Application Date

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7

03/08/2004 MON 09:26 [TX/RX NO 5213] 2008