

10-23-2006

U.S. DEPARTMENT OF

United States Patent and Trademark Office



To the Director of the U.S. Patent and Tr

uments or the new address(es) below.

103325560

1. Name of conveying party(ies):

Silicon Valley Bank
3003 Tasman Drive
Santa Clara, CA 95054

- Individual(s)
- General Partnership
- Association
- Limited Partnership

- Corporation-State
- Other

Additional name(s) of conveying parties attached? Yes No

3. Nature of conveyance/ Execution Date(s):

Execution Date(s): **September 19, 2006**

- Assignment
- Security Agreement
- Merger
- Change of Name

Other : **Release**

4. Application number(s) or registration number(s) and identification or description of the Trademark:

A. Trademark Application No.(s)

75131758

75487281

75743461

76089374

76089394

B. Trademark Registration No.(s)

C. Identification or Description of Trademark(s) (and Filing Date if Application or Registration Number is unknown):

Additional sheets attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: **Silicon Valley Bank**

Internal Address: **HF154**

Street Address: **3003 TASMAN DRIVE**

City: **SANTA CLARA** State: **CA** ZIP: **95054**

Phone Number: **(408)654-4044**

Fax Number: **(408)654-6313**

Email Address: **AMDC@SVB.COM**

6. Total number of applications and registrations involved: **5**

7. Total fee (37 CFR 2.6 (b)(6) & 3.41): **\$ 140.00**

- Authorized to be charged by credit card
- Authorized to be charged to deposit account
- Enclosed

8. Payment Information:

a. Credit Card Last 4 Numbers
Expiration Date

b. Deposit Account Number
Authorized User Name

9. Signature

Signature

September 19, 2006

Date

10/20/2006 DBYRNE

00000136 75131758

01 FC:0521

02 FC:0522

Kelly Green

Not a Person Signing

Total number of pages including cover sheet, attachments, and document:

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450

TRADEMARK

REEL: 003415 FRAME: 0001

10-20-06

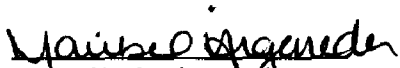
OFFICE OF PUBLIC RECORDS
2006 OCT 20 AM 11:24
FINANCE SECTION

RELEASE OF SECURITY AGREEMENT COVERING
INTERESTS IN TRADEMARKS

Silicon Valley Bank ("Secured Party"), hereby releases its security interest in the interests of **Netlogic Microsystems, Inc.** ("Assignor") in the trademarked works set forth in that certain **Intellectual Property And Security Agreement** dated, March 30, 2004, executed by Assignor in favor of Secured Party recorded with the United States Department of Commerce, Patent and Trademark Office on April 19, 2004, Reel 2953, Frame 0246.

Dated: **September 19, 2006**

SILICON VALLEY BANK

By: 
Name: Maribel Higareda
Title: Operations Supervisor

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement is entered into as of March 30, 2004, by and between SILICON VALLEY BANK ("Bank") and NETLOGIC MICROSYSTEMS, INC. ("Grantor").

RECITALS

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in the Amended and Restated Loan and Security Agreement by and between Bank and Grantor, dated March 30, 2004 (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement), and the AR Financing Loan Agreement by and between Bank and Grantor, dated March 30, 2004 (as the same may be amended, modified or supplemented from time to time, the "Formula Revolver Loan Agreement"). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works to secure the obligations of Grantor under the Loan Agreement, the other Loan Documents and the Formula Revolver Loan Agreement.

B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

To secure its obligations under the Loan Agreement, the other Loan Documents and the Formula Revolver Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including without limitation those Copyrights, Patents, Trademarks and Mask Works listed on Schedules A, B, C, and D hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and

concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

Upon the earlier of (1) the IPO, and (2) Bank's determination that Grantor has had two (2) consecutive fiscal quarters with a net profit, as determined in accordance with GAAP, Bank's security interest in the Intellectual Property Collateral shall terminate and Bank shall take all actions reasonably required to give effect to, and evidence of, the release of the Intellectual Property from the Collateral. The foregoing notwithstanding, the Collateral shall include at all times all accounts and general intangibles that consist of rights to payment and proceeds from the sale, licensing or disposition of all or any part, or rights in, the Intellectual Property (the "Rights to Payment"). Moreover, if a judicial authority (including a U.S. Bankruptcy Court) holds that a security interest in the underlying Intellectual Property is necessary to have a security interest in the Rights to Payment, then the Collateral shall automatically and at all times include the Intellectual Property to the extent necessary to permit perfection of Bank's security interest in the Rights to Payment.

IN WITNESS WHEREOF, the parties have cause this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

GRANTOR:

Address of Grantor:

NETLOGIC MICROSYSTEMS, INC.

450 National Avenue
Mountain View, CA 94043

By: *Paul S. JL*

Attn: *DOU WETMER*

Title: *PRESIDENT & CEO*

BANK:

Address of Bank:

SILICON VALLEY BANK

3003 Tasman Drive
Santa Clara, CA 95054-1191

By: *Musa Di*

Attn: Loan Services

Title: *VICE PRESIDENT*

EXHIBIT A

Copyrights

Description

Registration/
Application
Number

Registration/
Application
Date

None registered.

EXHIBIT B

Patents

Description

Registration/
Application
Number

Registration/
Application
Date

Please see attached sheet for patent listing.

EXHIBIT C

Trademarks

Description

Registration/
Application
Number

Registration/
Application
Date

Please see attached sheet for trademark listing.

Attachment to

EXHIBIT C – TRADEMARKS
IP Security Agreement

Trademark	Filing Date	Serial No.	Status	Publ Date	Reg Date	Reg No.
SyncCAM	7/9/1996	75/131,758	Registered	3/4/1997	3/17/1998	2,145,371
IPCAM	5/18/1998	75/487,281	Registered	4/13/1999	12/21/1999	2,302,875
Enabling the Silicon Backbone for the Internet	7/6/1999	75/743,461	Registered	10/24/2000	7/17/2001	2,470,393
Zero Table Management	7/14/2000	76/089,374	Registered	4/9/2002	2/3/2004	2,810,999
ZTM	7/14/2000	76/089,394	Registered	3/12/2002	3/6/2004	2,714,043

EXHIBIT D

Mask Works

Description

Registration/
Application
Number

Registration/
Application
Date

None registered.

Exhibit "B"
Patents

Title	Filing Date	Application No.	Status	Issue Date
Synchronous Content Addressable Memory with Single Cycle Operation	30-Oct-97	08/967,314	Patent No. 6,199,140	6-Mar-01
Method and Apparatus for Cascading Content Addressable Memory Devices	30-Dec-97	09/001,110	Patent No. 6,148,364	14-Nov-00
Method and Apparatus for Implementing a LEARN Instruction in a Content Addressable Memory Device	11-May-98	09/076,337	Patent No. 6,219,748	17-Apr-01
Method and Apparatus for Implementing a LEARN Instruction in a Depth Cascaded Content Addressable Memory System	11-May-98	09/076,336	Patent No. 6,240,485	29-May-01
Method and Apparatus for Performing a Read Next Highest Priority Match Instruction in a Content Addressable Memory Device	06-Jul-98	09/111,364	Patent No. 6,381,673	30-Apr-02
Ternary Content Addressable Memory With Compare Operand Selected According to Mask Value	09-Sep-98	09/150,517	Patent No. 6,418,042	9-Jul-02
Method for Longest Prefix Matching in a Content Addressable Memory	05-Jan-99	09/225,918	Patent No. 6,237,061	22-May-01
Match Line Control Circuit for Content Addressable Memory	05-Jan-99	09/225,919	Patent No. 6,125,049	26-Sep-00
Method and Apparatus For Simultaneously Performing a Plurality of Compare Operations in Content Addressable Memory Device	26-Mar-99	09/276,885	Patent No. 6,137,707	24-Oct-00
Method and Apparatus for Determining a Longest Prefix Match in a Content Addressable Memory Device	22-Jun-99	09/338,452	Patent No. 6,460,112	1-Oct-02
Method for Apparatus For Detecting Multiple Matches in A Content Addressable Memory	12-Jul-99	09/351,962	Patent No. 6,175,513	16-Jan-01
Method Of Generating an Almost Full Flag and a Full Flag in a Content Addressable Memory	12-Jul-99	09/351,545	Patent No. 6,393,514	21-May-02
Method and Apparatus for Selective Match Line Pre-charging in a Content Addressable Memory	12-Jul-99	09/351,541	Patent No. 6,166,939	26-Dec-00
Selective Match Line Pre-Charging in a Partitioned Content Addressable Memory Array	09-Sep-99	09/391,989	Patent No. 6,243,280	5-Jun-01
Selective Match Line Discharging in A Partitioned Content Addressable Memory Array	09-Sep-99	09/392,972	Patent No. 6,191,969	20-Feb-01
Method and Apparatus for Performing Packet Classification for Policy-Based Packet Routing	23-Sep-99	09/406,170	Pending	
Row Redundancy For Content Addressable Memory	18-Oct-99	09/420,516	Patent No. 6,275,426	14-Aug-01
Method and Apparatus for Determining a Longest Prefix Match in a Segmented Content Addressable Memory Device	12-Nov-99	09/439,834	Patent No. 6,499,081	24-Dec-02
Method and Apparatus for Determining an Exact Match in a Ternary Content Addressable Memory Device	12-Nov-99	09/442,042	Patent No. 6,539,455	25-Mar-03
Ternary Content Addressable Memory Cell	12-Nov-99	09/439,317	Patent No. 6,154,384	28-Nov-00
Method and Apparatus for Determining the Address of the Highest Priority Matching Entry in a Segmented Content Addressable Memory Device	06-Dec-99	09/455,726	Patent No. 6,591,331	8-Jul-03
Match Line Control Circuit for Content Addressable Memory	21-Dec-99	09/471,103	Patent No. 6,147,891	14-Nov-00
Method and Apparatus for Preventing Match Line Discharging in a CAM	12-May-00	09/570,746	Patent No. 6,191,970	20-Feb-01
Spare Address Decoder	08-Jun-00	09/590,792	Patent No. 6,229,742	8-May-01
Method and Apparatus for Partitioning a Content Addressable Memory Device	08-Jun-00	09/590,642	Patent No. 6,324,087	27-Nov-01
Method and Apparatus for Address Translation in a Partitioned Content Addressable Memory Device	08-Jun-00	09/590,428	Pending	

file: B5

Title	Filing Date	Application No.	Status	Issue Date
Method and Apparatus for Re-Assigning Priority in a Partitioned Content Addressable Memory Device	08-Jun-00	09/590,775	Patent No. 6,687,785	3-Feb-04
Row Redundancy in a Content Addressable Memory	08-Jun-00	09/590,779	Patent No. 6,249,467	19-Jun-01
Inter-row configurability of Content Addressable Memory	14-Jun-00	09/594,203	Patent No. 6,252,789	26-Jun-01
Method and Apparatus For Using an Inter-Row Configurable Content Addressable Memory	14-Jun-00	09/594,199	Patent No. 6,246,601	12-Jun-01
Intra-Row Configurability of Content Addressable Memory	14-Jun-00	09/594,206	Pending	
Method and Apparatus for Loading Comparand Data into a Content Addressable Memory System	14-Jun-00	09/594,209	Pending	
Method and Apparatus for Loading Comparand Data into a Content Addressable Memory System	14-Jun-00	09/594,420	Patent No. 6,243,281	5-Jun-01
System	14-Jun-00	09/594,201	Pending	
Method and Apparatus for Detecting a Match in an Intra-Row Configurable CAM Device	14-Jun-00	09/594,194	Pending	
Method and Apparatus for Detecting a Multiple Match in an Intra-Row Configurable CAM Device	14-Jun-00	09/594,202	Pending	
Method and Apparatus for Determining a Match Address in an IntraRow Configurable CAM Device	14-Jun-00	09/594,195	Patent No. 6,560,670	6-May-03
Inter-row Configurability of Content Addressable Memory	16-Jun-00	09/595,850	Patent No. 6,317,350	13-Nov-01
Hierarchical Depth Cascading of Content Addressable Memory Devices	16-Jun-00	09/595,850	Patent No. 6,697,911	24-Feb-04
Content Addressable Memory Device Having Selective Cascade Logic And Method for Selectively Combining Match Information in a CAM Device	16-Jun-00	09/595,773	Patent No. 6,493,793	10-Dec-02
Method and Apparatus for Performing Priority Encoding in a Segmented Classification System	05-Dec-00	09/729,871	Pending	
Method and Apparatus for Generating a Device Index in a Content Addressable Memory	08-Dec-00	09/733,819	Patent No. 6,490,650	3-Dec-02
Synchronous Content Addressable Memory	06-Feb-01	09/778,170	Patent No. 6,697,911	24-Feb-04
Selective Look-Ahead Match Line Pre-charging in a Partitioned Content Addressable Memory Array	20-Mar-01	09/813,900	Patent No. 6,430,074	6-Aug-02
Multi-chip Module Having Content Addressable Memory	22-Mar-01	09/815,232	Patent No. 6,521,994	18-Feb-03
Transparent cascade of CAM devices	22-Mar-01	09/815,233	Pending	
Method and Apparatus for Selecting a Most Significant Priority Number for a Device Using a Partitioned Priority Index Table	24-Mar-01	09/815,921	Pending	
Enable Signals	24-Mar-01	09/815,778	Pending	
Classless Interdomain Routing Using Binary Content Addressable Memory	09-Apr-01	09/829,355	Pending	
Memory Storage Cell Based Array of Counters	30-Apr-01	09/846,513	Patent No. 6,567,340	20-May-03
Row Redundancy in a Content Addressable Memory	18-Jun-01	09/866,235	Patent No. 6,445,628	3-Sep-02
Content Addressable Memory with Error Detection	03-Aug-01	09/922,423	Pending	
Concurrent Searching of Different Tables Within a Content Addressable Memory	22-Aug-01	09/935,997	Pending	
Content Addressable Memory with Configurable Class-Based Storage Partitions	27-Aug-01	09/940,832	Patent No. 6,542,391	1-Apr-03
Content Addressable Memory with Error Detection Signalling	18-Sep-01	09/954,827	Patent No. 6,597,595	22-Jul-03
Content Addressable Memory with Range Compare Function	24-Sep-01	09/963,334	Pending	
Comparand Generation in a Content Addressable Memory	31-Oct-01	10/000,158	Pending	
Content Addressable Memory with Programmable Word Width and Programmable Priority	31-Oct-01	10/000,122	Pending	
Content Addressable Memory with Selectable Mask Write Mode	31-Oct-01	09/999,800	Pending	

Title	Filing Date	Application No.	Status	Issue Date
Content Addressable Memory With Block-Selectable Mask Write Mode, Word Width and Priority	31-Oct-01	09/999,978	Pending	
Content Addressable Memory with Priority-Biased Error Detection Sequencing	01-Nov-01	10/002,713	Pending	
Method and Apparatus for Testing a Content Addressable Memory Device	01-Nov-01	10/004,209	Pending	
Method and Apparatus for Expanding Entries in a Content Addressable Memory Apparatus	10-Dec-01	10/013,245	Pending	
Method and Apparatus for Addressing Entries in a Content Addressable Memory Apparatus	10-Dec-01	10/013,250	Pending	
Method and Apparatus for Performing a Read Next Highest Priority Match Instruction in a Content Addressable Memory Device	18-Dec-01	10/025,661	Patent No. 6,564,289	13-May-03
Programmable Delay Circuit Within a Content Addressable Memory	28-Dec-01	10/040,714	Patent No. 6,650,575	18-Nov-03
Input Data Selection for Content Addressable Memory	14-Jan-02	10/047,754	Pending	
Temary Content Addressable Memory Device	23-Jan-02	10/055,058	Pending	
Content Addressable Memory Device	01-Feb-02	10/062,307	Patent No. 6,697,276	24-Feb-04
Content Addressable Memory Device	01-Feb-02	10/062,307	Pending	
Bit Level Programming Interface in a Content Addressable Memory	15-Feb-02	10/077,829	Pending	
Content Addressable Memory Having Dynamic Match Resolution	30-Mar-02	10/112,630	Patent No. 6,661,686	9-Dec-03
Content Addressable Memory With Selective Error Logging	12-Apr-02	10/121,344	Patent No. 6,690,595	10-Feb-04
Method and Apparatus for Determining a Longest Prefix Match in a Content Addressable Memory Device	23-Apr-02	10/131,370	Pending	
Method and Apparatus for Determining an Exact Match in a Content Addressable Memory Device	09-May-02	10/142,855	Patent No. 6,574,702	3-Jun-03
Content Addressable Memory Having Column Redundancy	10-May-02	10/143,051	Patent No. 6,714,430	30-Mar-04
Selectable Data Path in a Content Addressable Memory	20-May-02	10/152,551	Pending	
Content Addressable Memory Match Line Detection	28-May-02	10/156,532	Pending	
Content Addressable Memory With Simultaneous Write and Compare Function	05-Jun-02	10/163,263	Patent No. 6,707,693	16-Mar-04
CAM Device Having Multiple Index Generators	17-Jun-02	10/174,325	Pending	
Scheduling Processor	03-Jul-02	60/393,902	Pending	
Auto Read Content Addressable Memory Cell and Array	29-Jul-02	10/207,306	Pending	
Content Addressable Memory with Cascaded Array	01-Aug-02	10/211,774	Pending	
Error-Correcting Content Addressable Memory	05-Aug-02	10/213,244	Pending	
Search Selection Table in a Content Addressable Memory	30-Aug-02	10/232,576	Pending	
Selective Match Line Control Circuit for Content Addressable Memory Array	30-Aug-02	10/233,022	Pending	
Match and Priority Encoding Logic Circuit	12-Sep-02	10/243,706	Pending	
Dynamic Random Access Memory (DRAM) Based Content Addressable Memory (CAM) Cell	18-Oct-02	10/273,629	Pending	
Content Addressable Memory (CAM) Device Including Match Line Sensing	18-Oct-02	10/273,684	Pending	
BlockWritable Content Addressable Memory Device	19-Nov-02	10/300,653	Pending	
Entry Relocation in a Content Addressable Memory Device	19-Nov-02	10/300,652	Patent No. 6,700,809	2-Mar-04
Timing Execution of Compare Instructions in a Synchronous Content Addressable Memory	11-Dec-02	10/318,251	Patent No. 6,678,786	13-Jan-04
Multi-chip Module Having Content Addressable Memory	17-Dec-02	10/321,160	Pending	
Integrated Circuit Package With Solder Bumps And Method For Fabricating the Same	18-Dec-02	10/323,963	Pending	

Title	Filing Date	Application No.	Status	Issue Date
Content Addressable Memory with Configurable Class-Based Storage Partitions	11-Feb-03	10/364,147	Patent No. 6,711,049	23-Mar-04
Bit Line Control Circuit For A Content Addressable Memory	21-Mar-03	10/394,983	Pending	
Word Line Driver Circuit For A Content Addressable Memory	21-Mar-03	10/394,466	Pending	
Row Redundancy In A Content Addressable Memory Device	30-May-03	10/449,422	Pending	
Multi-Compare Content Addressable Memory Cell	30-May-03	10/448,820	Pending	
High-Density Content Addressable Memory Cell	30-May-03	10/452,230	Pending	
Content Addressable Memory Device With Source-Selecting Data Translation	30-May-03	10/448,819	Pending	
Thyristor-Based Content Addressable Memory (CAM) Cells	30-May-03	10/452,216	Pending	
Content Addressable Memory with Error Detection Signaling	03-Jun-03	10/453,719	Pending	
Content Addressable Memory with Error Detection Signaling	03-Jun-03	10/453,276	✓ Patent No. 6,700,810	2-Mar-04
Burst Insert In A Content Addressable Memory	10-Jun-03	60/477,496	Pending	
Method For Finding A Longest Prefix Match In A Ternary CAM	11-Jun-03	60/477,737	Pending	
Method and Apparatus for Calculating Packet Departure Times	02-Jul-03	10/613,891	Pending	
Method and Apparatus for Managing Individual Traffic Flows	02-Jul-03	10/613,629	Pending	
Method and Apparatus for Queuing Different Traffic Types	02-Jul-03	10/613,347	Pending	
Method and Apparatus for Throttling Selected Traffic Flows	02-Jul-03	10/613,628	Pending	
Method and Apparatus for Terminating Selected Traffic Flows	02-Jul-03	10/613,892	Pending	
Rollover Bits for Packet Departure Time Calculator	02-Jul-03	10/613,776	Pending	
Content Addressable Memory With Multi-Ported Compare and Word Length Selection	18-Jul-03	10/622,862	Pending	
Concurrent Searching of Different Tables Within a Content Addressable Memory	11-Aug-03	10/639,153	Pending	
Content Addressable Memory With Error Signaling	11-Aug-03	10/639,187	Pending	
Programmable Delay Circuit Within a Content Addressable Memory	19-Aug-03	10/644,454	Pending	
Content Addressable Memory Having Dynamic Match Resolution	02-Oct-03	10/679,073	Pending	
Content Addressable Memory Having Dynamic Match Resolution	02-Oct-03	10/679,067	Pending	
Content Addressable Memory Having Dynamic Match Resolution	08-Oct-03	10/681,525	Pending	
Multiple String Searching Using a Content Addressable Memory	03-Nov-03	10/700,722	Pending	
Low Power Content Addressable Memory Device	15-Nov-03	10/713,185	Pending	
Content Addressable Memory with Configurable Class-Based Storage Partitions	18-Nov-03	10/716,140	Pending	
Content Addressable Memory Device	22-Nov-03	10/719,099	Pending	
Content Addressable Memory With Selective Error Logging	12-Dec-03	10/734,666	Pending	
Content Addressable Memory With Mode-Selectable Match Detect Timing	12-Dec-03	10/734,464	Pending	
Content Addressable Memory with Cascaded Array	17-Dec-03	10/739,246	Pending	
Synchronous Content Addressable Memory	22-Dec-03	10/743,962	Pending	
Content Addressable Memory With Simultaneous Write and Compare Function	07-Jan-04	10/752,889	Pending	
Table Management Within A Packet Classification System	27-Jan-04	60/539,693	Pending	
Content Based Content Addressable Memory Block Enabling	06-Feb-04	10/774,168	Pending	
Segmented Content Addressable Memory Array and Priority Encoder	06-Feb-04	10/773,591	Pending	
Content Addressable Memory Having Column Redundancy	09-Feb-04	10/775,526	Pending	
Content Addressable Memory with Priority-Based Hierarchical Cascade	10-Feb-04	10/776,441	Pending	

Title	Filing Date	Application No.	Status	Issue Date
Priority Circuit for Content Addressable Memory	26-Feb-04	10789,299	Pending	
Entry Location In A Content Addressable Memory	27-Feb-04	10789,705	Pending	
Integrated Circuit Package With Solder Bumps And Method For Fabricating the Same	05-Mar-04	10794,945	Pending	
Transposing of Bits in Input Data to Form a Comparand Within a Content Addressable Memory	15-Mar-04		Pending	