

TRADEMARK ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | Security Agreement |

CONVEYING PARTY DATA

| Name | Formerly | Execution Date | Entity Type |
|------------|----------|----------------|-------------------------|
| IWATT INC. | | 09/28/2009 | CORPORATION: CALIFORNIA |

RECEIVING PARTY DATA

| | |
|-----------------|-------------------------|
| Name: | SILICON VALLEY BANK |
| Street Address: | 3003 Tasman Drive |
| City: | Santa Clara |
| State/Country: | CALIFORNIA |
| Postal Code: | 95054 |
| Entity Type: | CORPORATION: CALIFORNIA |

PROPERTY NUMBERS Total: 8

| Property Type | Number | Word Mark |
|----------------------|----------|-------------|
| Registration Number: | 3286380 | IKOR |
| Registration Number: | 2915747 | IKOR |
| Registration Number: | 2826243 | IWATT |
| Registration Number: | 2811611 | IWATT |
| Registration Number: | 1594012 | POWER BRICK |
| Serial Number: | 76071956 | IWATT |
| Serial Number: | 76085538 | PULSETRAIN |
| Serial Number: | 77480725 | EZ-EMI |

CORRESPONDENCE DATA

Fax Number: (800)680-9592
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.
 Phone: 202-783-2700
 Email: Oleh.Hereliuk@federalresearch.com
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 Address Line 1: 1023 Fifteenth Street, NW, Ste 401

CH \$215.00 3286380

Address Line 2: attn: Oleh Hereliuk
Address Line 4: Washington, DISTRICT OF COLUMBIA 20005

| | |
|-------------------------|---------------|
| ATTORNEY DOCKET NUMBER: | 445394 |
| NAME OF SUBMITTER: | Oleh Hereliuk |
| Signature: | /oh/ |
| Date: | 10/06/2009 |

Total Attachments: 21
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iWatt, Inc. - Trademark Status Report - September 9, 2009

| <i>Trademark</i> | <i>Country</i> | <i>Class</i> | <i>App. No.</i> | <i>App. Date</i> | <i>Reg No.</i> | <i>Reg Date</i> | <i>Status</i> |
|-------------------|---------------------------|--------------|-------------------|------------------|----------------|-----------------|---------------|
| EZ-EMI | China (People's Republic) | 09 | 6,986,101 | 10/6/2008 | | | Pending |
| EZ-EMI | Japan | 09 | 2008/94,412 | 11/21/2008 | | | Pending |
| EZ-EMI | Korea, Republic of | 09 | 40-2008-0,045,241 | 9/19/2008 | | | Pending |
| EZ-EMI | United States of America | 09 | 77/480,725 | 5/21/2008 | | | Allowed |
| IKOR | China (People's Republic) | 09 | 5,653,128 | 10/11/2006 | | | Pending |
| IKOR | European Union | 09; 37; 42 | 005,310,181 | 9/13/2006 | 005,310,181 | 6/5/2008 | Registered |
| IKOR | Korea, Republic of | 09 | 40-2006-0,046,447 | 9/11/2006 | 40-0,715,189 | 6/28/2007 | Registered |
| IKOR | Singapore | 09 | T06/18,919C | 9/8/2006 | T06/18,919C | 9/8/2006 | Registered |
| IKOR | Taiwan | 09 | 095,051,833 | 10/14/2006 | | | Pending |
| IKOR | United States of America | 09 | 78/955,832 | 8/18/2006 | 3,286,380 | 8/28/2007 | Registered |
| IKOR (and design) | United States of America | 09 | 76/543,919 | 8/29/2003 | 2,915,747 | 1/4/2005 | Registered |
| IWATT | China (People's Republic) | 09 | 5,711,422 | 11/9/2006 | | | Pending |
| IWATT | Japan | 09 | 2000/134,012 | 12/13/2000 | 4,487,981 | 7/6/2001 | Registered |
| IWATT | Taiwan | 09 | 89,071,309 | 12/12/2000 | 984,569 | 2/16/2002 | Registered |
| IWATT | United States of America | 09 | 76/976,256 | 6/16/2000 | 2,826,243 | 3/23/2004 | Registered |
| IWATT | United States of America | 42 | 76/071,956 | 6/16/2000 | | | Abandoned |
| IWATT (& Design) | United States of America | 09 | 76/976,165 | 9/2/2000 | 2,811,611 | 2/3/2004 | Registered |
| POWER BRICK | United States of America | 09 | 73/825,282 | 9/14/1989 | 1,594,012 | 5/1/1990 | Registered |
| PULSETRAIN | Taiwan | 09 | 89,074,354 | | 1,006,778 | 7/16/2002 | Registered |
| PULSETRAIN | United States of America | 09; 42 | 76/085,538 | 7/5/2000 | | | Abandoned |

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement is entered into as of September ²⁸, 2009 by and between SILICON VALLEY BANK ("Bank") and IWatt Inc., a California corporation ("Grantor").

RECITALS

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in that certain Loan and Security Agreement by and between Bank, Grantor and IKOR Acquisition Corporation dated November 25, 2008 (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans under the Loan Agreement, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works (as each term is described below) to secure the obligations of Grantor under the Loan Agreement.

B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its intellectual property (all of which shall collectively be called the "Intellectual Property Collateral"), including, without limitation, the following:

(a) Any and all copyright rights, copyright applications, copyright registrations and like protections in each work or authorship and derivative work thereof, whether published or unpublished and whether or not the same also constitutes a trade secret, now or hereafter existing, created, acquired or held, including without limitation those set forth on Exhibit A attached hereto (collectively, the "Copyrights");

(b) Any and all trade secrets, and any and all intellectual property rights in computer software and computer software products now or hereafter existing, created, acquired or held;

(c) Any and all design rights that may be available to Grantor now or hereafter existing, created, acquired or held;

(d) All patents, patent applications and like protections including, without limitation, improvements, divisions, continuations, renewals, reissues, extensions and continuations-in-part of the same, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the "Patents");

(e) Any trademark and servicemark rights, whether registered or not, applications to register and registrations of the same and like protections, and the entire goodwill of the business of Grantor

connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the "Trademarks");

(f) All mask works or similar rights available for the protection of semiconductor chips, now owned or hereafter acquired, including, without limitation those set forth on Exhibit D attached hereto (collectively, the "Mask Works");

(g) Any and all claims for damages by way of past, present and future infringements of any of the rights included above, with the right, but not the obligation, to sue for and collect such damages for said use or infringement of the intellectual property rights identified above;

(h) All licenses or other rights to use any of the Copyrights, Patents, Trademarks, or Mask Works and all license fees and royalties arising from such use to the extent permitted by such license or rights;

(i) All amendments, extensions, renewals and extensions of any of the Copyrights, Trademarks, Patents, or Mask Works; and

(j) All proceeds and products of the foregoing, including without limitation all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

Grantor represents and warrants as follows: Exhibit A sets forth a list of all of Grantor's registered copyrights and applications for registration of copyrights; Exhibit B sets forth a list of all of Grantor's patents and patent applications; Exhibit C sets forth a list of all of Grantor's federally registered trademarks and applications for federal registration of trademarks; and Exhibit D sets forth a list of all of Grantor's registered mask works and applications for registration of mask works.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies. This Intellectual Property Security Agreement may be executed in any number of counterparts and by different parties on separate counterparts, each of which, when executed and delivered, is an original, and all taken together constitute one Intellectual Property Security Agreement. Bank agrees to release its security interest in the Intellectual Property Collateral upon payment in full in cash of the Obligations (other than inchoate indemnity obligations and any obligations arising under any warrants issued by Grantor to Bank) and at such time as Bank's obligation to make Credit Extensions has terminated. Further, Bank agrees to modify its lien and security interest regarding the Intellectual Property Collateral under certain conditions and subject to the terms and provisions as set forth in the Loan Agreement.

[Signature page follows.]

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

Address of Grantor:

101 Albright Way, Suite D
Los Gatos, CA 95032

Attn: Chief Financial Officer

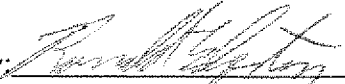
Address of Bank:

3003 Tasman Drive
Santa Clara, CA 95054-1191


Attn: Collateral

GRANTOR:

IWatt Inc.

By:  R. EOBERTON

Title: PRESIDENT & CEO


BANK: JAMES V. MCCANN, VP & CFO

SILICON VALLEY BANK

By: 

Title: Relationship Manager

EXHIBIT A

Copyrights

Description

Registration/
Application
Number

Registration/
Application
Date

None.

EXHIBIT B

Patents

Description

Registration/
Application
Number

Registration/
Application
Date

See attached Schedule.

Patent Portfolio for iWatt Inc.

July 27, 2009

| <u>CONTENTS</u> | <u>NO. OF CASES</u> |
|---|---------------------|
| Issued U.S. Patents | 33 |
| Pending U.S. Non-Provisional Applications | 11 |
| Pending U.S. Provisional Applications | 4 |
| Invention Disclosures | 2 |
| PCT/Foreign Applications | 8 |

*Note Abandoned/Expired/Inactive Matters Were Excluded From Status Report

ISSUED PATENTS

| F&W Ref. | Patent No. | Issue Date | Serial No. | Filing Date (Priority Date) | Title | Inventors | Notes |
|----------|------------|------------|------------|-----------------------------|---|--|---|
| 7390 | 6,944,034 | 9/13/2005 | 10/610,977 | 6/30/2003 | System and Method For Input Current Shaping In A Power Converter (Carmen) | Anatoly Shteynberg, J. Zheng, H. Rodriguez, M. | 7.5 year maintenance fee due on 3/13/2013. |
| 7980 | 6,956,750 | 10/18/2005 | 10/735,211 | 12/12/2003 | Power Converter Controller Having Event Generator For Detection of Events and Generation of Digital Error | Mark D. Eason; Mark Muegge; Harry Rodriguez | 7.5 year maintenance fee due on 4/18/2013. |
| 9032 | 6,990,000 | 1/24/2006 | 10/944,014 | 9/17/2004 | Reconstruction of the Output Voltage of a AC-DC Power Converter | Harry Rodriguez, Anatoly Shteynberg, Mark Telefus, Mark Eason | 3.5 year maintenance fee due on 7/24/2009. Instructed to pay. Waiting for confirmation. |
| 9270 | 6,972,969 | 12/6/2005 | 10/922,661 | 8/19/2004 | System and Method for Controlling Current Limit with Primary Side Sensing | Anatoly Shteynberg, Junjie Zheng, Harry Rodriguez, Mark Telefus, Donsheng Zhou | 7.5 year maintenance fee due on 06/06/2013. |
| 9541 | 6,882,552 | 4/19/2005 | 10/306,830 | 11/27/2002 | Power Converters with Primary-Only Feedback | Mark D. Telefus, Mark R. Muegge, Charles R. Geber, Dickson T. Wong | 3.5 year maintenance fee due on 10/19/2012. |
| 9542 | 6,894,911 | 5/17/2005 | 10/306,780 | 11/27/2002 | Method of Driving a Power Converter by Using a Power Pulse and a Sense Pulse | Mark D. Telefus, Mark R. Muegge, Charles R. Geber, Dickson T. Wong | 3.5 year maintenance fee due on 11/17/2012. |

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|------|-----------|------------|------------|------------|---|--|---|
| 9543 | 6,900,995 | 5/31/2005 | 10/306,719 | 11/27/2002 | Power Converter Controller Having Event Generator For Detection of Events and Generation of Digital Error | Mark R. Muegge, Mark Eason, Mark D. Telefus | 7.5 year maintenance fee due on 11/30/2012. |
| 9544 | 6,275,018 | 8/14/2001 | 9,585,928 | 6/2/2000 | Switching Power Converter with Gated Oscillator Controller | Mark D. Telefus, Arthur J. Collmeyer, Dickson T. Wong, David B. Manner | 10.5 year maintenance fee due on 2/14/2013. |
| 9619 | 7,024,649 | 4/4/2006 | 10/367,562 | 2/14/2003 | Multi-Output Power Supply Design System | Arthur J. Collmeyer, Thomas S. Szepesi, R. Allen Samuel, Dickson Wong | 3.5 year maintenance fee due on 10/04/2009. |
| 9731 | 6,304,473 | 10/16/2001 | 09/679,949 | 10/4/2000 | Operating a Power Converter at Optimal Efficiency | Mark D. Telefus, Dickson T. Wong, C. Geber | 10.5 year maintenance fee due on 4/16/2013 |
| 9732 | 6,324,079 | 11/27/2001 | 09/679,943 | 10/4/2000 | Four and Five Terminal PWM-Controlled Power Supply Packages | Arthur J. Collmeyer, Mark D. Telefus | 10.5 year maintenance fee due on 5/27/2013. |
| 9733 | 6,370,039 | 4/9/2002 | 09/540,058 | 3/31/2000 | Isolated Power Converter Having Primary Feedback Control | Mark D. Telefus | 7.5 year maintenance fee due on 10/9/2009. |
| 9734 | 6,381,150 | 4/30/2002 | 09/444,032 | 11/19/1999 | Isolated Dual Converter Having Primary Side Internal Feedback for Output Regulation | Mark D. Telefus | 7.5 year maintenance fee due on 10/30/2009. |
| 9735 | 6,385,059 | 5/7/2002 | 09/713,132 | 11/14/2000 | Transformer-Coupled Switching Power Converter Having Primary Feedback Control | Mark D. Telefus, Mark R. Meugge | 7.5 year maintenance fee due on 11/07/2009. |
| 9736 | 6,404,173 | 6/11/2002 | 09/627,953 | 7/28/2000 | Linear AC to DC Regulator with Synchronous Rectification | Mark D. Telefus | 7.5 year maintenance fee due on 12/11/2009. |

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|-------|-----------|-----------|-------------|------------|--|---|---|
| 9737 | 6,434,021 | 8/13/2002 | 09/7/13,541 | 11/15/2000 | Switching Power Supply Packages | Arther J. Collmeyer, Mark D. Telefus, Dickson T. Wong, and David B. Marner | 3.5 year maintenance fee due on 2/13/2010. |
| 9738 | 6,621,255 | 9/16/2003 | 10/051,760 | 1/15/2002 | Linear AC to DC Regulator with Synchronous Rectification | Mark D. Telefus | 7.5 year maintenance fee due on 03/16/2011. |
| 9739 | 6,707,285 | 3/16/2004 | 10/041,664 | 1/10/2002 | Phase Controlled AC-DC Power Converter | Mark D. Telefus, Dickson T. Wong | 7.5 year maintenance fee due on 09/16/2011. |
| 9740 | 6,765,774 | 7/20/2004 | 10/159,364 | 5/13/2002 | High Impedance Insertion System for Blocking EMI | Mark D. Telefus, Anatoly Shteynberg | 7.5 year maintenance fee due on 01/20/2012 |
| 9744 | 6,862,198 | 3/1/2005 | 10/306,728 | 11/27/2002 | Digital Regulation of Power Converters Using Primary-Only Feedback | Mark R. Muegge, Mark Eason, Mark D. Telefus | 3.5 year maintenance fee due on 09/01/2012. |
| 10711 | 7,161,816 | 1/9/2007 | 11/208,323 | 8/19/2005 | System and Method for Input Current Shaping in a Power Converter | Anatoly Shteynberg, Junjie Zheng, Harry Rodriguez, Mark Telefus, Donsheng Zhou, Arthur J. Collmeyer | 3.5 year maintenance fee due on 7/9/2010. |
| 12057 | 7,317,615 | 1/8/2008 | 10/659,995 | 6/4/2005 | Parallel Current Mode Control | Wanfeng Zhang, Yan-Fel Liu, and Bin Wu | 3.5 year maintenance fee due on 7/8/2011. |
| 12249 | 7,336,058 | 2/26/2008 | 11/671,793 | 2/6/2007 | Multistage Low Dropout Voltage Regulation | Ping Lo, Xuecheng Jin | 3.5 year maintenance fee due on 8/26/2011. |
| 12147 | 7,382,308 | 6/3/2008 | 11/676,026 | 2/16/2007 | Reference Buffer Using Current Mirrors and Source Followers to Generate Reference Voltages | Enzhu Liang, Xuecheng Jin | 3.5 year maintenance fee due on 12/3/2011. |

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|-------|-----------|------------|------------|-----------------------|--|---|---|
| 8635 | 7,433,211 | 10/7/2008 | 10/880,221 | 6/28/2004 | System and Method for Input Current Shaping in a Power Converter (Digital Carmen) | Arthur J. Collmeyer, Anatoly Shteynberg, Junjie Zheng, Harry Rodriguez, Dickson Wong, Paul King | 3.5 year maintenance fee due on 4/7/2012. |
| 12821 | 7,443,700 | 10/28/2008 | 11/745,299 | 5/7/2007 | On-Time Control For Constant Current Mode In A Flyback Power Supply | Liang Yan, Junjie Zheng, John Kesterson, Xiaoyan Wang, Hien Bui | 3.5 year maintenance fee due on 4/28/2012. |
| 12250 | 7,474,130 | 1/6/2009 | 11/671,810 | 2/6/2007 | Compensation of Voltage-to-Current Converter | Ping Lo, Xuecheng Jin | 3.5 year maintenance fee due on 7/6/2012. |
| 12070 | 7,558,093 | 7/7/2009 | 11/558,286 | 11/9/2006 | Power Converter with Emulated Peak Current Mode Control | Junjie Zheng | 3.5 year maintenance fee due on 1/7/2013. |
| 12071 | 7,505,287 | 3/17/2009 | 11/558,326 | 11/9/2006 | On-Time Control For Constant Current Mode In A Flyback Power Supply | John William Kesterson | 3.5 year maintenance fee due on 9/17/2012. |
| 13258 | 7,511,973 | 3/31/2009 | 11/856,582 | 9/17/2007 (9/18/2006) | Constant Current Mode Ripple Attenuation Method In Flyback Power Converter | John W. Kesterson, Junjie Zheng | 3.5 year maintenance fee due on 9/30/2012. |
| 13658 | 7,514,913 | 4/7/2009 | 11/951,228 | 12/5/2007 (6/4/2004) | Parallel Current Mode Control Using a Direct Duty Cycle Algorithm with Low Computational Requirements to Perform Power Factor Correction | Wanfeng Zhang, Yan-Fei Liu, and Bin Wu | 3.5 year maintenance fee due on 10/7/2012. |
| 14348 | 7,554,821 | 6/30/2009 | 12/146,261 | 6/25/2008 | System and Method for Input Current Shaping in a Power Converter (Digital Carmen) | Arthur J. Collmeyer, Anatoly Shteynberg, Junjie Zheng, Harry Rodriguez, Dickson Wong, Paul King | 3.5 year maintenance fee due on 12/30/2012. |

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|-------|-----------|-----------|------------|-----------|---|---|--|
| 14349 | 7,548,438 | 6/16/2009 | 12/146,273 | 6/28/2004 | System and Method for Input Current Shaping in a Power Converter (Digital Carmen) | Arthur J. Collmeyer, Anatoly Shteynberg, Junjie Zheng, Harry Rodriguez, Dickson Wong, Paul King | 3.5 year maintenance fee due on 12/16/2012 |
|-------|-----------|-----------|------------|-----------|---|---|--|

iWatt Patent Portfolio
November 20, 2006

PENDING U.S. NON-PROVISIONAL APPLICATIONS

| F&W Ref. | Serial No. | Filing Date (Priority Date) | Title | Inventors | Related Case(s) | Attorney | Status |
|----------|------------|-----------------------------|--|---|-----------------|----------|--|
| 12068 | 11/558,143 | 11/9/2006 | Power Converter Controller IC Having Input Voltage Pin with Multiple Functions | Allan Ming-Lun Lin, Junjie Zheng, and Jiang Chen | 10974 | JWS | Awaiting 1st office action. |
| 12072 | 11/557,953 | 11/8/2006 | Power Converter Controller Controlled By Variable Reference Voltage Generated By Dual Output Digital to Analog Converter | Allan Ming-Lun Lin, Mark Dean Eason, and Mark Robert Muegge | 10974 | JWS/DA | Allowed. WILL ISSUE 9/15/09 as Pat 7,589,983 |
| 12985 | 11/745,300 | 5/7/2007 | Digital Compensation For Cable Drop In A Primary Side Control Power Supply Controller | John W. Kesterson, Mark R. Muegge, Mark D. Eason | | JTM | Awaiting 1st office action. |
| 13237 | 11/847,147 | 8/29/2007 | Compensating On-Time Delay Of Switching Transistor In Switched Power Converters | Junjie Zheng, John W. Kesterson | UTL of 12067 | JWS | Awaiting 1st office action. |
| 13933 | 12/164,909 | 6/30/2008 | LED Driver with Multiple Feedback Loops | Yuhui ("David") Chen, Junjie Zheng, and John W. Kesterson | | JWS | Awaiting 1st Office Action. |
| 14023 | 12/174,389 | 7/16/2008 | LED Lamp | E.H. Quek, Mark R. Muegge, and Gordon Chen | | JWS | Awaiting 1st Office Action. |

iWatt Patent Portfolio
November 20, 2006

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|-------|------------|-----------|--|--|------------------|-----|---|
| 14502 | 12/199,471 | 8/27/2008 | System and Method for Controlling a Current Limit with Primary Side Sensing Using a Hybrid PWM and PFM Control | Yong Li Junjie Zheng Liang Yan John Kesterson Xiao Yan Wang | Utility of 13257 | JTM | Awaiting 1st Office Action. |
| 14553 | 12/266,483 | 11/6/2008 | Digital Phase Lock Loop With Multi-Phase Master Clock | John W. Kesterson, Carrie Seim, Selcuk Sen, and Xuecheng Jin | | JWS | Awaiting 1st Office Action. Foreign filing deadline is 11/6/2009. |
| 14849 | 12/388,709 | 2/19/2009 | Detecting Light Load Conditions and Improving Light Load Efficiency in a Switching Power Converter | Liang Yan, Xiaoyan Wang, Jun Zheng, Junjie Zheng, and Clarita Poon | | JWS | Awaiting 1st Office Action. Foreign filing deadline is 2/19/2010. |
| 14850 | 12/398,878 | 3/5/2009 | Adaptive Control for Transition Between Multiple Modulation Modes in a Switching Power Converter | Xiaoyan Wang, Liang Yan, Junjie Zheng, John William Kesterson, and Clarita Poon | | JWS | Awaiting 1st Office Action. Foreign filing deadline is 3/5/2010. |
| 15662 | 12/503,063 | 7/14/2009 | Adaptive Dimmer Detection and Control for LED Lamp | Junjie Zheng, John W. Kesterson, Richard M. Myers, Baorong Chen, and Gordon Chen | | JWS | Awaiting 1st Office Action. Foreign filing deadline is 7/14/2010. |

iWatt Patent Portfolio
November 20, 2006

PENDING U.S. PROVISIONAL APPLICATIONS

| F&W Ref. | Serial No. | Filing Date (Priority Date) | Title | Inventors | Related Case(s) | Attorney | Status |
|----------|------------|-----------------------------|---|---|-----------------|----------|--|
| 15096 | 61/140,605 | 12/23/2008 | Controller for Switching Power Converter Driving BJT Based on Primary Side Adaptive Digital Control | Yong Li, Jun Zheng, Junjie Zheng, and John William Kesterson | | JWS | US utility conversion and foreign filing deadline is 12/23/2009. |
| 15097 | 61/141,600 | 12/30/2008 | Improved Valley-Mode Switching Schemes for Switching Power Converters | Yong Li, Hien Huu Bui, Junjie Zheng, and John William Kesterson | | JWS | US utility conversion and foreign filing deadline is 12/30/2009. |
| 15098 | 61/141,059 | 12/29/2008 | Adaptive Multi-Mode Digital Control Improving Light-Load Efficiency in Switching Power Converters | Yong Li, Carrie Seim, Junjie Zheng, and John William Kesterson | | JWS/DA | US utility conversion and foreign filing deadline is 12/29/2009. |
| 15045 | 61/149,579 | 2/3/2009 | Switching Power Converter with Load Impedance Detection | Junjie Zheng | | JWS | US utility conversion and foreign filing deadline is 2/3/2010. |

iWatt Patent Portfolio
November 20, 2006

INVENTION DISCLOSURES

| F&W Ref. | Serial No. | Filing Date (Priority Date) | Title | Inventors | Related Case(s) | Attorney | Status |
|----------|------------|-----------------------------|--|--|-----------------|----------|--|
| 15356 | | | Pin Multiplexing for Integrated Circuit in Power Converter | Junjie Zheng, Liang Yan, Xiaoyan Wang, and Gordon Chen | | JWS | 1st draft of patent application was sent to inventors for review on 7/7/09. Waiting for inventors' comments. |
| 15707 | | | Adaptive Switch Mode LED Driver | Xuecheng Jin, Yu Chong Chang, Yang Li, Maofeng Lan, John W. Kesterson, Xiaoyan Wang, James Chenghung Pan | | JWS | Patent application is currently being drafted and will be forwarded to the inventors shortly. |

PCT/FOREIGN APPLICATIONS

| F&W Ref. | Serial No. | Filing Date (Priority Date) | Title | Inventors | Related Case(s) | Attorney | Status |
|-----------|-------------------|-----------------------------|---|---|----------------------|----------|---|
| 12568 PCT | PCT/CN2008/001429 | 8/6/2008 | Power Converter Using Energy Stored in Leakage Inductance of Transformer To Power Switch Controller | E.H. Quek, Jun Zheng | | JWS/DA | PCT application was filed with Chinese PCT Receiving Office. Can file counterpart US national phase application and other foreign national phase applications when iWatt wishes, by 2/6/2011. |
| 13335 PCT | PCT/CN2007/002846 | 9/28/2007 | Method and Apparatus to Dynamic Drive Switching Transistors | Junjie Zheng, Jun Zheng, Andrew Lee, John Kesterson, Allan Lin, Hieh Bien, Carrie Seim, Yong Li | 12568 US 13335 US | JWS | PCT application was filed with Chinese PCT Receiving Office. Can file counterpart US national phase application and other foreign national phase applications when iWatt wishes, 3/28/2010. |

TRADEMARK

REEL: 004074 FRAME: 0136

**iWatt Patent Portfolio
November 20, 2006**

| | | | | | | | |
|--------------|-----------------------|--------------------------|--|---|----------------------|------------|--|
| 14054 PCT | PCT/US200 8/060368 | 4/15/2008 (4/20/2007) | Method and Apparatus for Small Die Low Power System-on-Chip Design with Intelligent Power Supply Chip | Xuecheng Jin, Andrey B. Malinin, and John W. Kesterson | 14054 TW | JWS | 30 month national phase deadline is 10/20/2009. |
| 14054 TW | Taiwan 97114348 | 4/18/2008 (4/20/2007) | Method and Apparatus for Small Die Low Power System-on-Chip Design with Intelligent Power Supply Chip | Xuecheng Jin, Andrey B. Malinin, and John W. Kesterson | 14054 PCT | JWS | Request for Examination is due on 4/18/2011. |
| 14502 PCT | PCT/US200 8/074457 | 8/27/2008 (8/28/2007) | Hybrid PWM And PFM Current Limit Control | Yong Li Junjie Zheng Liang Yan John Kesterson Xiao Yan Wang | 14502 US | JTM | 30 month national phase deadline is 2/28/2010. |
| 14168 PCT | PCT/CN200 8/001964 | 12/3/2008 | Reducing EMI from Ring Oscillation in Switching Power Converter | Jun Zheng, Eng Hwee Quek, and Huaming Guo | 14168 US 13933 US | JTM JWS | PCT application was filed with Chinese PCT Receiving Office. Can file counterpart US national phase application and other foreign national phase applications when iWatt wishes, by 6/3/2011. 30 month national phase deadline is 12/30/2010. |
| 13933 PCT | PCT/US200 9/046617 | 6/8/2009 (6/30/2008) | LED Driver with Multiple Feedback Loops | Yuhui ("David") Chen, Junjie Zheng, and John W. Kesterson | 14023 US | JWS | 30 month national phase deadline is 1/16/2011. |
| 14023 PCT | PCT/US200 9/046767 | 6/9/2009 (7/16/2008) | LED Lamp | E.H. Quek, Mark R. Muegge, and Gordon Chen | | | |

EXHIBIT C

Trademarks

Description

Registration/
Application
Number

Registration/
Application
Date

See attached Schedule.

iWatt, Inc. – Trademark Status Report – September 9, 2009

| <i>Trademark</i> | <i>Country</i> | <i>Class</i> | <i>App. No.</i> | <i>App. Date</i> | <i>Reg No.</i> | <i>Reg Date</i> | <i>Status</i> |
|-------------------|---------------------------|--------------|-------------------|------------------|----------------|-----------------|---------------|
| EZ-EMI | China (People's Republic) | 09 | 6,986,101 | 10/6/2008 | | | Pending |
| EZ-EMI | Japan | 09 | 2008/94,412 | 11/21/2008 | | | Pending |
| EZ-EMI | Korea, Republic of | 09 | 40-2008-0,045,241 | 9/19/2008 | | | Pending |
| EZ-EMI | United States of America | 09 | 77/480,725 | 5/21/2008 | | | Allowed |
| IKOR | China (People's Republic) | 09 | 5,653,128 | 10/11/2006 | | | Pending |
| IKOR | European Union | 09; 37; 42 | 005,310,181 | 9/13/2006 | 005,310,181 | 6/5/2008 | Registered |
| IKOR | Korea, Republic of | 09 | 40-2006-0,046,447 | 9/11/2006 | 40-0,715,189 | 6/28/2007 | Registered |
| IKOR | Singapore | 09 | T06/18,919C | 9/8/2006 | T06/18,919C | 9/8/2006 | Registered |
| IKOR | Taiwan | 09 | 095,051,833 | 10/14/2006 | | | Pending |
| IKOR | United States of America | 09 | 78,955,832 | 8/18/2006 | 3,286,380 | 8/28/2007 | Registered |
| IKOR (and design) | United States of America | 09 | 76/543,919 | 8/29/2003 | 2,915,747 | 1/4/2005 | Registered |
| IWATT | China (People's Republic) | 09 | 5,711,422 | 11/9/2006 | | | Pending |
| IWATT | Japan | 09 | 2000/134,012 | 12/13/2000 | 4,487,981 | 7/6/2001 | Registered |
| IWATT | Taiwan | 09 | 89,071,309 | 12/12/2000 | 984,569 | 2/16/2002 | Registered |
| IWATT | United States of America | 09 | 76/976,256 | 6/16/2000 | 2,826,243 | 3/23/2004 | Registered |
| IWATT | United States of America | 42 | 76/071,956 | 6/16/2000 | | | Abandoned |
| IWATT (& Design) | United States of America | 09 | 76/976,165 | 9/2/2000 | 2,811,611 | 2/3/2004 | Registered |
| POWER BRICK | United States of America | 09 | 73/825,282 | 9/14/1989 | 1,594,012 | 5/1/1990 | Registered |
| PULSETRAIN | Taiwan | 09 | 89,074,354 | | 1,006,778 | 7/16/2002 | Registered |
| PULSETRAIN | United States of America | 09; 42 | 76/085,538 | 7/5/2000 | | | Abandoned |

EXHIBIT D

Mask Works

Description

Registration/
Application
Number

Registration/
Application
Date

None.