TRADEMARK ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: RELEASE BY SECURED PARTY

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
Silicon Valley Bank		01/25/2011	CORPORATION: CALIFORNIA

RECEIVING PARTY DATA

Name:	Spectra Linear, Inc.
Street Address:	2200 Laurelwood Road
City:	Santa Clara
State/Country:	CALIFORNIA
Postal Code:	95054
Entity Type:	CORPORATION: DELAWARE

PROPERTY NUMBERS Total: 3

Property Type	Number	Word Mark
Registration Number:	3475884	EPROCLOCK
Registration Number:	3771326	READY CLOCK
Registration Number:	1694052	IMI

CORRESPONDENCE DATA

Fax Number: (650)833-2001

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 650.833.2373

Email: dash.mclean@dlapiper.com

Correspondent Name: Paul A. McLean, Esq., DLA Piper LLP (US)

Address Line 1: 2000 University Avenue

Address Line 4: East Palo Alto, CALIFORNIA 94303-2215

ATTORNEY DOCKET NUMBER:	355160-37 PAM
NAME OF SUBMITTER:	Paul A. McLean, Esq., DLA Piper LLP (US)
Signature:	/Paul A. McLean/

900183615 TRADEMARK REEL: 004471 FRAME: 0214 3475884

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Date:	02/08/2011
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RELEASE OF INTELLECTUAL PROPERTY SECURITY INTEREST

THIS RELEASE OF INTELLECTUAL PROPERTY SECURITY INTEREST (this "Release") is made as of January 25, 2011 (the "Effective Date") by and between Spectra Linear, Inc. ("Grantor") and SILICON VALLEY BANK ("Grantee").

WHEREAS, pursuant to the terms and conditions of that certain Intellectual Property Security Agreement by and between Grantor and Grantee dated May 12, 2008 (the "Intellectual Property Security Agreement"), Grantor granted to Grantee a continuing security interest in, to and under all of Grantor's right, title and interest in all of the patents and trademarks, and registrations of and applications to register the foregoing, (collectively, the "Intellectual Property") including, without limitation, the United States and foreign patent properties set forth on Schedule A attached hereto (the "Patents") and the United States and foreign trademark properties set forth on Schedule B attached hereto (the "Trademarks").

WHEREAS, Grantor has paid all of its outstanding indebtedness to Grantee.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Grantee hereby terminates the Intellectual Property Security Agreement, and hereby terminates, cancels and releases any and all security interests it has against the Intellectual Property, the Patents and the Trademarks and hereby assigns and transfers any and all interest Grantee may have in and to the Intellectual Property, the Patents and the Trademarks to the Grantor.

Grantee represents and warrants that it has the full power and authority to execute this Release;

Grantee shall at Grantor's expense, take all further actions, and provide to Grantor, its successors, assigns or other legal representatives, all such cooperation and assistance (including, without limitation, the execution and delivery of any and all documents or other instruments), reasonably requested by Grantor to more fully and effectively effectuate the purposes of this Release.

This Release shall be governed by and construed in accordance with the laws of the State of California.

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Title: <u>Relations</u>	nip Mai	nager		
SILICON VALLEY BAN		V		
STATE OF)) SS.		
COUNTY OF)	33.		
On this da to me, who acknowled and with full authority o	ged that he	, there appeared before me _ e signed the foregoing Release	as his voluntary act an	, personally known id deed on behalf
			Notary Public	

authorized representative as of the Effective Date.

IN WITNESS WHEREOF, Grantee has caused this Release to be executed by its duly

X Please See Attached CALIfornia All-purpose Acknowledgment

CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT

State of California	
County of Santa Clara	}
On 2/8/11 before me,	Kimio a I Pamma
personally appeared Liam Fair	Here Insert Name and Title of the Officer
personally appeared	Name(s) of Signer(s)
Kimie J. Gamma Commission * 1833727 Notary Public - California Santa Clara County My Comm. Expires Jan 27, 2013	who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/a)s subscribed to the within instrument and acknowledged to me that he/sige/they executed the same in his/he/(their authorized capacity(n/s), and that by his/he/(their signature)s on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument. I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.
	WITNESS my hand and official seal.
Place Notary Seal Above	Signature Signature of violage Pyblic
Though the information below is not required by law,	TIONAL it may prove valuable to persons relying on the document
·	reattachment of this form to another document.
Description of Attached Document	
Title or Type of Document:	
Document Date:	
Signer(s) Other Than Named Above:	
Capacity(ies) Claimed by Signer(s)	
Signer's Name: Individual Corporate Officer — Title(s): Partner — Limited General Attorney in Fact Trustee Guardian or Conservator Other:	☐ Individual ☐ Corporate Officer — Title(s): ☐ Partner — ☐ Limited ☐ General ☐ Attorney in Fact ☐ Attorney in Fact ☐ Corporate ☐ General ☐ Genera
Signer Is Representing:	Signer Is Representing:

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$\frac{\text{SCHEDULE A}}{\text{PATENTS}}$

Patent/Case Number	Title	Status
6,963,992	Method and Apparatus for Over-Clocking Recovery in a PLL	Issued- 11/08/05
7,330,078	Apparatus and Method for Limiting The Overshoot and Undershoot When Turning On the Spread Spectrum of a Reference Signal	Issued- 2/12/08
7,362,185	Analog Based, State Machine Controlled, Frequency Smooth Switching Method for Frequency Timing Generators	Issued 4/22/08
5,559,477	Pulse Generator Having Controlled Delay to Control Duty Cycle	lssued-09/24/97
5,646,955	Apparatus for Measuring Cycle to Cycle Jitter of a Digital Signal	Issued-07/08/97
6,130,541	Adaptive Driver with Capacitive Load Sensing and Method of Operation	lssued-10/10/00
CD06061-P	Low Voltage Single-Level Differential Fet Logic Architecture	Filed - 06/07/06
12/370,163	Programmable IO Architecture	Filed - 2/12/2009 Allowed 9/7/10
12/369,868	Download Sequencing Techniques for Circuit Configuration	Filed - 02/12/09 Allowed 9/16/10 Continuation Filed 12/17/2010
12/369,875	Presetable RAM	Filed - 02/12/09 Wait PTO Action
12/388,086	Voltage-Controlled Oscillator Topology	Filed - 02/18/09 In Prosecution
12/395,781	Power-On-Reset Circuit	Filed - 03/02/09 Allowed 11/16/10
12/395,854	Extended Range Oscillator	Filed - 03/02/09 Abandoned
12/400,999	Replica-Bias Automatic Gain Control	Filed - 03/10/09 Allowed 9/14/10
12/416,459	Rail-To-Rail Input CMOS Operational Amplifier	Filed - 04/01/09 Allowed 9/17/10
12/421,667	Programmable Signal Routing	Filed - 04/10/09 In Prosecution
12/421,682	Calibrated Download Rate	Filed - 04/10/09 Wait PTO Action
12/421,685	Power Sequence Technique	Filed - 4/10/09 Wait PTO Action
12/421,739 7,705,575	Standby Regulator	Filed - 04/10/09 Issued- 04/27/2010
12/752,864	Buffer with an Output Swing Created Using an Over-Supply Voltage	Filed - 04/01/10 Wait PTO Action

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TRADEMARK

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SCHEDULE B TRADEMARKS

Trademark	Country Name	Status	Registration No.
EProClock	United States of America	Registered	3475884
Ready Clock	United States of America	Registered	3771326
IMI	United States of America	Registered	1694052

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RECORDED: 02/08/2011