

TRADEMARK ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	SECURITY INTEREST

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
NEWPORT MEDIA, INC.		05/01/2012	CORPORATION: CALIFORNIA

RECEIVING PARTY DATA

Name:	PINNACLE VENTURES, L.L.C.
Street Address:	130 Lytton Avenue, Suite 220
City:	Palo Alto
State/Country:	CALIFORNIA
Postal Code:	94301
Entity Type:	LIMITED LIABILITY COMPANY: DELAWARE

PROPERTY NUMBERS Total: 5

Property Type	Number	Word Mark
Registration Number:	3255181	NEWPORT MEDIA
Registration Number:	3180962	N
Registration Number:	3246756	SUNDANCE SERIES
Registration Number:	3475834	NMI
Registration Number:	3408056	MOBIZAP

CORRESPONDENCE DATA

Fax Number: 6504936811
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.

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 Email: nbouch@wsgr.com
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CH \$140.00 3255181

ATTORNEY DOCKET NUMBER:	30897.057-NEWPORT
NAME OF SUBMITTER:	Nancy Bouch
Signature:	/s/Nancy Bouch
Date:	05/31/2012

Total Attachments: 14

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GRANT OF SECURITY INTEREST

IN INTELLECTUAL PROPERTY SECURITY AGREEMENT

THIS GRANT OF SECURITY INTEREST IN INTELLECTUAL PROPERTY SECURITY AGREEMENT (this "Agreement", dated as of May 1, 2012, is executed by NEWPORT MEDIA, INC., a Delaware corporation ("Parent") and NEWPORT MEDIA, INC., a California corporation ("Newport Media", and collectively with Parent, each a "Debtor" and together the "Debtors"), in favor of Pinnacle Ventures, L.L.C. as agent ("Agent" and "Secured Party") for the benefit of each of the parties to the Loan Agreement (as defined below).

A. Reference is made to that certain Loan and Security Agreement, dated as of April 7, 2011 as amended on May 1, 2012 (as further amended, restated, supplemented or otherwise modified from time to time, the "Loan Agreement"), by and between Debtors and Secured Party.

B. Debtors own the letters patent, and/or applications for letters patent, of the United States, more particularly described on Schedule A annexed hereto as part hereof (collectively, the "Patents");

C. Debtors have adopted, used and are using the trademarks, more particularly described on Schedule B annexed hereto as part hereof, which trademarks are registered or subject to an application for registration in the United States Patent and Trademark Office (collectively, the "Trademarks");

D. Schedules A and B hereof constitute a complete list, as of the date hereof, of registrations or applications for registrations of Patents and Trademarks in or to which Debtors have any right, title, interest, claim or demand. After the date of the Loan Agreement, the terms and provisions of which are hereby incorporated herein as if fully set forth herein, Debtors shall provide written notice to Secured Party, in accordance with the provisions of the Loan Agreement, of any addition or change which is necessary to be made to Schedules A and B in order to maintain such schedules completeness or accuracy, and, further, Debtors shall provide such notice to Secured Party within a reasonable period of time following the date of the event that is the basis for such addition or change.

E. Debtors hereby grant to Secured Party a security interest in all right, title and interest of Debtors in and to the Patents, together with any reissue, continuation, continuation-in-part or extension thereof, and all proceeds thereof, including any and all causes of action which may exist by reason of infringement thereof for the full term of the Patents (the "Patents"), to secure the prompt payment, performance and observance of the Obligations, as defined in the Loan Agreement;

F. Debtors hereby grant to Secured Party a security interest in all right, title and interest of Debtors in and to the Trademarks, together with the goodwill of the business symbolized by the Trademarks and the customer lists and records related to the Trademarks and the applications and registrations thereof, and all proceeds thereof, including any and all causes

of action which may exist by reason of infringement thereof (the "Trademarks"), to secure the payment, performance and observance of the Obligations, as defined in the Loan Agreement;

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, Debtors do hereby further grant to Secured Party a security interest in the Patents and Trademarks (collectively, the "Collateral") to secure the prompt payment, performance and observance of the Obligations.

Debtors do hereby further acknowledge and affirm that the rights and remedies of Secured Party with respect to the security interest in the Collateral granted hereby are more fully set forth in the Loan Agreement.

Secured Party's address is: 130 Lytton Avenue, Palo Alto, CA 94301

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IN WITNESS WHEREOF, Debtor has caused this instrument to be executed as of the day and year first above written.

NEWPORT MEDIA, INC.,
a Delaware corporation

By: Mohy F. Abdelgany
Name: Mohy F. Abdelgany
Title: Chief Executive Officer

NEWPORT MEDIA, INC.,
a California corporation

By: Mohy F. Abdelgany
Name: Mohy F. Abdelgany
Title: Chief Executive Officer

SCHEDULE A

PATENTS AND PATENT APPLICATIONS

PATENTS

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
ERROR DETECTION AND CORRECTION IN DATA TRANSMISSION PACKETS	November 4, 2008	7,447,980
FAST SWITCHING BETWEEN TIME DIVISION MULTIPLEXED (TDM) CHANNELS	May 24, 2011	7,950,042
WIDEBAND ULTRA LOW NOISE AMPLIFIER	May 27, 2008	7,378,911
LOW POWER HIGHLY LINEAR RF DOWNCONVERTER	June 9, 2009	7,546,110
SYSTEM AND METHOD FOR DIGITAL AUTONOMOUS AUTOMATIC GAIN CONTROL (AGC) FOR DVB-H RECEIVERS	March 9, 2010	7,676,207
PORTABLE MEMORY DEVICE AND METHOD	May 19, 2009	7,536,203
ROBUST TRANSMISSION SYSTEM AND METHOD FOR MOBILE TELEVISION APPLICATIONS	April 28, 2009	7,525,993
SYSTEM AND METHOD FOR TELEVISION PROGRAMMING DATA MANAGEMENT	February 15, 2011	7,889,976
SYSTEM AND METHOD FOR STATISTICAL MULTIPLEXING OF VIDEO CHANNELS FOR DVB-H MOBILE TV APPLICATIONS	August 16, 2011	8,000,395
DB-LINEAR ANALOG VARIABLE GAIN AMPLIFIER (VGA) REALIZATION SYSTEM AND	April 1, 2008	7,352,238

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
METHOD		
LOW COMPLEXITY SOFT-INPUT VITERBI DECODING FOR DIGITAL COMMUNICATION SYSTEMS	October 6, 2009	7,599,441
EFFICIENT DOPPLER COMPENSATION METHOD AND RECEIVER FOR ORTHOGONAL-FREQUENCY-DIVISION-MULTIPLEXING (OFDM) SYSTEMS	September 9, 2008	7,424,062
A RECEIVER WITH A VISUAL PROGRAM GUIDE FOR MOBILE TELEVISION APPLICATIONS AND METHOD FOR CREATION	April 27, 2010	7,707,611
LOW-POWER, LOW-JITTER, FRACTIONAL-N ALL-DIGITAL PHASE-LOCKED LOOP (PLL)	April 29, 2008	7,365,607
METHOD AND APPARATUS FOR DC OFFSET CANCELLATION IN AMPLIFIERS	March 25, 2008	7,348,839
FAST RE-SYNCHRONIZATION TECHNIQUES FOR DVB-H SYSTEMS	June 1, 2010	7,729,462
HOST PROCESSOR ASSISTED FAST RE-SYNCHRONIZATION TECHNIQUES FOR DVB-H SYSTEMS	June 1, 2010	7,729,463
DETECTING CHANNEL IMPULSE MAGNITUDE RESPONSE IN DIGITAL COMMUNICATION SYSTEMS	August 17, 2010	7,778,316
STEPPED AUTOMATIC GAIN CONTROL FOR OFDM SYSTEMS	April 13, 2010	7,697,644
UNIFIED INTERFACING FOR DVB-T/H MOBILE TV APPLICATIONS	November 15, 2011	8,059,655
INTEGRATED MPE-FEC RAM FOR DVB-H RECEIVERS	February 15, 2011	7,890,845
COMPACT MPE-FEC ERASURE	September 14, 2010	

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
LOCATION CACHE MEMORY FOR DVB-H RECEIVER		7,796,600
NOISE SHAPED NTH ORDER FILTER	April 28, 2009	7,525,372
UNIVERSAL TUNER FOR MOBILE TV	July 5, 2011	7,973,861
HIGH LINEARITY, LOW NOISE FIGURE, FRONT END CIRCUIT WITH FINE STEP GAIN	May 4, 2010	7,711,031
COHERENT DETECTION FOR DIFFERENTIALLY ENCODED OFDM SYSTEMS	May 4, 2010	7,710,857
ADAPTIVE TIME-DOMAIN INTERPOLATION FOR OFDM SCATTERED PILOT SYMBOLS	February 22, 2011	7,894,331
AN ADAPTIVE TECHNIQUE FOR INTER-CARRIER-INTERFERENCE CANCELING IN OFDM SYSTEMS	May 4, 2010	7,711,031
ROBUST TECHNIQUE FOR FRAME SYNCHRONIZATION IN DAB-IP SYSTEMS	May 10, 2011	7,940,759
NULL DETECTION IN DAB, T-DMB, AND DAB-IP SYSTEMS	June 1, 2010	7,729,435
ADAPTIVE DEINTERLEAVER MEMORY ALLOCATION	August 24, 2010	7,782,982
AN INTEGRATED CMOS DC-DC CONVERTER IMPLEMENTATION IN LOW-VOLTAGE CMOS TECHNOLOGY USING LDO REGULATOR	June 1, 2010	7,728,550
HIGH EFFICIENCY DC-DC CONVERTER USING PULSE SKIPPING MODULATION WITH PROGRAMMABLE BURST DURATION	September 22, 2009	7,592,791

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
LOW NOISE, LOW POWER, HIGH LINEARITY DIFFERENTIAL AMPLIFIER WITH A CAPACITIVE INPUT IMPEDANCE	September 22, 2009	7,592,870
SCATTERED PILOT CORRELATION IN DVB-H SYSTEMS	March 1, 2011	7,899,140
SCATTERED PILOT CORRELATION IN DVB-H SYSTEMS	May 10, 2011	7,940,867
SCATTERED PILOT CORRELATION IN DVB-H SYSTEMS	May 10, 2011	7,940,868
TIMING AND FREQUENCY ACQUISITION FOR MEDIAFLO SYSTEMS	October 26, 2010	7,821,916
RE-ACQUISITION OF SYMBOL INDEX IN THE PRESENCE OF SLEEP TIMER ERRORS FOR MEDIAFLO SYSTEMS	May 10, 2011	7,940,865
FAST ACQUISITION IN MEDIAFLO SYSTEMS	March 15, 2011	7,907,637
AREA AND POWER EFFICIENT ARCHITECTURES OF TIME DEINTERLEAVER FOR ISDB-T RECEIVERS	July 27, 2010	7,764,595
DOUBLE LAYER MAXIMUM RATIO COMBINING FOR OFDM RECEIVER DESIGN WITH INTER-CARRIER-INTERFERENCE CANCELLING	March 22, 2011	7,912,142
TIME AND FREQUENCY DOMAIN BASED APPROACHES FOR FINE TIMING AND FREQUENCY ESTIMATIONS IN ISDB-T AND ISDB-TSB RECEIVER DESIGN	February 15, 2011	7,889,803
WIC AND LIC ESTIMATION FOR MEDIAFLO SYSTEMS	July 6, 2010	7,751,309
MEMORY SHARING OF TIME AND		

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
FREQUENCY DE-INTERLEAVER FOR ISDB-T RECEIVERS	May 17, 2011	7,945,746
MEMORY ARCHITECTURE FOR HIGH THROUGHPUT RS DECODING FOR MEDIAFLO RECEIVERS	January 24, 2012	8,103,944
INTERLEAVER ADDRESS GENERATION IN TURBO DECODERS FOR MEDIAFLO COMMUNICATION SYSTEMS	January 24, 2012	8,102,791
ENHANCED STREAM LAYER TRANSMISSION FOR MEDIAFLO MOBILE MULTIMEDIA MULTICAST SYSTEM	December 21, 2010	7,855,996
RECONFIGURABLE AND ADAPTIVE CONTINUOUS TIME-SIGMA DELTA DATA CONVERTER	April 28, 2009	7,525,465
A CONTINUOUS TIME SIGMA-DELTA ANALOG-TO-DIGITAL CONVERTER WITH STABILITY	December 1, 2009	7,626,527
DIGITAL APPROACH FOR IMAGE REJECTION CORRECTION IN LOW INTERMEDIATE FREQUENCY CONVERSION TUNER	May 10, 2011	7,941,117
SYMMETRIC PILOT PROCESSING FOR ROBUST TIMING OFFSET AND FREQUENCY OFFSET ESTIMATIONS IN ISDB-T AND ISDB-TSB RECEIVERS	March 22, 2011	7,912,157
ITERATIVE DECODING FOR LAYER CODED OFDM COMMUNICATION SYSTEMS	November 22, 2011	8,065,596
COARSE CARRIER FREQUENCY OFFSET ESTIMATOR FOR CMMB MOBILE TV RECEIVER	May 17, 2011	7,944,983
ROBUST DEFRAMING OF MAC LAYER PACKETS FOR MEDIAFLO	September 28, 2010	

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
SYSTEMS		7,804,846
APPLICATION OF SUPERFAST ALGORITHMS TO A PILOT-BASED CHANNEL ESTIMATION PROCESS	March 15, 2011	7,907,683
CHANNEL ESTIMATION FOR HIGH DOPPLER MOBILE ENVIRONMENTS	May 10, 2011	7,940,864
ESTIMATING DOPPLER FREQUENCY IN ISDB-T SYSTEMS	June 28, 2011	7,970,083
ITERATIVE DECODING BETWEEN TURBO AND RS DECODERS FOR IMPROVING BIT ERROR RATE AND PACKET ERROR RATE	December 27, 2011	8,086,940
FAST COMMON OVERHEAD SERVICES ACQUISITION FOR MEDIAFLO	December 21, 2010	7,855,959
METHOD AND APPARATUS FOR LOCATING MEDIAFLO CAPABLE WIRELESS DEVICES	December 13, 2011	8,077,659
HARDWARD ACCELERATED PROTOCOL STACK	August 24, 2010	7,782,903
ROBUST FINE FREQUENCY AND TIME ESTIMATION IN MEDIA FLO SYSTEM RECEIVERS	May 17, 2011	7,944,999
LOOK-UP TABLE BASED APPROACH FOR LAYER COMBINING IN ISDB-T AND ISDB-TSB RECEIVERS	October 26, 2010	7,822,039
TIME DOMAIN BASED APPROACH FOR FAST FADING CHANNEL FFT TRIGGER POINT TRACKING IN ISDB RECEIVERS	October 11, 2011	8,036,291
COARSE FREQUENCY OFFSET ESTIMATION IN ISDB RECEIVERS	November 22, 2011	8,064,553
ROBUST FFT TRIGGER POINT		

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>
TRACKING FOR ECHO CHANNELS IN OFDM BASED COMMUNICATION SYSTEMS	December 6, 2011	8,073,064
HIGH LINEARITY VOLTAGE TO CURRENT CONVERSION	March 29, 2011	7,915,928
NOISE-SHAPED BLOCKER-REJECT AMPLIFIER	November 17, 2009	7,619,472
EFFICIENT DECODING OF MEDIAFLO SYSTEM NON REAL- TIME SERVICE FILES	August 23, 2011	8,005,990
DOPPLER FREQUENCY ESTIMATION AND ADAPTATION FOR MEDIAFLO SYSTEMS	July 5, 2011	7,974,364
FAST AUDIO/VISUAL RECEPTION IN DVB-H SYSTEMS	December 13, 2011	8,077,736
ADAPTIVE FREQUENCY DOMAIN EQUALIZATION IN OFDM BASED COMMUNICATION SYSTEM	June 28, 2011	7,970,070
SEGMENTED-FRAME SYNCHRONIZATION FOR ISDB-T AND ISBD-TSB RECEIVER	October 11, 2011	8,036,292
SUBSAMPLING WIDEBAND RSSI CIRCUIT	August 23, 2011	8,005,449
WIDEBAND RESISTIVE INPUT MIXER WITH NOISE-CANCELLED IMPEDANCE	October 18, 2011	8,041,327
COARSE FREQUENCY OFFSET ESTIMATION FOR DIGITAL MULTIMEDIA BROADCASTING	August 23, 2011	8,005,173
HIGH LINEARITY, LOW NOISE, WIDE BANDWIDTH AMPLIFIER/BUFFER	February 14, 2012	8,115,553
OPTIMIZED GAIN FILTERING AND AMPLIFICATION WITH NOISE SHAPING	September 22, 2009	7,592,863

<u>Title</u>	<u>Date Issued</u>	<u>Patent No.</u>

PATENT APPLICATIONS

<u>Title</u>	<u>Application Date</u>	<u>Application No.</u>
SYSTEM AND METHOD FOR PERFORMING RF FILTERING	September 15, 2010	12/883,179
SYSTEM AND METHOD FOR TELEVISION PROGRAMMING DATA MANAGEMENT	December 18, 2010	12/972,457
PHYSICAL LAYER AWARE VIDEO ENCODING FOR MOBILE TV APPLICATIONS	December 22, 2006	11/615,283
A USB DEVICE THAT PROVIDES WIRELESS DATA ACCESS, DIGITAL TV ACCESS, RADIO, AND STORAGE CAPABILITY	November 30, 2006	11/564,963
IMPLEMENTATION OF DIVERSITY ANTENNAS IN SMALL PORTABLE MEDIA DEVICES AND CELL PHONES	April 9, 2008	12/099,881
SWITCHABLE DVB-H RECEIVER	October 10, 2007	11/869,801
ROBUST PID FILTERING FOR DVB-H	October 26, 2007	11/924,628
AREA AND POWER EFFICIENT ARCHITECTURES OF TIME DEINTERLEAVER FOR ISDB-T RECEIVERS	May 13, 2010	12/779,630
INTERLEAVER ADDRESS GENERATION IN TURBO DECODERS FOR MEDIAFLO COMMUNICATION SYSTEMS	November 21, 2011	13/300,817
LOW COMPLEXITY HIGH		

<u>Title</u>	<u>Application Date</u>	<u>Application No.</u>
PERFORMANCE TMCC ACQUISITION IN ISDB-T AND ISDB- TSB RECEIVERS	March 12, 2008	12/046,956
TECHNIQUE FOR TUNER AUTOMATIC GAIN CONTROL CIRCUIT FOR MOBILE TELEVISION	February 19, 2008	12/033,323
DOPPLER FREQUENCY ESTIMATION IN WIRELESS COMMUNICATION SYSTEMS	April 28, 2008	12/110,602
FAST CHANNEL SWITCHING FOR ENCRYPTED MEDIAFLO MOBILE MULTIMEDIA	January 23, 2008	12/018,483
BUFFER SIZE REDUCTION FOR WIRELESS ANALOG TV RECEIVERS	April 9, 2010	12/757,378
SYSTEM AND METHOD FOR VIDEO DOWNSCALING	March 27, 2010	12/748,391
APPARATUS AND METHOD FOR ADAPTIVE FILTERING	April 13, 2010	12/759,656
APPARATUS AND METHOD FOR EQUALIZING ANALOG TV SIGNALS	April 13, 2010	12/759,665
SECAM LINE IDENTIFICATION AND PAL SWITCH	April 13, 2010	12/759,669
ALL DIGITAL FRONT-END ARCHITECTURE FOR TELEVISION WITH SIGMA-DELTA ADC INPUT	April 14, 2010	12/760,405
DUAL BURST LOCKED OSCILLATOR ARCHITECTURE FOR AN ANALOG TELEVISION RECEIVER	April 13, 2010	12/759,673
PILOT BASED ADAPTATION FOR FM RADIO RECEIVER	April 13, 2010	12/759,675
FRAME BUFFER COMPRESSION AND MEMORY ALLOCATION IN A VIDEO DECODER	April 9, 2010	12/757,438

<u>Title</u>	<u>Application Date</u>	<u>Application No.</u>
IMAGE COMPRESSION		
FIXED BUFFER REAL-TIME IMAGE COMPRESSION TECHNIQUE	April 9, 2010	12/757,474
Multi-Chip Antenna Diversity Picture-in-Picture Architecture	April 14, 2010	12/760,429
LOW POWER, MULTI-CHIP DIVERSITY ARCHITECTURE	September 30, 2010	12/894,789
CHIP AREA OPTIMIZED PADS	September 14, 2010	12/882,174
MULTI-PHASE VOLTAGE CONTROLLED OSCILLATOR USING CAPACITANCE DEGENERATED SINGLE ENDED	April 14, 2010	12/760,442
TRANSCONDUCTANCE STAGE AND INDUCTANCE/CAPACITANCE LOAD	October 19, 2011	13/276,477
HIGH VOLTAGE ELECTROSTATIC DISCHARGE CLAMP USING DEEP SUBMICRON CMOS TECHNOLOGY	February 16, 2012	13/398,638

SCHEDULE B

TRADEMARKS

TRADEMARKS

<u>Mark</u>	<u>Registration Date</u>	<u>Registration No.</u>
Trademark: NEWPORT MEDIA	June 26, 2007	3,255,181
Trademark: NEWPORT MEDIA ("N" Logo)	December 5, 2006	3,180,962
Trademark: SUNDANCE SERIES	May 29, 2007	3,246,756
Trademark: NMI (Stylized and/or with Design)	July 29, 2008	3,475,834
Trademark: MOBIZAP	April 8, 2008	3,408,056

TRADEMARK APPLICATIONS

<u>Mark</u>	<u>Application Date</u>	<u>Application No.</u>
Trademark: NMI (Stylized and/or with Design)	February 16, 2012	13/398,638

MASK WORKS

<u>Description</u>	<u>Registration Date</u>	<u>Registration No.</u>
None		