TRADEMARK ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
Silicon Valley Bank		04/26/2013	CORPORATION: CALIFORNIA

RECEIVING PARTY DATA

Name:	Magnum Semiconductor, Inc.
Street Address:	591 Yosemite Drive
City:	Milpitas
State/Country:	CALIFORNIA
Postal Code:	95035
Entity Type:	CORPORATION: DELAWARE

PROPERTY NUMBERS Total: 2

Property Type	Number	Word Mark
Registration Number:	2175498	LUXSONOR
Registration Number:	2362661	STREAM MACHINE

CORRESPONDENCE DATA

Fax Number: 6508332001

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.

Phone: 650-833-2209
Email: susan.pingue@dlapiper.com
Correspondent Name: Ben Griebe
Address Line 1:   DLA Piper LLP US
Address Line 2:   2000 University Avenue
Address Line 4:   East Palo Alto, CALIFORNIA   94303

ATTORNEY DOCKET NUMBER:	359412-1
NAME OF SUBMITTER:	Ben Griebe
Signature:	/Ben Griebe/

CH $65.00 2175498

TRADEMARK

Date:	05/10/2013

Total Attachments: 5
source=Silicon Valley Bank Security Release 04262013#page1.tif
source=Silicon Valley Bank Security Release 04262013#page2.tif
source=Silicon Valley Bank Security Release 04262013#page3.tif
source=Silicon Valley Bank Security Release 04262013#page4.tif
source=Silicon Valley Bank Security Release 04262013#page5.tif

REASSIGNMENT AND RELEASE OF SECURITY INTEREST

This Reassignment and Release of Security Interest is executed as of April 26, 2013 by SILICON VALLEY BANK ("Assignor") for the benefit of MAGNUM SEMICONDUCTOR, INC., a Delaware corporation ("Assignee").

RECITALS

A.   WHEREAS, Assignee assigned certain interests in the intellectual property described on Exhibit B and Exhibit C (together, the "IP") to the Assignor under a certain Intellectual Property Security Agreement, dated as of June 12, 2006 (the "IP Agreement") and recorded with the U.S. Patent and Trademark Office and U.S. Copyright Office on June 12, 2006; and

B.   WHEREAS, Assignor wishes to release and reassign all interest that Assignor may have in the IP as granted under the IP Agreement.

AGREEMENT

Now, therefore, Assignor agrees that it releases its security interest in the IP and reassigns to Assignee, without warranty or recourse, all interest of Assignor in the IP as granted under the IP Agreement.

ASSIGNOR:

Address:

SILICON VALLEY BANK

3003 TASMAN DRIVE
SANTA CLARA, CALIFORNIA 95054

By: _____

Title: _Director_ _____

EXHIBIT A

Patents

STATUS	PATENT NO./ APPLICATION NO.	FILING DATE/ ISSUE DATE	TITLE
Pending	10/706,014	12 November 2003	End-User Configurable Digital Versatile Disk Menus and Methods for Generating the Same
Abandoned	09/607,086	28 June 2000	Versatile Video Interface Architecture for Video Compression and Decompression
Pending	09/755,767	5 January 2001	Apparatus for Single-Chip MPEG2 Audio/Video Encoder and Decoder
Pending	09/632,472	4 August 2000	Method and Apparatus for Real-Time Audio/Video Recording on Compact Disc Media
Abandoned	09/713,427	15 November 2000	Computationally Simple Strategy for Block Switching in Audio Compression Algorithms
Abandoned	09/680,564	5 October 2000	Design of Digital Video Recorder/ Player
Issued	6,950,605	27 September 2005	Method and Apparatus for Recording Real-Time Audio/Video Information Onto Recordable Compact Disc Drives
Abandoned	09/858,839	15 May 2001	Fast Algorithms for Computing Transcendental Functions for Fixed-Point Digital Signal Processors
Pending	09/917,312	27 July 2001	Architecture for Hardware- Assisted Context Switching Between Register Group Dedicated to Time-Critical or Non-Time Critical Tasks Without Saving State
Pending	10/856,436	28 May 2004	Audio Clocking in Video Applications
Pending	10/871,287	17 June 2004	Circuits, Systems and Methods for Real-Time De-Shuffling of Shuffled Audio Data
Pending	10/828,914	21 April 2004	Dual-Mode Transform Calculation Systems and Methods
Pending	10/825,346	15 April 2004	Methods and Apparatus for Error Detection and Mitigation in Data Decoding Systems
Pending	10/853,692	25 May 2004	Methods and Apparatus for Color Component Sub-Sampling and Up-Sampling Which Preserve Color Integrity During Repeated Video Encoding-Decoding Operations

STATUS	PATENT NO./ APPLICATION NO.	FILING DATE/ ISSUE DATE	TITLE
Pending	10/926,060	25 August 2004	Method and System for Switching Between Video Modes in Consumer Electronics
Pending	10/894,340	20 July 2004	Recovery of Real Time Video Data After Power Loss
Pending	10/948,791	24 September 2004	Telecine Conversion Detection for Progressive Scan Playback
Pending	10/948,745	24 September 2004	Optimal Buffering and Scheduling Strategy for Smooth Reverse in a DVD Player or the Like
Pending	11/089,459	25 March 2005	Intelligent Caching Scheme for Streaming File Systems
Pending	11/089,458	25 March 2005	Synchronized Streaming Layer with Presentation Layer
Pending	10/980,250	3 November 2004	Sequential Decoding of Progressive Coded JPEGs
Converted	60/585,072	6 July 2004	Improvements in DVD Player/Recorder and Consumer Audio/Video Products
Pending	11/167,756	27 June 2005	Circuits and Methods for Detecting 2:2 Encoded Video and Systems Utilizing the Same
Pending	11/167,877	27 June 2005	Circuits and Methods for Deinterlacing Video Display Data and Systems Using the Same
Pending	11/167,682	27 June 2005	Systems and Methods for Detecting a Change in a Sequence of Interlaced Data Fields Generated From a Progressive Scan Source
Pending	11/175,109	05 July 2005	Technique for Minimizing Memory Bandwidth Used for Motion Compensation
		Unfiled	Motion Based Video Noise Reduction with Edge Enhancement
		Unfiled	Edge Detection for Directional Interpolation
Issued	6,400,852	4 June 2002	Arbitrary Zoom "On-the-Fly"
Issued	6,011,870	4 January 2000	Multiple Stage and Low-Complexity Motion Estimation for Interframe Video Coding
Issued	6,108,047	22 August 2000	Variable-Size Spatial and Temporal Video Scaler
Issued	6,192,075	20 February 2001	Single-Pass Variable Bit-Rate Control for Digital Video Coding
Issued	6,320,905	20 November 2001	Postprocessing System for Removing Blocking Artifacts in Block-Based Codecs

STATUS	PATENT NO./ APPLICATION NO.	FILING DATE/ ISSUE DATE	TITLE
Issued	6,584,156	24 June 2003	LSI Architecture and Implementation of MPEG Video Codec
Issued	6,754,618	22 June 2004	Fast Implementation of MPEG Audio Coding
Abandoned	09/805,402	12 March 2001	Home Media Center
Pending	11/206,402	18 August 2005	Systems and Methods for Reducing Noise During Video Deinterlacing
Pending	11/172,323	30 June 2005	Systems and Methods for Display Object Edge Detection and Pixel Data Interpolation and Video Processing Systems

Trademarks

Trademark	Status	Registration/ Application Date	Registration/ Application Number
CRISC	Unfiled		
HOLOWORKX	Unfiled		
Miscellaneous Design (Circle with WavyLines)	Unfiled		
LUXSONOR	Registered		U.S. Reg. No. 2,175,498; Class 9
STREAM MACHINE	Registered		U.S. Reg. No 2,362,661; Class 9
MAGNUM			