

TRADEMARK ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
Comerica Bank		01/30/2014	Banking Association: TEXAS

RECEIVING PARTY DATA

Name:	Bay Microsystems, Inc.
Street Address:	2055 Gateway Place, Suite 650
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95110
Entity Type:	CORPORATION: DELAWARE

PROPERTY NUMBERS Total: 8

Property Type	Number	Word Mark
Serial Number:	76322628	BAY MICROSYSTEMS
Serial Number:	76393705	MONTEGO
Serial Number:	78149854	BISCAYNE
Serial Number:	78643524	ABEX
Serial Number:	78739495	CHESAPEAKE
Serial Number:	85407624	IBEX
Serial Number:	85407681	INTELLIGENT BANDWIDTH EXCHANGE
Serial Number:	85407715	SARANA

CORRESPONDENCE DATA

Fax Number: 8586385130
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.
 Phone: 858-677-1400
 Email: susan.reynholds@dlapiper.com
 Correspondent Name: DLA Piper LLP (US)

CH \$215.00 76322628

Address Line 1: 4365 Executive Drive, Suite 1100
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ATTORNEY DOCKET NUMBER:	329034-203
NAME OF SUBMITTER:	Troy Zander
Signature:	/s/ Troy Zander
Date:	01/30/2014

Total Attachments: 4
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REASSIGNMENT AND RELEASE OF SECURITY INTEREST

This Reassignment and Release of Security Interest is executed as of January 30, 2014 by COMERICA BANK, a Texas banking corporation ("Assignor") for the benefit of BAY MICROSYSTEMS, INC., a Delaware corporation ("Assignee").

RECITALS

A. WHEREAS, Assignee assigned certain interests in the intellectual property described on Exhibit A and Exhibit B (together, the "IP") to the Assignor pursuant to certain Intellectual Property Security Agreements, dated as of November 13, 2003 and December 29, 2008, respectively, and recorded with the U.S. Patent and Trademark Office; and

B. WHEREAS, Assignor wishes to release and reassign all interest that Assignor may have in the IP.

AGREEMENT

Now, therefore, Assignor agrees that it releases its security interest in the IP and reassigns to Assignee, without warranty or recourse, all interest of Assignor in the IP.

ASSIGNOR:

Address:

39200 W. Six Mile Road
MC 7512
Livonia, MI 48152

COMERICA BANK

By:

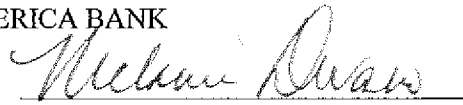

Melanie Dovano, First Vice President

EXHIBIT A

Patents

Recorded: 11/25/2003
Reel/Frame: 016482/0071

<u>Description</u>	<u>Patent/Appl. No.</u>	<u>Issue/File Date</u>
System and method for shifting the phase of a clock signal	6690223	2/10/04
Memory management system and algorithm for network processor architecture	7006505	2/28/06
Differentiated services for a network processor	7349403	3/25/08
Vertical instruction and data processing in a network processor architecture	6996117	2/7/06
Network processor architecture	7310348	12/18/07
Data forwarding engine	8010751	8/30/11

Recorded: 12/30/2008
Reel/Frame: 022043/0030

<u>Description</u>	<u>Patent/Appl. No.</u>	<u>Issue/File Date</u>
Deserializer	6556152	4/29/03
Interlocking sonet/SDH network architecture	7161899	1/9/07
Hitless re-routing in composite switches	7224706	5/29/07
Fault-tolerant mesh network comprising interlocking ring networks	09969703	10/3/01
Composite add/drop multiplexor	6973048	12/6/05
Switching network	7184662	2/27/07
Serializer	6593863	7/15/03
Forward error correction and framing protocol	6671833	12/30/03
Coherent provisioning of multiple traffic paths in transport networks	7433964	10/7/08
Simplifying the layout of printed circuit boards	6876227	4/5/05
Redundant add/drop multiplexor	7269130	9/11/07
Bi-directional serializer/deserializer with discretionary loop-back	7110424	9/19/06
Add/drop multiplexor with aggregate serializer/deserializers	7161965	1/9/07
Expansion of telecommunications networks with automatic protection switching	7173902	2/6/07
Composite add/drop multiplexor with crisscross loop back	7145922	12/5/06
Multiplexed automatic protection switching channels	7145882	12/5/06
Hitless reconfiguration of a switching network	7139291	11/21/06
Reference timing architecture	7221687	5/22/07
Overhead engine for telecommunications nodes	10193579	7/11/02
Overhead processing in telecommunications nodes	10193580	7/11/02
Multiport overhead cell processor for telecommunications nodes	7349435	3/25/08
Hierarchical finite-state machines	10194603	7/11/02
Robust mesh transport network comprising conjoined rings	8625411	1/7/14

<u>Description</u>	<u>Patent/Appl. No.</u>	<u>Issue/File Date</u>
Uniform impedance printed circuit board	10446361	5/28/03
Economically expandible switching network	7349411	3/25/08
Compact and hitlessly-resizable multi-channel queue	11103978	4/12/05
Highly-scalable hardware-based traffic management within a network processor integrated circuit	7742411	6/22/10
Integrated circuit device interface with parallel scrambler and descrambler	11935303	11/5/07
Network processor integrated circuit with a software programmable search engine communications module	11945930	11/27/07
Programmable hardware-based traffic policing	12045507	3/10/08

Recorded: 3/28/2012

Reel/Frame: 027944/0632

<u>Description</u>	<u>Patent/Appl. No.</u>	<u>Issue/File Date</u>
Network processor architecture	7742405	6/22/10
Clock regeneration for optical communications	8437641	5/7/13
Network processor architecture	12819587	6/21/10
System and method for grouping multiple processors	13219930	8/29/11

EXHIBIT B

Trademarks

Recorded: 12/1/2003
Reel/Frame: 2947/0455

<u>Description</u>	<u>Registration/ Serial No.</u>	<u>Registration/ File Date</u>
BAY MICROSYSTEMS	76322628	10/9/01
MONTEGO	76393705	4/9/02
BISCAYNE	78149854	8/1/02

Recorded: 4/7/2010
Reel/Frame: 4182/0026

<u>Description</u>	<u>Registration/ Serial No.</u>	<u>Registration/ File Date</u>
ABEX	78643524	6/3/05
CHESAPEAKE	78739495	6/3/05

Recorded: 3/28/12
Reel/Frame: 4744/0437

<u>Description</u>	<u>Registration/ Serial No.</u>	<u>Registration/ File Date</u>
IBEX	85407624	8/25/11
INTELLIGENT BANDWIDTH EXCHANGE	85407681	8/25/11
SARANA	85407715	8/25/11