

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM300765

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	ASSIGNS THE ENTIRE INTEREST AND THE GOODWILL		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
OCZ Technology Group, Inc.		01/21/2014	CORPORATION: DELAWARE
RECEIVING PARTY DATA			
Name:	TAEC ACQUISITION CORP.		
Street Address:	6373 SAN IGNACIO AVE		
City:	SAN JOSE		
State/Country:	CALIFORNIA		
Postal Code:	95119		
Entity Type:	CORPORATION: CALIFORNIA		
PROPERTY NUMBERS Total: 17			
Property Type	Number	Word Mark	
Registration Number:	4119820	SUPERSCALE	
Registration Number:	4105681	INTREPID	
Registration Number:	4099159	DENEVA	
Registration Number:	3417286	HYPERSONIC	
Registration Number:	2810218	OCZ	
Registration Number:	4099161	VELODRIVE	
Registration Number:	4099160	TALOS	
Registration Number:	4249091	INDILINX INFUSED	
Registration Number:	4249090	INDILINX INFUSED	
Registration Number:	4150238	DENEVA	
Registration Number:	4150140	INTREPID	
Registration Number:	4201127	INDILINX	
Registration Number:	4201238	INDILINX	
Registration Number:	4139254	VELODRIVE	
Registration Number:	4139249	TALOS	
Serial Number:	85457269	VERITESSE	
Serial Number:	86069161	ZD XL	
CORRESPONDENCE DATA			
Fax Number:	7147558290		

OP \$440.00 4119820

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.

Phone: 714-540-1235
Email: ipdocket@lw.com
Correspondent Name: LATHAM & WATKINS LLP
Address Line 1: 650 TOWN CENTER DRIVE, SUITE 2000
Address Line 4: COSTA MESA, CALIFORNIA 92626

ATTORNEY DOCKET NUMBER: 053821-0001

NAME OF SUBMITTER: Anna T Kwan

SIGNATURE: /atk/

DATE SIGNED: 04/08/2014

Total Attachments: 23

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INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

THIS INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT (this "Assignment") is made and entered into as of January 21, 2014, by and among OCZ Technology Group, Inc., a Delaware corporation ("Seller"), Indilinx, Inc., a California corporation, and Sanrad, Inc., a Delaware corporation (collectively, the "Assignors" and each individually referred to as an "Assignor"), and TAEC Acquisition Corp., a California corporation (the "Assignee"). The Assignors and the Assignee are collectively referred to herein as the "Parties" and individually as a "Party."

WHEREAS, Assignors and Toshiba Corporation, a Japanese corporation (the "Buyer") are parties to that certain Asset Purchase Agreement dated as of December 2, 2013 (the "Purchase Agreement"), pursuant to which the Buyer has agreed to purchase from the Selling Entities, and the Selling Entities have agreed to sell to the Buyer, substantially all of the Selling Entities' assets, and the Buyer has agreed to assume from the Selling Entities certain specified liabilities, in each case pursuant to the terms and subject to the conditions set forth in the Purchase Agreement and further subject to the Bankruptcy Court Order in the Bankruptcy Case;

WHEREAS, the Parties acknowledge and agree that OCZ Israel Ltd. is not a party to this Assignment, and therefore this Assignment does not assign, convey, transfer or deliver to the Assignee, or its successors or assigns, any of OCZ Israel Ltd.'s right, title or interest in and to any of the Seller IP; and

WHEREAS, in connection with the Closing of the transactions contemplated by the Purchase Agreement, each of the Seller and the Buyer have agreed to deliver this Agreement to the other Party.

NOW, THEREFORE, in accordance with the Purchase Agreement and in consideration of the premises and of the mutual covenants and agreements contained herein and therein, and for other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Assignors and Assignee, intending to be legally bound, hereby agree as follows:

ASSIGNMENT

Section 1. Definitions. Capitalized terms used but not defined in this Assignment shall have the meanings assigned thereto in the Purchase Agreement.

Section 2. Assignment.

(a) Upon the terms contained in the Purchase Agreement and pursuant to the terms of the Sale Order, each Assignor hereby sells, assigns, conveys, transfers and delivers to the Assignee and its successors and assigns, effective as of the Closing, all of such Assignor's right, title and interest, free and clear of all Encumbrances (other than the Permitted Encumbrances), in and to all of the Seller IP, together with any and all goodwill in connection therewith.

(b) The assignments described in this Section 2 include all claims (including claims for past infringement or misappropriation of Seller IP) and causes of action (other than, in each case, to the extent related to Excluded Assets or Excluded Liabilities) of the Assignors as of the Closing against Persons other than the Selling Entities (regardless of whether or not such claims and causes of action have been asserted by the Selling Entities) and all rights of indemnity, warranty rights, rights of contribution, rights to refunds, rights of reimbursement and other rights of recovery, including rights to insurance proceeds, possessed by the Assignors as of the Closing (regardless of whether such rights are currently exercisable) to the extent related to the Seller IP.

Section 3. Liabilities not Assumed. Assignee shall not assume or be obligated to pay, perform or otherwise discharge any of the Excluded Liabilities.

Section 4. Terms of the Purchase Agreement. Each of the Assignors and Assignee acknowledges and agrees that the representations, warranties and agreements contained in the Purchase Agreement, and any limitations thereto, shall not be superseded hereby but shall remain in full force and effect to the full extent provided therein. This Assignment is subject in all respects to the terms of the Purchase Agreement and, in the event of any conflict or inconsistency between the terms of the Purchase Agreement and the terms hereof, the terms of the Purchase Agreement shall govern.

Section 5. Succession and Assignment. This Agreement and all of the provisions hereof shall be binding upon, inure to the benefit of and be enforceable by the Parties and their respective successors and permitted assigns, including, in the case of the Assignors, any trustee in the Bankruptcy Case; provided, however, that no assignment by any Party shall relieve such Party of any of its obligations hereunder.

Section 6. Governing Law. Except to the extent the mandatory provisions of the Bankruptcy Code apply, this Assignment, and all claims and causes of action arising out of, based upon, or related to this Assignment or the negotiation, execution or performance hereof, shall be governed by, and construed, interpreted and enforced in accordance with, the Laws of the State of Delaware, U.S.A., without regard to choice or conflict of law principles that would result in the application of any Laws other than the Laws of the State of Delaware.

Section 7. Headings. The section headings contained in this Assignment are inserted for convenience only and shall not affect in any way the meaning or interpretation of this Assignment.

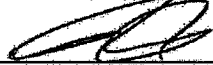
Section 8. Counterparts and Facsimile Signature. This Assignment may be executed by facsimile or pdf format and in one or more counterparts, and by the different Parties in separate counterparts, each of which when executed shall be deemed to be an original but all of which taken together shall constitute one and the same agreement, and which shall become effective when one or more counterparts have been signed by each of the Parties and delivered (by facsimile, pdf format or otherwise) to the other Parties.

(Signature page follows)


IN WITNESS WHEREOF, the parties have executed this Intellectual Property Assignment Agreement as of the date first above written.

ASSIGNORS:

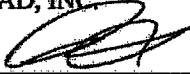
OCZ TECHNOLOGY GROUP, INC.

By: 
Name: _____
Title:

INDILINX, INC.

By: 
Name: _____
Title:

SANRAD, INC.

By: 
Name: _____
Title:

SIGNATURE PAGE TO INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

IN WITNESS WHEREOF, the parties have executed this Intellectual Property Assignment Agreement as of the date first above written.

ASSIGNEES:

TAEC ACQUISITION CORP.

By: 

Name: Julius G. Christensen

Title: President and Chief Executive Officer

SIGNATURE PAGE TO INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

TRADEMARK
REEL: 005256 FRAME: 0271

Owner	Country	Application#	Patent #	Title 1	Status	Filing Date	Issue Date	Maintenance Fee Due	Actions/Filing Due Dates
OCZ Technology Group Inc. OCZ	US	12/156185		Method for detecting and correcting errors in data stored in a memory device	Abandoned	6/15/2010			
OCZ Technology Group Inc. OCZ	US	12/815,661		Method for detecting and correcting errors in data stored in a memory device	Abandoned	6/15/2010			
OCZ Technology Group Inc. OCZ	US	12/996,564	8,488,885	Method for detecting and correcting errors in data stored in a memory device	Issued	1/7/2011	7/16/2013		
OCZ Technology Group Inc. OCZ	US	12/943,192	8,488,877	Method for detecting and correcting errors in data stored in a memory device	Issued	11/10/2010	7/16/2013		
OCZ Technology Group Inc. OCZ	US	13/058,314	8,554,986	Method for detecting and correcting errors in data stored in a memory device	Issued	9/2/2011	10/8/2013		
OCZ Technology Group Inc. OCZ	TW	I 380314		Method for detecting and correcting errors in data stored in a memory device	Issued		12/21/2012		

Alumniy assignment to OCZ Technology Group Inc in progress Alumniy assignment to OCZ Technology Group Inc in progress OCZ Technology Group Inc.	US 08/641,934	Solid state disk drive address generator with multiplier circuit	Issued	2/5/1996	7/3/2000
OCZ Technology Group Inc.	US 10/731,636	Exchange server method and system	Issued	12/8/2003	12/26/2006
OCZ Technology Group Inc.	US 10/907,420	Method for Increasing Stability of System Memory through Enhanced Quality of Supply Power	Issued		12/18/2007
OCZ Technology Group Inc.	US 11/162,029	Memory Module Having On-Package or On-Module Termination	Issued		6/2/2009
OCZ Technology Group Inc.	US 12/127,133	Method and Apparatus for Cooling Computer Memory	Issued	1/25/2011	7/25/2014

OCZ Technology Group Inc. OCZ Technology Group Inc.	US 12/872,114	Connector Assembly and Method for SATA Drives	Issued	12/27/2011
OCZ Technology Group Inc.	US 12/632,176	Methods for Modifying Memory Subsystem Performance	Issued	4/24/2012
OCZ Technology Group Inc. OCZ Technology Group Inc. OCZ Technology Group Inc. OCZ Technology Group Inc. OCZ Technology Group Inc.	US 12/179,715	Method for Optimizing Memory Modules for User- Specific Environments	Issued	7/25/2008 11/13/2012
OCZ Technology Group Inc.	US 12/859,339	Optical Memory Device and Method Therefor	Issued	8/19/2010 12/18/2012
OCZ Technology Group Inc.	US 12/793,023	Method and Apparatus for Reducing Write Cycles In NAND-Based Flash Memory Devices	Issued	6/3/2010 2/12/2013

OCZ Technology Group Inc. OCZ	US 13/027,597	8,464,106	Computer System with Backup Function and Method Therefor	Issued	2/15/2011	6/11/2013
OCZ Technology Group Inc. OCZ	US 10/891,390	7,460,672	Method for Securing Data Storage in a Storage Area Network	Issued	2/12/2008	
OCZ Technology Group Inc.	US 11/544,213	7,584,341	Method for Defragmenting of virtual volumes in a storage area network (SAN)	Issued	1/9/2009	
OCZ Technology Group Inc.	US 12/835,817		Method and Apparatus to Increase File Copy Performance on Solid State Mass Storage Devices	Allowed	7/14/2010	

OCZ Technology Group Inc.	US	12/862,176	NAND Flash-Based Storage Device With Built-In Test- Ahead for Failure Anticipation	Pending	8/24/2010
OCZ Technology Group Inc.	US	12/875,937	Storage Devices and Methods Thereof	Abandoned	9/7/2010
OCZ Technology Group Inc.	US	12/886,796	Central Processing Unit and Method for Workload Dependent Optimization Thereof	Abandoned	9/21/2010
OCZ Technology Group Inc.	US	12/890,598	Processing Method for Building Graphs Processing Data with and Non-Redundant Missing Capability	Pending	9/7/2010
OCZ Technology Group Inc.	US	12/903,260	Modular Mass Storage Devices and Methods of Using	Pending	10/13/2010
OCZ Technology Group Inc.	US	12/917,644	Method of Accessing Memory Devices Using Method for Restoring and Maintaining Solid-State Drive Performance	Pending	11/12/2010
OCZ Technology Group Inc.	US	12/945,100	Storage Systems Having Advanced Step Divided Method of Operation	Pending	11/7/2010
OCZ Technology Group Inc.	US	13/032,805	Methods and Systems Utilizing Nonvolatile Memory in a Computer System Main Memory	Abandoned	2/23/2011
OCZ Technology Group Inc.	US	13/077,889	Memory System and Method of Operating Thereof	Abandoned	2/23/2011

OCZ Technology Group Inc. OCZ	US 13/115,716	Solid State Drive with Low Write Amplification	Pending	5/25/2011	
OCZ Technology Group Inc.	US 13/115,716	Mass storage systems and methods using hardware and solid-state media	Pending	5/25/2011	
OCZ Technology Group Inc.	US 13/185,939	Solid state memory based storage device with low erase rate	Pending	7/27/2011	
OCZ Technology Group Inc. OCZ	US 13/205,300	PCIe Bus Extension System, Method and Interfaces Therefor	Pending	8/8/2011	
OCZ Technology Group Inc.	US 13/337,482	Mass storage devices and methods using solid-state memory media Methods, storage devices, and systems for promoting the endurance of non- volatile solid-state memory components	Pending	12/27/2011	Office Action response due 18 Jan 2014,
OCZ Technology Group Inc.	US 13/337,482	Non-volatile memory-based mass storage device and methods for writing data thereto	Pending	10/3/2011	
OCZ Technology Group Inc.	US 13/337,482	Non-volatile memory-based mass storage device and methods for writing data thereto	Pending	10/3/2011	
OCZ Technology Group Inc.	US 13/337,482	Non-volatile memory-based mass storage device and methods for writing data thereto	Pending	10/3/2011	

OCZ Technology Group Inc.	US	13/368,878	Solid State Memory-Based Storage Device Using Optical Input/Output Links	Pending	2/8/2012
OCZ Technology Group Inc.	US	13/675,189	Non-Volatile Solid State Memory-Based Storage Device and Method of Operation	Pending	11/15/2012
OCZ Technology Group Inc.	US	13/677,900	Solid State Mass Storage Device and Methods of Operation	Pending	11/15/2012
OCZ Technology Group Inc.	US	13/721,912	Method of Operating a Computer System Having a Non-Volatile Solid State Memory-Based Mass Storage Device and Method Thereof	Pending	7/26/2012
OCZ Technology Group Inc.	US	13/558,830	Memory-Based Mass Storage Device and Methods Thereof	Pending	7/26/2012
OCZ Technology Group Inc.	US	13/588,978	Method of Operating a Computer System Having a Non-Volatile Solid State Memory-Based Mass Storage Device and Method Thereof	Pending	7/26/2012
OCZ Technology Group Inc.	US	13/669,727	Storage/Processing Devices, Systems and Methods for Performing Big Data Analytics	Pending	11/6/2012
OCZ Technology Group Inc.	US	13/728,645	Method of Operating a Computer System Having a Graphene Based Memory Device and Methods Thereof	Pending	2/25/2013
OCZ Technology Group Inc.	US	13/775,916	Method of Operating a Computer System Having a Graphene Based Memory Device and Methods Thereof	Pending	2/25/2013
OCZ Technology Group Inc.	US	13/819,878	Method of Operating a Computer System Having a Graphene Based Memory Device and Methods Thereof	Pending	2/25/2013

OCZ Technology Group Inc. OCZ Technology Group Inc.	US 12/811,001	Flash Memory Device and Flash Memory Programming Method Equalizing Wear- Level Controller for Solid State Disks with sequential access only memory bank	Pending	11/11/2010	Office Action response due 28 Jan 2014
OCZ Technology Group Inc.	US 13/142,605	Memory Controller and Memory Management Method	Pending	6/29/2011	Final Office Action response due 18 Jan 2014. Allowable claims 1-8 with suggested claim change.
OCZ Technology Group Inc. OCZ Technology Group Inc.	US 13/201,362	Storage system using high speed storage device as cache	Pending	8/12/2011	
OCZ Technology Group Inc. OCZ Technology Group Inc.	US 13/147,403	Programming Method and Device for Buffer Control Memory Device, Memory Management Device, and Memory Management Method	Pending	8/2/2011	
OCZ Technology Group Inc.	US 13/257,458	SSD Controller, and Method for Operating an SSD Controller	Pending	9/19/2011	

OCZ Technology Group Inc.	US	13/519,724	Controller for detecting and correcting an error without buffer and method for controlling the same	Pending	6/28/2010	Office Action response due 10 Jan 2014
OCZ Technology Group Inc.	US	13/666,305	System and Method for Limiting Inrush Current in Solid State Devices	Pending	3/14/2013	Conversion to Utility by 01 Mar 2014 (as docket B3-3896)
OCZ Technology Group Inc.	US	13/771,440	Method and Apparatus for Providing Hypervisor-Level Acceleration and Virtualization Services	Pending	1/11/2012	
OCZ Technology Group Inc.	US	13/901,827	System and Method for Failure Anticipation	Pending	5/24/2013	

OCZ Technology Group Inc. OCZ	US	14/079,192	Cache Device for Hard Disk Drives and Methods of Operation	Pending	11/13/2013
OCZ Technology Group Inc. OCZ	US	13/768,998	Method of Wear Leveling in a Flash Memory Device and Apparatus	Pending	8/13/2010
OCZ Technology Group Inc. OCZ	KR	10-2008-0077997	Flash memory programming method equalizing wear-level	Pending	8/8/2008
OCZ Technology Group Inc. OCZ	KR	10-2007-0139108	Flash memory programming method equalizing wear-level	Registered	12/27/2007
OCZ Technology Group Inc. OCZ	KR	10-2007-0139108	Flash memory programming method equalizing wear-level	Registered	9/1/2008
OCZ Technology Group Inc. OCZ	KR	10-2008-0078484	Device and Method of Controlling a Flash Memory	Registered	8/11/2008
OCZ Technology Group Inc. OCZ	KR	10-2009-0023620	Apparatus and method for managing dram buffer	Registered	3/19/2009
OCZ Technology Group Inc. OCZ	KR	10-2009-0167375	Apparatus and method for managing dram buffer	Registered	2/1/2011

OCZ Technology Group Inc. OCZ	KR	10-2009-0033479	1023877	Method managing cache/disk and controller of enabling the method	Registered	4/17/2009	3/14/2011
OCZ Technology Group Inc. OCZ	KR	10-2009-0009229	1028901	Memory device, device and method for memory management	Registered	2/5/2009	4/5/2011
OCZ Technology Group Inc.	KR	10-2009-0011138	1056560	Apparatus and method for programming of buffer cache in solid state disk system	Registered	2/11/2009	8/5/2011
OCZ Technology Group Inc. OCZ	KR	10-2009-0131512	1139187	Controller for detecting and correcting an error without buffer and method of enabling the controller	Registered	12/28/2009	4/16/2012
OCZ Techno Group Inc. OCZ Techno	US	29/142,455	D456,022	COMPUTER	Issued	5/29/2001	4/23/2002

Appendix I to Schedule 5.11(e)

Indilinx	China	FPE-2010-0621/CN	200880127319.3	2010-08-16	READ ENABLE SIGNAL ADJUSTING FLASH MEMORY DEVICE AND READ CONTROL METHOD OF FLASH MEMORY DEVICE	Abandoned
Indilinx	China	FPE-2010-0625/CN	200880127721.1	2010-08-27	FLASH MEMORY DEVICE AND FLASH MEMORY PROGRAMMING METHOD EQUALIZING WEAR-LEVEL	Abandoned
Indilinx	China	FPE-2011-0214/CN	200880131512.4	2011-04-11	DEVICE AND METHOD OF CONTROLLING FLASH MEMORY	Abandoned
Indilinx	China	FPE-2011-0469/CN	200980154462.6	2011-07-12	CONTROLLER FOR SOLID STATE DISK OF CONTROLLING ACCESS TO MEMORY BANK	Abandoned
Indilinx	China	FPE-2011-0511/CN	200980153465.8	2011-06-30	MEMORY CONTROLLER AND MEMORY MANAGEMENT METHOD	Abandoned
Indilinx	China	FPE-2011-0515/CN	200980155507.1	2011-07-26	CONTROLLER FOR SOLID STATE DISK, WHICH CONTROLS SIMULTANEOUS SWITCHING-OF PADS	Abandoned
Indilinx	China	FPE-2011-0643/CN	200980156703.0	2011-08-12	STORAGE SYSTEM USING HIGH SPEED STORAGE DEVICE AS CACHE	Abandoned
Indilinx	China	FPE-2011-0646/CN	200980156623.5	2011-08-11	APPARATUS AND METHOD FOR PROGRAMMING OF BUFFER CACHE IN SOLID STATE DISK SYSTEM	Abandoned
Indilinx	China	FPE-2011-0649/CN	200980156055.9	2011-08-02	MEMORY DEVICE, MEMORY MANAGEMENT DEVICE, AND MEMORY MANAGEMENT METHOD	Abandoned
Indilinx	China	FPE-2011-0809/CN	200980158197.9	2011-09-19	APPARATUS AND METHOD FOR MANAGING DRAM BUFFER	Abandoned
Indilinx	China	FPE-2011-0815/CN	200980158216.8	2011-09-20	SSD CONTROLLER, AND METHOD FOR OPERATING AN SSD CONTROLLER	Abandoned

Appendix I to Schedule 5.11(a)

Indilinx	China	FPE-2011-0817/CN	200980158756.6	2011-10-14	CACHE AND DISK MANAGEMENT METHOD, AND A CONTROLLER USING THE METHOD	Abandoned
Indilinx	EPO	FPE-2010-0619/EP	08793469.1	2010-07-14	READ ENABLE SIGNAL ADJUSTING FLASH MEMORY DEVICE AND READ CONTROL METHOD OF FLASH MEMORY DEVICE	Abandoned
Indilinx	EPO	FPE-2010-0623/EP	08793470.9	2010-07-14	FLASH MEMORY DEVICE AND FLASH MEMORY PROGRAMMING METHOD EQUALIZING WEAR-LEVEL	Abandoned
Indilinx	EPO	FPE-2011-0215/EP	08 793 678.7	2011-03-11	DEVICE AND METHOD OF CONTROLLING FLASH MEMORY	Abandoned
Indilinx	EPO	FPE-2011-0468/EP	09 826 235.5	2011-06-13	CONTROLLER FOR SOLID STATE DISK WHICH CONTROLS ACCESS TO MEMORY BANK	Abandoned
Indilinx	EPO	FPE-2011-0509/EP	09 836 287.4	2011-07-29	MEMORY CONTROLLER AND MEMORY MANAGEMENT METHOD	Abandoned
Indilinx	EPO	FPE-2011-0513/EP	09839328.3	2011-08-25	CONTROLLER FOR SOLID STATE DISK, WHICH CONTROLS SIMULTANEOUS SWITCHING OF PADS	Abandoned
Indilinx	EPO	FPE-2011-0642/EP	09840098.9	2011-09-13	STORAGE SYSTEM USING A RAPID STORAGE DEVICE AS A CACHE	Abandoned
Indilinx	EPO	FPE-2011-0645/EP	09840104.5	2011-09-09	PROGRAMMING METHOD AND DEVICE FOR A BUFFER CACHE IN A SOLID-STATE DISK SYSTEM	Abandoned
Indilinx	EPO	FPE-2011-0648/EP	09839757.3	2011-09-05	MEMORY DEVICE, MEMORY MANAGEMENT DEVICE AND MEMORY MANAGEMENT METHOD	Abandoned
Indilinx	EPO	FPE-2011-0810/EP	09 841 960.9	2011-10-19	APPARATUS AND METHOD FOR MANAGING A DRAM BUFFER	Abandoned
Indilinx	EPO	FPE-2011-0814/EP	09 841 957.5	2011-10-20	SSD CONTROLLER, AND METHOD FOR OPERATING AN SSD CONTROLLER	Abandoned

Appendix I to Schedule 5.11(a)

Indilinx	Japan	FPE-2010-0620/JP	2010-540549	2010-06-28	READ ENABLE SIGNAL ADJUSTING FLASH MEMORY DEVICE AND READ CONTROL METHOD OF FLASH MEMORY DEVICE	Abandoned
Indilinx	Japan	FPE-2010-0624/JP	2010-540550	2010-06-28	FLASH MEMORY DEVICE AND FLASH MEMORY PROGRAMMING METHOD EQUALIZING WEAR-LEVEL	Abandoned
Indilinx	Japan	FPE-2011-0213/JP	2011-522887	2011-02-10	DEVICE AND METHOD OF CONTROLLING FLASH MEMORY	Abandoned
Indilinx	Japan	FPE-2011-0470/JP	2011-536235	2011-05-12	CONTROLLER FOR SOLID STATE DISK OF CONTROLLING ACCESS TO MEMORY BANK	Abandoned
Indilinx	Japan	FPE-2011-0510/JP	2011-544355	2011-06-29	MEMORY CONTROLLER AND MEMORY MANAGEMENT METHOD	Abandoned
Indilinx	Japan	FPE-2011-0514/JP	2011-547756	2011-07-29	SOLID STATE DISKS CONTROLLER OF CONTROLLING SIMULTANEOUSLY SWITCHING OF PADS	Abandoned
Indilinx	Japan	FPE-2011-0656/JP	2011-550046	2011-08-10	STORAGE SYSTEM USING HIGH SPEED STORAGE DEVICE AS CACHE	Abandoned
Indilinx	Japan	FPE-2011-0657/JP	2011-550047	2011-08-10	APPARATUS AND METHOD FOR PROGRAMMING OF BUFFER CACHE IN SOLID STATE DISK SYSTEM	Abandoned
Indilinx	Japan	FPE-2011-0658/JP	2011-547758	2011-07-29	MEMORY DEVICE, DEVICE AND METHOD FOR MEMORY MANAGEMENT	Abandoned
Indilinx	Japan	FPE-2011-0808/JP	2012-500706	2011-09-16	APPARATUS AND METHOD FOR MANAGING DRAM BUFFER	Abandoned
Indilinx	Japan	FPE-2011-0812/JP	2012-500705	2011-09-20	SSD CONTROLLER, AND METHOD FOR OPERATING AN SSD CONTROLLER	Abandoned
Indilinx	Japan	FPE-2011-0816/JP	2012-505798	2011-10-14	METHOD MANAGING CACHE/DISK AND CONTROLLER OF ENABLING THE METHOD	Abandoned


Appendix 1 to Schedule 5.11(e)

Indilinx	PCT	FPE-2011-1464/PCT	PCT/KR2011/010307	2011-12-29	ADAPTIVE DIGITAL PHY FOR HIGH SPEED EXTERNAL MEMORY INTERFACE	Abandoned
Indilinx	PCT	FPE-2012-0146/PCT	PCT/KR2012/000254	2012-01-11	STORAGE DEVICE OF ADAPTIVELY DETERMINING PROCESSING SCHEME WITH RESPECT TO REQUEST OF HOST BASED ON PARTITION INFORMATION AND OPERATING METHOD THEREOF	Abandoned
Indilinx	PCT	FPE-2012-0271/PCT	PCT/KR2012/001709	2012-03-08	STORAGE SYSTEM FOR SUPPORTING COPY AND MOVE COMMAND AND OPERATING METHOD THEREOF	Abandoned

Appendix II to Section 5.11(a)

OCZ Technology Group, Inc.

United States Trademarks

Mark	Registration No.	Registration Date	Renewal due
SUPERSCALE	4,119,820	3/27/2012	3/27/2018
INTREPID	4,105,681	2/28/2012	2/28/2018
DENEVA	4,099,159	2/14/2012	2/14/2018
HYPERSONIC	3,417,286	4/29/2008	04/29/2018
OCZ	2,810,218	2/3/2004	02/03/2024
VELODRIVE	4,099,161	2/14/2012	02/14/2018
TALOS	4,099,160	2/14/2012	02/14/2018
	4249091	11/27/2012	11/27/2018
INDILINX INFUSED	4249090	11/27/2012	11/27/2018
DENEVA	4150238	5/29/2012	05/29/2018
INTREPID	4150140	5/29/2012	05/29/2018
INDILINX	4201127	9/4/2012	09/04/2018

Mark	Registration No.	Registration Date	Renewal due
INDILINX	4201238	9/4/2012	09/04/2018
VeloDrive	4139254	5/08/2012	05/08/2018
TALOS	4139249	5/8/2012	05/08/2018

United States Trademark Applications

Mark	Application No.	Filing Date	Status
VERITESSE	85/457,269	10/26/2011	Notice of Allowance due April 17 2014
DATAWARD	85/346,413	6/15/2011	Abandoned 8/27/2012
DATASCRIBE	85/346,409	6/15/2011	Abandoned 8/27/2012
AROWANA	85/067040	8/25/2011	Abandoned 3/27/2012
Z-DRIVE	85/406740	8/25/2012	Abandoned 11/2/2012
ZD-XL	86/069,161	09/19/2013	Pending

International Trademarks and Applications

Attorney	Country	Mark	Registration No.	Filing Date	Registration Date	Renewal due
DLA Piper	Australia	HYPERSONIC	949448	12/31/2007	12/31/2007	12/31/2017
DLA Piper	EU	HYPERSONIC	949448	12/31/2007	12/31/2007	12/31/2017
DLA Piper	Intl Madrid Protocol Only	HYPERSONIC	949448	12/31/2007	12/31/2007	12/31/2017
DLA Piper	Turkey	OCZ	Appl. 2013/16777	02/22/2013		

Korean Registered Trademarks

Country	Application#	Trademark #	Docket #	Status	Filing Date	Issue Date
KR	41-2008-0023252	0189214	ATM-2008-0926	Registered	28/08/2008	25/08/2009
KR	41-2008-0023253	0189215	ATM-2008-0927	Registered	28/08/2008	25/08/2009
KR	41-2008-0023255	0189216	ATM-2008-0929	Registered	28/08/2008	25/08/2009
KR	41-2008-0023256	0189217	ATM-2008-0930	Registered	28/08/2008	25/08/2009
KR	41-2008-0023257	0189218	ATM-2008-0932	Registered	28/08/2008	25/08/2009
KR	41-2008-0023258	0189219	ATM-2008-0933	Registered	28/08/2008	25/08/2009
KR	40-2008-0042130	0801630	ATM-2008-0925	Registered	28/08/2008	22/09/2009
KR	40-2008-0042131	0801631	ATM-2008-0928	Registered	28/08/2008	22/09/2009

KR	40-2008-0042132	0801632	ATM-2008-0931	Registered	28/08/2008	22/09/2009
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