

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM302585

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	ASSIGNS THE ENTIRE INTEREST AND THE GOODWILL		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
Midgard Acquisition LLC	FORMERLY N/A	04/21/2014	LIMITED LIABILITY COMPANY: DELAWARE
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	Xilinx, Inc.		
<b>Street Address:</b>	2100 Logic Drive		
<b>City:</b>	San Jose		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	95124		
<b>Entity Type:</b>	CORPORATION: DELAWARE		
<b>PROPERTY NUMBERS Total: 5</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Registration Number:</b>	2756309		
<b>Registration Number:</b>	2756308	MODELWARE	
<b>Registration Number:</b>	2201205	MODELWARE	
<b>Registration Number:</b>	2821169	PLURIBUS	
<b>Registration Number:</b>	2834348	STANDARDS TO SILICON	
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>	4158362501		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>			
<b>Phone:</b>	415-836-2557		
<b>Email:</b>	tmdocket@dlapiper.com		
<b>Correspondent Name:</b>	Heather A. Dunn, DLA Piper LLP (US)		
<b>Address Line 1:</b>	555 Mission Street, Suite 2400		
<b>Address Line 4:</b>	San Francisco, CALIFORNIA 94105		
<b>NAME OF SUBMITTER:</b>	Heather A. Dunn, Attorney of Record		
<b>SIGNATURE:</b>	/Heather Dunn/		
<b>DATE SIGNED:</b>	04/24/2014		
<b>Total Attachments: 5</b>			
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## TRADEMARK ASSIGNMENT AGREEMENT

This **TRADEMARK ASSIGNMENT AGREEMENT** (the "Assignment") is made and entered by and between Midgard Acquisition LLC, a Delaware limited liability company (the "Assignor") and Xilinx, Inc., a Delaware corporation (the "Assignee").

**WHEREAS**, Assignee is the sole member of Midgard Acquisition LLC; and

**WHEREAS**, Assignor desires to assign and transfer its rights, interests, and claims in, and title to its trademarks and service marks, and related trademark and service mark applications and registrations, all as set forth in Schedule A attached hereto (the "Trademarks"), together with any and all common law rights and goodwill associated therewith to Assignee, and Assignee desires to acquire, receive, and accept from Assignor all of its rights, interests, and claims in and title to the Trademarks.

**NOW, THEREFORE**, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby contributes, conveys, grants, sets over, assigns and transfers to Assignee and its successors and assigns, and Assignee hereby accepts, Assignor's entire worldwide right, interest and claim in, and title to the Trademarks, together with the common law rights and goodwill associated therewith, together with the right to sue for and collect upon all claims for profits and damages as a result of past infringement of its Trademarks, if any, in each case whether now existing or hereafter created, together with the proceeds thereof (collectively, the "Intellectual Property").

Assignor hereby constitutes and appoints Assignee as Assignor's true and lawful attorney in fact, with full power of substitution in Assignor's name and stead, to take any and all steps, including proceedings at law, in equity or otherwise, to execute, acknowledge, deliver, and file any and all instruments, documents and assurances necessary or expedient in order to vest or perfect the aforesaid rights and related causes of action more effectively in Assignee or to protect the same or to enforce any claim or right of any kind with respect thereto to further the purposes of this Assignment with the same legal force and effect as if executed by Assignor. This includes, but is not limited to any rights with respect to the Intellectual Property that may have accrued in Assignor's favor from the respective date of first use or registration of any of the Intellectual Property through the Effective Date of this Assignment. Assignor hereby declares that the foregoing power is coupled with an interest and as such is irrevocable. Assignor further agrees that it shall on the Effective Date hereof and from time to time thereafter, perform or cause to be performed such acts and execute, acknowledge and deliver at the request of Assignee, such documents as may reasonably be required to evidence or effectuate the conveyance, assignment, transfer and delivery to Assignee of the Intellectual Property or for the performance by Assignor of any of its obligations hereunder.

Each party represents that it has the power and authority to enter into this Assignment. If any term of this Assignment is held invalid or unenforceable for any reason, the remainder of such term shall be amended to achieve as closely as possible the effect of the original term, and all other terms shall continue in full force and effect. This Assignment shall be governed by the laws of the State of California, without regard to its conflicts of law principles.

IN WITNESS WHEREOF, the Parties hereto have caused this Assignment to be duly executed on their respective behalf, by their respective officers thereunto duly authorized, and this

Assignment shall be effective as of the day and year on which it has been fully executed by the Parties. This Assignment may be executed in any number of counterparts and each executed counterpart shall have the same force and effect as an original instrument.

"Assignor"

Midgard Acquisition LLC

By: \_\_\_\_\_

Name: Scott Hover-Smoot

Title: Corporate Vice President, General Counsel and Secretary, Xilinx, Inc., sole member of Midgard Acquisition LLC

Date: 4/21/2014

"Assignee"

Xilinx, Inc.

By: \_\_\_\_\_

Name: Catia Hagopian

Title: Vice President

Date: 4/18/14

SCHEDULE A

[ATTACHED]

Trademark

Application Number	Filing Date
Registration Number	Registration Date
	None

MISC. DESIGN (STAR LOGO)

78/174200	14-Oct-2002
2756309	26-Aug-2003

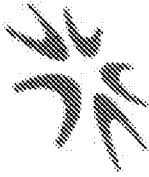
Country: United States of America

Owner: Midguard Acquisition LLC

Class and Goods: Class: 09 Int.

Computer software, namely, virtual components or cores which are circuits that implement telecommunications standards, written using hardware description programming languages, used in the manufacture of integrated circuits and computer chips, or downloadable into programmable computer chips called Field Programmable Gate Arrays (FPGAs).

Image/Links:



MODELWARE

78/174196	14-Oct-2002
2756308	26-Aug-2003

Country: United States of America

Owner: Midguard Acquisition LLC

Class and Goods: Class: 09 Int.

Computer software, namely, virtual components or cores which are circuits that implement telecommunications standards, written using hardware description programming languages, used in the manufacture of integrated circuits and computer chips, or downloadable into programmable computer chips called Field Programmable Gate Arrays (FPGAs).

MODELWARE

75/327856	21-Jul-1997
2201205	03-Nov-1998

Country: United States of America

Owner: Midguard Acquisition LLC

Class and Goods: Class: 42 Int.

New product design services in the field of telecommunications and consulting in conjunction therewith.

Trademark Status by ClientName/TrademarkName/Country Name

Trademark

Application Number	Filing Date
Registration Number	Registration Date

PLURIBUS

78174267	14-Oct-2002
2821169	09-Mar-2004

Country: United States of America

Owner: Midguard Acquisition LLC

Class and Goods: Class: 09 Int.

Computer software, namely, virtual components or cores which are circuits that implement telecommunications standards, written using hardware description programming languages, used in the manufacture of integrated circuits and computer chips, or downloadable into programmable computer chips called Field Programmable Gate Arrays (FPGAs).

STANDARDS TO SILICON

78174198	14-Oct-2002
2834348	20-Apr-2004

Country: United States of America

Owner: Midguard Acquisition LLC

Class and Goods: Class: 09 Int.

Computer software, namely, virtual components or cores which are circuits that implement telecommunications standards, written using hardware description programming languages, used in the manufacture of integrated circuits and computer chips, or downloadable into programmable computer chips called Field Programmable Gate Arrays (FPGAs).