

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM305241

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
Sidense Corp.		05/15/2014	a corporation existing under the laws of Ontario: CANADA
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	COMERICA BANK		
<b>Street Address:</b>	39200 Six Mile Road		
<b>Internal Address:</b>	National Documentation Services, Mail Code 7578		
<b>City:</b>	Livonia		
<b>State/Country:</b>	MICHIGAN		
<b>Postal Code:</b>	48152		
<b>Entity Type:</b>	a Texas banking association and authorized foreign bank under the Bank Act (Canada): TEXAS		
<b>PROPERTY NUMBERS Total: 1</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Registration Number:</b>	3558052	THE FUTURE OF LOGIC NVM	
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>	7349302494		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
<b>Phone:</b>	734-930-0121		
<b>Email:</b>	asujek@bodmanlaw.com		
<b>Correspondent Name:</b>	Angela Alvarez Sujek - Bodman PLC		
<b>Address Line 1:</b>	201 South Division, Suite 400		
<b>Address Line 4:</b>	Ann Arbor, MICHIGAN 48104		
<b>NAME OF SUBMITTER:</b>	Angela Alvarez Sujek		
<b>SIGNATURE:</b>	/Angela Alvarez Sujek/		
<b>DATE SIGNED:</b>	05/21/2014		
<b>Total Attachments: 6</b>			
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**INTELLECTUAL PROPERTY SECURITY AGREEMENT  
(Borrower)**

This Intellectual Property Security Agreement is entered into as of ~~April~~ <sup>May</sup> 15, 2014, by and between **COMERICA BANK** ("Bank") and **SIDENSE CORP.**, a corporation existing under the laws of Ontario ("Grantor").

**RECITALS**

A. Bank has agreed to make certain advances of money and to extend certain financial the "Loans") to Grantor in the amounts and manner set forth in that certain Loan Agreement between Bank and Grantor dated as of even date herewith (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"). Capitalized terms used herein without definition are used as defined in the Loan Agreement.

B. Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in its Intellectual Property, as defined in that certain General Security Agreement executed by Grantor and dated as of even date herewith (as the same may be amended, modified or supplemented from time to time, the "Security Agreement") to secure the Obligations.

C. Pursuant to the terms of the Security Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under the Intellectual Property.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of the Obligations, Grantor hereby represents, warrants, covenants and agrees as follows:

**AGREEMENT**

To secure its obligations under the Loan Agreement and under any other agreement now existing or hereafter arising between Bank and Grantor, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including without limitation those Copyrights, Patents and Trademarks listed on Exhibits A, B and C hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

Grantor represents and warrants that Exhibits A, B, and C attached hereto, as such Exhibits may be amended, restated, replaced or otherwise modified from time to time, set forth any and all intellectual property rights

in connection to which Grantor has registered or filed an application with either the United States Patent and Trademark Office or the United States Copyright Office, as applicable.


Grantor acknowledges and agrees that Bank may, in its sole discretion, amend this Agreement, without first obtaining Grantor's approval of or signature to such amendment, by amending Exhibits A, B and C hereto to include reference to any right, title or interest in any Copyrights, Patents or Trademarks acquired by Grantor after the date hereof or to delete any reference to any right, title or interest in any Copyrights, Patents or Trademarks in which Grantor no longer has or claims to have any right, title or interest.

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

GRANTOR:

Address of Grantor:  
c/o Sidense Corp.  
84 Hines Road, Suite 200  
Ottawa, Ontario K2K 3G3  
Attn:  
Fax:

SIDENSE CORP.

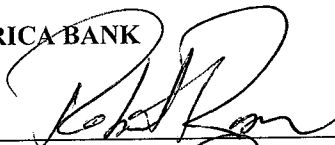
By:   
Title: STEVE MAYNES, CFO

Address of Bank:

Comerica Bank  
National Documentation Services  
39200 Six Mile Rd.  
Mail Code 7578  
Livonia, MI 48152

BANK:

COMERICA BANK

By:   
Title: SENIOR VICE PRESIDENT  
of REGIONAL MARKETING  
DIRECTOR

**EXHIBIT A**

**Copyrights**

None.

**EXHIBIT B****Patents**

Title	App. No.	Filing Date	Reg. No.	Date Granted
Test cells for an unprogrammed OTP memory array	13/291520	11/8/11	8,526,254	9/3/13
AND-type one time programmable memory cell	12/609605	10/30/09	8,471,355	6/25/13
Redundancy system for non-volatile memory	12/843498	7/26/10	8,369,166	2/5/13
Anti-fuse memory cell	13/219215	8/26/11	8,313,987	11/20/12
Method for operating a register stage of a dual function data register	13/229189	9/9/11	8,266,483	9/11/12
Low power antifuse sensing scheme with improved reliability	12/713991	2/26/10	8,223,526	7/17/12
High speed OTP sensing scheme	12/822332	6/24/10	8,130,532	3/6/12
Anti-fuse memory cell	12/814124	6/11/10	8,026,574	9/27/11
High speed OTP sensing scheme	12/389933	2/20/09	7,764,532	7/27/10
Anti-fuse memory cell	11/762552	6/13/07	7,755,162	7/13/10
Split-channel antifuse array architecture	11/877229	10/23/07	7,642,138	1/5/10
High speed OTP sensing scheme	11/618330	12/29/06	7,511,982	3/31/09
Split-channel antifuse array architecture	10/553873	5/6/05	7,402,855	7/22/08
High reliability OTP memory	12/701140	2/5/10	8,213,211	7/3/12
Program verify method for OTP memories	12/442274	12/20/07	8,082,476	12/20/11
Power up detection system for a memory device	12/306940	12/20/07	7,940,595	5/10/11
Dual function data register	12/306808	12/20/07	8,023,338	9/20/11
Mask programmable anti-fuse architecture	12/306114	12/20/07	7,944,727	5/17/11
Low VT antifuse device	12/266828	11/7/08	n/a	n/a

Title	App. No.	Filing Date	Reg. No.	Date Granted
Test circuit for an unprogrammed OTP memory array	12/342367	12/23/08	8,059,479	11/15/11
Program lock circuit for a mask programmable anti-fuse memory array	12/306260	12/20/07	7,817,456	10/19/10
Split-channel antifuse array architecture	12/139992	6/16/08	8,283,751	10/9/12
Circuit and method for reducing write disturb in a non-volatile memory device	13/896712	5/17/13	n/a	n/a
Power up detection system for a memory device	13/894824	5/15/13	n/a	n/a
Redundancy system for non-volatile memory	13/740747	1/14/13	8,654,598	2/18/14
Reverse optical proximity correction method	13/662842	10/29/12	n/a	n/a
OTP memory cell having low current leakage	13/504295	10/29/10	n/a	n/a
Methods for testing unprogrammed OTP memory	13/412500	3/5/12	n/a	n/a

**EXHIBIT C**

**Trademarks**

Mark	App. No.	Filing Date	Reg. No.	Reg. Date
THE FUTURE OF LOGIC NVM	77/052340	11/28/06	3,558,052	1/6/09