

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM305615

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	RELEASE OF SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
Silicon Valley Bank		05/23/2014	bank: CALIFORNIA
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	Egenera, Inc.		
<b>Street Address:</b>	80 Central St.		
<b>City:</b>	Boxborough		
<b>State/Country:</b>	MASSACHUSETTS		
<b>Postal Code:</b>	01719		
<b>Entity Type:</b>	CORPORATION: DELAWARE		
<b>PROPERTY NUMBERS Total: 4</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Registration Number:</b>	2831978	EGENERA	
<b>Registration Number:</b>	3465735	PAN MANAGER	
<b>Registration Number:</b>	2748042	BLADEFRAME	
<b>Registration Number:</b>	2722188		
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>	3128637867		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
<b>Phone:</b>	312-863-7267		
<b>Email:</b>	jaclyn.digrande@goldbergkohn.com		
<b>Correspondent Name:</b>	Jaclyn Di Grande - Paralegal		
<b>Address Line 1:</b>	Goldberg Kohn Ltd.		
<b>Address Line 2:</b>	55 E Monroe St., Ste 3300		
<b>Address Line 4:</b>	Chicago, ILLINOIS 60603		
<b>ATTORNEY DOCKET NUMBER:</b>	6913.042		
<b>NAME OF SUBMITTER:</b>	Jaclyn Di Grande		
<b>SIGNATURE:</b>	/jaclyn di grande/		
<b>DATE SIGNED:</b>	05/27/2014		
<b>Total Attachments: 4</b>			
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**RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY**

THIS RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY ("Release") is made as of this 23<sup>rd</sup> day of May, 2014, by SILICON VALLEY BANK, a California bank ("Bank"), in favor of EGENERA, INC., a Delaware corporation ("Company").

FOR GOOD AND VALUABLE CONSIDERATION, the receipt and adequacy of which are hereby acknowledged, the Bank hereby unconditionally and expressly releases, terminates, and extinguishes its liens on and security interests in the patents, trademarks, and copyrights listed on Schedule A attached hereto and made a part hereof, which liens and security interests were established under and pursuant to an Intellectual Property Security Agreement dated December 29, 2008 executed by Company in favor of Bank recorded with the United States Patent and Trademark Office (patents at Reel/Frame 022102/0963; trademarks at Reel/Frame 3923/0424 and Reel/Frame 4791/0825).

Bank consents and agrees to execute and deliver, at the request and cost of Company, such further instruments, documents and release forms as Company may reasonably request to more effectively, release, terminate and extinguish any such liens and security interests upon such patents, trademarks and copyrights.

IN WITNESS HEREOF, Bank has caused this Release to be duly executed by  
its duly authorized officer as of the day and year first above written.

SILICON VALLEY BANK

By: 

Name: TYLER DITRICH

Its: VICE PRESIDENT

## SCHEDULE A

### PATENTS

<b>PATENT</b>	<b>PATENT/ APPLICATION NUMBER</b>	<b>ISSUE DATE/FILE DATE</b>
Service Clusters and Method in a Processing System with Failover Capability	6,971,044	11/29/05
Simplified Power and Data Connector for Use with Chassis System that Houses Multiple Processors	6,927,974	8/9/05
Latching Mechanism for Securing a Computer Component into a Housing	6,953,232	10/11/05
System and Method for Virtualizing Basic Input/Output System (BIOS) Including Bios Run Time Services	7,032,108	4/18/06
Reconfigurable, Virtual Processing System, Cluster, Network and Method	7,231,430	6/12/07
Address Resolution Protocol System and Method in a Virtual Network	7,174,390	2/6/07
Disaster Recover for Processing Resources Using Configurable Deployment Platform	7,178,059	2/13/07
System and Method for Emulating Serial Port Communication	7,228,265	6/5/07
Disaster Recovery for Processing Resources Using Configurable Deployment Platform	7,296,182	11/13/07
Service Clusters and Method in a Processing System with Failover Capability	7,305,581	11/14/07
Distributed Multicast System and Method in a Network	10/999,118	11/29/04
Providing Virtual Machine Technology as an Embedded Layer within a Processing Platform	11/513,877	8/31/06
System and Method for Emulating Serial Port Communication	11/799,294	5/1/07
Reconfigurable, Virtual Processing System, Cluster, Network and Method	11/759,076	6/6/07
Reconfigurable, Virtual Processing System, Cluster, Network and Method	11/759,077	6/6/07
Reconfigurable, Virtual Processing System, Cluster, Network and Method	11/759,078	6/6/07
A System, Method, and Adapter for Creating Fault-Tolerant Communication Busses from Standard Components	12/112,836	4/30/08
Method For Determining which of Two Redundant Servers Has Survived a Failure	12/126,547	5/23/08
System for Virtualizing NVRAM Settings	12/190,930	8/13/08

TRADEMARKS

MARK	REGIS. NUMBER	REGIS. DATE
EGERERA	2831978	4/13/04
PAIN MANAGER	3465735	7/15/08
BLADEFRAME	2748042	8/5/03
[design only]	2722188	6/3//03

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None