

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM371519

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
AZUL SYSTEMS, INC.		12/03/2015	CORPORATION: DELAWARE
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	SILICON VALLEY BANK		
<b>Street Address:</b>	3003 TASMAN DRIVE		
<b>Internal Address:</b>	SUITE 100		
<b>City:</b>	SANTA CLARA		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	95054		
<b>Entity Type:</b>	CORPORATION: CALIFORNIA		
<b>PROPERTY NUMBERS Total: 3</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Registration Number:</b>	4784650	READYNOW!	
<b>Registration Number:</b>	4554961	ZULU	
<b>Registration Number:</b>	4077727	ZING	
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>	4048853900		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
<b>Phone:</b>	4048853868		
<b>Email:</b>	rusty.close@troutmansanders.com		
<b>Correspondent Name:</b>	CHRISTOPHER CLOSE		
<b>Address Line 1:</b>	TROUTMAN SANDERS LLP		
<b>Address Line 2:</b>	600 PEACHTREE STREET NE, SUITE 5200		
<b>Address Line 4:</b>	ATLANTA, GEORGIA 30308-2216		
<b>ATTORNEY DOCKET NUMBER:</b>	220763.000905		
<b>NAME OF SUBMITTER:</b>	Christopher Close		
<b>SIGNATURE:</b>	/Christopher Close/		
<b>DATE SIGNED:</b>	02/02/2016		
<b>Total Attachments: 8</b>			

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**ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT**

THIS ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT (this “**Agreement**”) is executed pursuant to, and is an addendum to, that certain Intellectual Property Security Agreement dated as of November 18, 2009, by and between SILICON VALLEY BANK, a California corporation (“**Assignee**”), and AZUL SYSTEMS, INC., a Delaware corporation (“**Assignor**”). This Agreement is presented for recordation as constructive notice that Assignor, the owner of the intellectual property identified in the exhibits attached hereto, has granted to Assignee, a security interest in the intellectual property, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Assignor has executed this Addendum to Intellectual Property Security Agreement as of December 3, 2015.

AZUL SYSTEMS, INC.

By:  \_\_\_\_\_

Name: Scott Sellers

Title: President & CEO

EXHIBIT A

Copyrights

Description	Registration Number	Application Number
None Identified		

EXHIBIT B

Patents

Description	Application Number	Registration Number
Concurrent Atomic Execution		7,865,701 (01/04/2011)
System and Method for Allocating Resources of a Core Space Among a Plurality of Core Virtual Machines		7,620,953 (11/17/2009)
Garbage Collection		7,647,458 (01/12/2010)
Resource Management		7,669,202 (02/23/2010)
Processor Instruction Used to Determine Whether to Perform a Memory-Related Trap		7,689,782 (03/30/2010)
Information Redirection		7,742,398 (06/22/2010)
Dynamic Concurrent Atomic Execution		7,836,280 (11/16/2010)

Description	Application Number	Registration Number
Transparent Concurrent Atomic Execution		7,840,785 (11/23/2010)
Detecting Software Race Conditions		7,844,862 (11/30/2010)
Accelerated Class Check		7,987,473 (07/26/2011)
Accelerated Class Check		8,037,482 (10/11/2011)
External Data Source Redirection in Segmented Virtual Machine		8,356,297 (01/15/2013)
Garbage Collection with Memory Quick Release		8,452,938 (05/28/2013)
Cooperative Preemption		8,544,020 (09/24/2013)
Source Switching of Virtual Machines		8,572,605 (10/29/2013)

Description	Application Number	Registration Number
Multiple Cluster Processor		8,990,501 (03/24/2015)
Concurrent Atomic Execution		8,949,583 (02/03/2015)
Accelerated Class Check		8,843,944 (09/23/2014)
Accelerated Class Check		8,839,274 (09/16/2014)
Garbage Collection with Memory Quick Release		8,725,982 (05/13/2014)
Resource Allocation		8,589,920 (11/19/2013)
Detecting Software Race Conditions		8,230,271 (07/24/2012)
Processor Instruction Used to Perform a Matrix Test to Generate a Memory-Related Trap		8,108,628 (01/31/2012)

Description	Application Number	Registration Number
Garbage Collection Barrier with Direct User Mode Traps		8,046,544 (10/25/2011)
Transparent Concurrent Atomic Execution		8,041,926 (10/18/2011)
Cooperative Preemption	13/963,879 (08/09/2013)	
CPU Utilization Metering on Systems that Include Multiple Hardware Threads per Core	14/461,172 (08/15/2014)	
CPU Utilization Metering on Systems that Include Multiple Hardware Threads per Core	PCT US2007012447 (05/24/2007)	



EXHIBIT C

Trademarks

Description	Serial Number	Registration Number
READYNOW!		4,784,650 (08/04/2015)
ZULU		4,554,961 (06/24/2014)
ZING		4,077,727 (12/27/2011)

EXHIBIT D

Mask Works

Description	Application	Registration
None Identified		

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