TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM422465

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
Silicon Valley Bank		04/04/2017	Corporation: CALIFORNIA

RECEIVING PARTY DATA

Name:	Magnum Semiconductor, Inc.
Street Address:	130 Baytech Drive
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134
Entity Type:	Corporation: DELAWARE

PROPERTY NUMBERS Total: 2

Property Type	Number	Word Mark
Registration Number:	3808970	MAGNUM SEMICONDUCTOR
Registration Number:	3805344	MAGNUM SEMICONDUCTOR

CORRESPONDENCE DATA

Fax Number: 80091442

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 614-280-3568
Email: John.Salvage@wolterskluwer.com
Correspondent Name: CT Corporation System
Address Line 1:   4400 Easton Commons Way
Address Line 2:   Suite 125
Address Line 4:   Columbus, OHIO 43219

NAME OF SUBMITTER:	Elaine Carrera
SIGNATURE:	/Elaine Carrera/
DATE SIGNED:	04/04/2017

Total Attachments: 6
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Form **PTO-1594** (Rev. 6-12)
OMB Collection 0651-0027 (exp. 04/30/2018)

U.S. DEPARTMENT OF COMMERCE
United States Patent and Trademark Office

RECORDATION FORM COVER SHEET
TRADEMARKS ONLY

To the Director of the U. S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies):

Silicon Valley Bank

- [ ] Individual(s)
- [ ] Association
- [ ] Partnership
- [ ] Limited Partnership
- [X] Corporation- State: CA
- [ ] Other

Citizenship (see guidelines) USA

Additional names of conveying parties attached? [ ] Yes [X] No

3. Nature of conveyance/Execution Date(s) :

Execution Date(s) April 4, 2017

- [ ] Assignment
- [ ] Merger
- [ ] Security Agreement
- [ ] Change of Name
- [X] Other Release of Security Interes

2. Name and address of receiving party(ies)

Additional names, addresses, or citizenship attached? [ ] Yes [X] No

Name: Magnum Semiconductor, Inc.

Street Address: 130 Baytech Drive

City: San Jose

State: CA

Country: USA   Zip: 95134

- [ ] Individual(s)   Citizenship
- [ ] Association   Citizenship
- [ ] Partnership   Citizenship
- [ ] Limited Partnership   Citizenship
- [X] Corporation   Citizenship USA-DE
- [ ] Other_____   Citizenship

If assignee is not domiciled in the United States, a domestic representative designation is attached: [ ] Yes [ ] No
(Designations must be a separate document from assignment)

4. Application number(s) or registration number(s) and identification or description of the Trademark.

A. Trademark Application No.(s)   Text

See Schedule I

B. Trademark Registration No.(s)

See Schedule I

Additional sheet(s) attached? [X] Yes [ ] No

C. Identification or Description of Trademark(s) (and Filing Date if Application or Registration Number is unknown):

5. Name & address of party to whom correspondence concerning document should be mailed:
Name: Elaine Carrera, Legal Assistant

Internal Address:

Street Address: c/o Cahill Gordon & Reindel LLP
80 Pine Street

City: New York

State: NY   Zip: 10005

Phone Number: (212) 701-3365

Docket Number:

Email Address: ecarrera@cahill.com

6. Total number of applications and registrations involved: 2

7. Total fee (37 CFR 2.6(b)(6) & 3.41) $_____

- [ ] Authorized to be charged to deposit account
- [ ] Enclosed

8. Payment Information:

Deposit Account Number _____

Authorized User Name _____

9. Signature: *Elaine Carrera*
Signature

Elaine Carrera
Name of Person Signing

April 4, 2017
Date

Total number of pages including cover sheet, attachments, and document: 6

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Branch, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450

TRADEMARK
REEL: 006027 FRAME: 0118

RELEASE OF SECURITY INTEREST IN
INTELLECTUAL PROPERTY

This RELEASE, dated as of April 4, 2017, is made by Silicon Valley Bank (the "Secured Party"), pursuant to that certain Amended and Restated Security Agreement dated as of April 5, 2016 (the "Security Agreement") and that certain Third Amended and Restated Loan and Security Agreement dated as of April 5, 2016 (the "Amended Security Agreement").

W I T N E S S E T H

WHEREAS, Magnum Semiconductor, Inc., a Delaware corporation, (the "Grantor") is party to the Security Agreement;

WHEREAS, pursuant to the Security Agreement, the Grantor executed that certain Intellectual Property Security Agreement dated as of April 5, 2016 and recorded with the U.S. Patent and Trademark Office on April 6, 2016 at Reel/Frame No. 038366/0098 with respect to the patents and patent applications assigned, and at Reel/Frame No. 5766/0697 with respect to the trademarks and trademark applications assigned (the "Intellectual Property Security Agreement"), pursuant to which the Grantor granted to the Secured Party a security interest in all of its right, title and interest in and to, among other intellectual property, the intellectual property set forth in Schedule I attached hereto (the "Patent and Trademark Collateral");
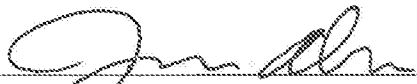
NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Secured Party agrees as follows:

The Secured Party hereby TERMINATES, without recourse, representation or warranty the Intellectual Property Security Agreement, and RELEASES, without recourse, representation or warranty its security interest in the Patent and Trademark Collateral, including the patents, trademarks and patent applications set forth in Schedule I.

[SIGNATURE PAGE FOLLOWS]

SFACTIVE-904400957.2

IN WITNESS WHEREOF, the Secured Party has caused this Release to be duly executed and delivered as of the date first written above.

Silicon Valley Bank, as Secured Party

By: _____

Name: _Janice Ahn_____

Title: _Vice President_____

SCHEDULE I
to
RELEASE OF SECURITY INTEREST IN PATENT AND TRADEMARK COLLATERAL

Magnum Semiconductor, Inc. - U.S. Patents

PATENT NO.	ISSUE DATE	TITLE
7,134,039	2006-11-07	Recovery of real-time video data after power loss
7,974,523	2011-07-05	Optimal buffering and scheduling strategy for smooth reverse in a DVD player or the like
7,391,468	2008-06-24	Telecine conversion detection for progressive scan playback
7,469,067	2008-12-23	Sequential decoding of progressive coded JPEGs
7,574,580	2009-08-11	Intelligent caching scheme for streaming file systems
7,813,621	2010-10-12	Synchronized streaming layer with presentation layer
7,864,858	2011-01-04	Techniques for minimizing memory bandwidth used for motion compensation
7,613,615	2009-11-03	Circuits, systems, and methods for real-time de-shuffling of shuffled audio data
7,587,131	2009-09-08	Audio clocking in video applications
7,538,824	2009-05-26	Systems and methods for reducing noise during video deinterlacing
7,414,671	2008-08-19	Systems and methods for display object edge detection and pixel data interpolation in video processing systems
6,950,605	2005-09-27	Method and apparatus for recording real-time audio/video information onto recordable compact disc drives
7,246,220	2007-07-17	Architecture for hardware-assisted context switching between register groups dedicated to time-critical or non-time critical tasks without saving state
7,646,968	2010-01-12	End-user configurable digital versatile disk menus and methods for generating the same
7,894,681	2011-02-22	Sequential decoding of progressive coded JPEGS
9,118,425	2015-08-25	Transport stream multiplexers and methods for providing packets on a transport stream
9,154,782	2015-10-06	Apparatuses and methods for adjusting coefficients using dead zones
7,450,184	2008-11-11	Circuits and methods for detecting 2:2 encoded video and systems utilizing the same
7,522,214	2009-04-21	Circuits and methods for deinterlacing video display data and systems using the same
7,420,626	2008-09-02	Systems and methods for detecting a change in a sequence of interlaced data fields generated from a progressive scan source
6,011,870	2000-01-04	Multiple stage and low-complexity motion estimation for interframe video coding
6,108,047	2000-08-22	Variable-size spatial and temporal video scaler
6,192,075	2001-02-20	Single-pass variable bit-rate control for digital video coding
6,320,905	2001-11-20	Postprocessing system for removing blocking artifacts in block-based codecs
6,584,156	2003-06-24	LSI Architecture and implementation of MPEG video codec
6,754,618	2004-06-22	Fast implementation of MPEG audio coding
6,400,852	2002-06-04	Arbitrary zoom "on -the -fly"
8,487,797	2013-07-16	Audio clocking in video applications
8,378,867	2013-02-19	Audio clocking in video applications
9,258,517	2016-02-09	Methods and apparatuses for adaptively filtering video signals

| 9,277,254 | 2016-03-01 | Transport stream multiplexers and methods for providing packets on a transport stream |

Magnum Semiconductor, Inc. - U.S. Patent Applications

APPLICATION NO.	FILING DATE	TITLE
13/743,091	2013-01-16	Methods and apparatuses for providing an adaptive reduced resolution update mode
13/434,736	2012-03-29	Apparatuses and methods for providing quantized coefficients for video encoding
13/454,669	2012-04-24	Apparatuses and methods for bitstream bitstuffing
13/467,624	2012-05-09	Apparatuses and methods for estimating bitstream bit counts
13/660,803	2012-10-25	Rate-distortion optimizers and optimization techniques including joint optimization of multiple color components
13/627,776	2012-09-26	Apparatuses and methods for optimizing rate-distortion of syntax elements
13/800,980	2013-03-13	Video synchronization techniques using projection
13/760,871	2013-02-06	Apparatuses and methods for performing joint rate-distortion optimization of prediction mode
13/800,804	2013-03-13	Method and apparatus for perceptual macroblock quantization parameter decision to improve subjective visual quality of a video signal
13/836,746	2013-03-15	Apparatuses and methods for providing quantized coefficients for video encoding
14/011,503	2013-08-27	Apparatuses and methods for cabac initialization
13/889,778	2013-05-08	Systems, apparatuses, and methods for transcoding a bitstream
13/851,737	2013-03-27	Apparatuses and methods for staggered-field intra-refresh
13/856,995	2013-04-04	Apparatuses and methods for pooling multiple channels into a multi-program transport stream
13/886,047	2013-05-02	Methods and apparatuses including a statistical multiplexer with global rate control
14/071,341	2013-11-04	Methods and apparatuses for multi-pass adaptive quantization
14/011,535	2013-08-27	Methods and apparatuses for adjusting macroblock quantization parameters to improve visual quality for lossy video encoding
14/161,930	2014-01-23	Methods and apparatuses for content-adaptive quantization parameter modulation to improve video quality in lossy video coding
13/889,028	2013-05-07	Methods and apparatuses including a statistical multiplexer with bitrate smoothing
14/037,148	2013-09-25	Apparatuses and methods for reducing rate and distortion costs during encoding by modulating a lagrangian parameter
14/109,539	2013-12-17	Apparatuses and methods for providing optimized quantization weight matrices
14/326,211	2014-07-08	Methods and apparatuses for stripe-based temporal and spatial video processing
14/721,967	2015-05-26	Apparatuses and methods for providing data consistency messaging for shared memory systems
13/937,733	2013-07-09	Apparatuses and methods for adjusting a quantization parameter to improve subjective quality
14/025,522	2013-09-12	Methods and apparatuses including an encoding system with temporally adaptive quantization
14/854,359	2015-09-15	Apparatuses and methods for adjusting coefficients using dead zones
14/201,492	2014-03-07	Apparatuses and methods for performing video quantization rate distortion

14/133,261	2013-12-18	Apparatuses and methods for optimizing rate-distortion costs in video encoding
14/309,034	2014-06-19	Apparatuses and methods for parameter selection during rate-distortion optimization
14/592,539	2015-01-08	Apparatuses and methods for efficient random noise encoding
14/263,535	2014-04-28	Methods and apparatuses including a statistical multiplexer with multiple channel rate control
14/705,658	2015-05-06	Method and apparatus for parallel decoding
14/495,583	2014-09-24	Apparatuses and Methods for Filtering Noise from a Video Signal
14/316,329	2014-06-26	Methods and apparatuses for edge preserving and/or edge enhancing spatial filter
14/746,146	2015-06-22	Methods and apparatuses for filtering of ringing artifacts post decoding
14/747,879	2015-06-23	Apparatuses and methods for performing information extraction and insertion on bitstreams
14/717,931	2015-05-20	Method for time dependent visual quality encoding for broadcast services

Magnum Semiconductor, Inc. – U.S. Trademarks

REGISTRATION NO.	DATE	TRADEMARK
3808970	2010-06-29	MAGNUM SEMICONDUCTOR
3805344	2010-06-22	MAGNUM SEMICONDUCTOR & DESIGN