

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM426899

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	ASSIGNMENT OF THE ENTIRE INTEREST AND THE GOODWILL		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
Elantec Semiconductor LLC		03/29/2013	Limited Liability Company: DELAWARE
RECEIVING PARTY DATA			
Name:	Intersil Americas LLC		
Street Address:	1001 Murphy Ranch Road		
City:	Milpitas		
State/Country:	CALIFORNIA		
Postal Code:	95053		
Entity Type:	Limited Liability Company: DELAWARE		
PROPERTY NUMBERS Total: 1			
Property Type	Number	Word Mark	
Registration Number:	2566377	ELANTEC	
CORRESPONDENCE DATA			
Fax Number:	3128324700		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	312-832-4552		
Email:	delder@foley.com,jolsen@foley.com		
Correspondent Name:	Diane G. Elder		
Address Line 1:	321 North Clark Street, Suite 2800		
Address Line 4:	Chicago, ILLINOIS 60654		
NAME OF SUBMITTER:	Diane G. Elder		
SIGNATURE:	/Diane Grace Elder;/dge/61590/		
DATE SIGNED:	05/09/2017		
Total Attachments: 13			
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INTELLECTUAL PROPERTY ASSIGNMENT

This Intellectual Property Assignment ('Assignment') is made between:

Elantec Semiconductor LLC (formerly known as Elantec Semiconductor, Inc.), a limited liability company organized and existing under the laws of the State of Delaware, having a principal place of business at 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignor," and

Intersil Americas LLC (formerly known as Intersil Americas Inc.), a limited liability company organized and existing under the laws of the State of Delaware, having a principal business address of 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignee."

DEFINITIONS

"Intellectual Property Rights" means all intellectual property, industrial property or other proprietary rights that may exist or be created under the laws of any jurisdiction throughout the world including all of the following, whether registered or unregistered, all applications and registrations therefor (whether pending, existing, abandoned or expired), and any physical embodiments thereof: (i) inventions or discoveries, whether or not patentable, reduced to practice or made the subject of one or more pending patent applications, and whether or not under design or development, invention disclosures, improvements, confidential and proprietary information, know-how and technology, (ii) U.S. and foreign patents, patent applications, patent disclosures, utility and industrial models or other rights relating to the protection of inventions worldwide and all rights related thereto, including all original applications, provisional applications, divisional applications, reissues, re-examinations, extensions, continuations, continuations-in-part, continuing applications, or renewals thereof, all counterparts claiming priority therefrom, (iii) trademarks, service marks, certification marks, trade dress (including packaging and package designs, product inserts, labels or associated artwork), logos, slogans, domain names, internet addresses, uniform resource locators, keywords and purchased search terms, identifying symbols, designs, product names, business and company names, trade names, corporate names, insignia and general intangibles of a similar nature (whether registered or not registered) in the United States and all other nations throughout the world, including all variations, derivations, combinations, registrations and applications for registration or renewals of the foregoing and all goodwill associated therewith, (iv) copyrights in both published and unpublished works (whether or not registered) and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, including all works, derivative works, moral rights, renewals, extensions, reversions or restorations associated with such copyrights, now or hereafter provided by law, regardless of the medium of fixation or means of expression, and any other rights of authorship in any other published and unpublished works, including all moral rights in any of the foregoing (v) mask works and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, (vi) computer software (including source code, object code, firmware, operating systems and, development tools, files, records, specifications and all media on which any of the foregoing is

recorded), (vii) information that derives economic value from not being generally known to other Persons and all information that is proprietary or confidential to Assignor, including all trade secrets and, whether or not confidential, business information (including pricing and cost information, business and marketing plans and customer and supplier lists) and technology and know-how (including manufacturing and production processes and techniques and, research and development information, patterns, drawings, blueprints, bills of materials, specifications, products in development, processes, applications, and circuits), (viii) industrial designs (whether or not registered), (ix) databases and data collections, and all rights therein throughout the world, and (x) copies and tangible embodiments of any of the foregoing, in whatever form or medium (including electronic media).

ASSIGNMENT

Assignor is the owner of all right, title, and interest in and to Intellectual Property Rights and Assignee is desirous of acquiring any and all such interest in and to Assignor's said Intellectual Property Rights. Assignor and Assignee are sister companies directly or indirectly owned by the same corporate entity.

NOW, THEREFORE, be it known, that for good and valuable consideration, the receipt of which is hereby acknowledged by Assignor, Assignor's entire right, title and interest in and to said Intellectual Property Rights, including without limitation:

- a. the patents specified in Exhibit A attached hereto and made part of this Assignment;
- b. the patent applications specified in Exhibit B attached hereto and made part of this Assignment;
- c. the registered trademarks specified in Exhibit C attached hereto and made part of this Assignment;
- d. the registered mask works specified in Exhibit D attached hereto and made part of this Assignment;
- e. the registered copyrights specified in Exhibit E attached hereto and made part of this Assignment;
- f. the domain names specified in Exhibit F attached hereto and made part of this Assignment;
- g. the right to file applications for patents, trademarks, or registrations of service marks, trade dress, copyrights, and mask works;
- h. the right to file reissue, re-examination, continuation, or divisional applications;
- i. the right to file extensions;
- j. the right to file statements of use;
- k. the right to sue (i) for, recover and collect damages and costs and attorneys' fees for past, present and future, and (b) for injunctive relief, for infringement or misappropriation; and
- l. the right to license, and collect royalties, license fees, and all other forms of payment on account of such Intellectual Property Rights

is hereby assigned, conveyed and transferred to Assignee.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, trademark registrations, copyright registrations, mask work registrations, or other governmental grants or issuances that may be granted upon any of the Intellectual Property Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Intellectual Property Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

Assignee hereby accepts all right, title and interest granted to it herein.

Assignor agrees that Assignor will make, execute and deliver any and all other instruments in writing including any and all further application papers, affidavits, assignments and other documents, and will communicate to the Assignee, its successors and representatives all facts known to Assignor relating to the Intellectual Property Rights and generally do all things which may be necessary or desirable more effectually to secure to and vest in the Assignee, its successors or assigns the entire right, title and interest in and to the Intellectual Property Rights.

This Agreement contains the complete and entire agreement between the Parties related to the subject matter of this Agreement, and supersedes any previous communications, representations, or agreements, whether verbal or written.

The provisions of this Agreement shall be deemed separable. Therefore, if any part of this Agreement is rendered void, invalid or unenforceable, such rendering shall not affect the validity or enforceability of the remainder of this Agreement.

{END OF CLAUSES}

IN WITNESS whereof the parties hereto have executed this Assignment.

Elantec Semiconductor LLC (Assignor)

By: Douglas A. Balog

Name: Douglas A. Balog

Title: Assistant Corporate Secretary

Date: March 29, 2013

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State of Florida)
) SS
County of Brevard)

On March 29, 2013 before me, Patricia M. Wiles, Notary Public, personally appeared Douglas A. Balog, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his capacity as Assistant Corporate Secretary of Elantec Semiconductor LLC, and has proven to me on the basis of satisfactory evidence that he/she had and has the full authority to execute documents on behalf of Elantec Semiconductor LLC and without any countersignature by any other individual, and that by his signature on the instrument the entity, upon behalf of which the person acted, executed the instrument.

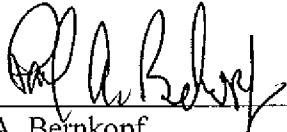
WITNESS my hand and official seal.



Patricia M. Wiles

Notary Public

Intersil Americas LLC (Assignee)

By: 
Name: Paul A. Bernkopf
Title: Vice President and Asst. Secretary

Date: March 29, 2013



State of Florida)
) SS
County of Brevard)

On March 29, 2013 before me, Patricia M. Wiles, Notary Public, personally appeared Paul A. Bernkopf, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his capacity as Vice President and Asst. Secretary of Intersil Americas LLC, and has proven to me on the basis of satisfactory evidence that he/she had and has the full authority to execute documents on behalf of Intersil Americas LLC and without any countersignature by any other individual, and that by his signature on the instrument the entity, upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.



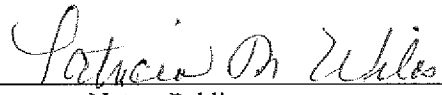

Notary Public

Exhibit A
Patents

ClientRef	InvTitle	Country	IssDate	Pat #
SE-1817-EL	CURRENT MIRROR WITH ERROR CORRECTION	US	14-Jun-1994	5321371
SE-1818-EL	CIRCUIT FOR INTRODUCING HYSTERISIS	US	02-Aug-1994	5334883
SE-1820-EL	ANALOG MULTIPLEXER	US	04-Oct-1994	5352987
SE-1821-EL	LOW INPUT RESISTANCE AMPLIFIER STAGE	US	04-Oct-1994	5352989
SE-1822-EL	LOW INPUT RESISTANCE CURRENT-MODE FEEDBACK OPERATIONAL AMPLIFIER INPUT STAGE	US	27-Sep-1994	5351012
SE-1823-EL	COMPLEMENTARY GAIN CONTROL CIRCUIT	US	20-Dec-1994	5374898
SE-1825-EL	INPUT STAGE IMPROVEMENTS FOR CURRENT FEEDBACK AMPLIFIERS	US	23-May-1995	5418495
SE-1826-EL	DIFFERENTIAL AMPLIFIER MULTIPLEXER	US	20-Jun-1995	5426396
SE-1827-EL	VARACTOR COMPENSATION IN AMPLIFIER CIRCUITS	US	30-May-1995	5420542
SE-1828-EL	DIFFERENTIAL ANALOG MEMORY CELL AND METHOD FOR ADJUSTING SAME	US	04-Jul-1995	5430670
SE-1829-EL	ACTIVE FOLDED CASCODE	US	21-Nov-1995	5469104
SE-1830-EL	VOLTAGE CONTROLLED AMPLIFIER WHICH REDUCES DIFFERENTIAL GAIN	US	21-Nov-1995	5469106
SE-1831-EL	ULTRA HIGH GAIN AMPLIFIER	US	26-Dec-1995	5479133
SE-1832-EL	CLASS AB COMPLEMENTARY OUTPUT STAGE	US	12-Dec-1995	5475343
SE-1833-EL	SYNCHRONIZING SIGNAL ACTIVE FILTER AND METHOD	US	18-Jun-1996	5528303
SE-1834-EL	AMPLIFIER CIRCUIT WITH DYNAMIC OUTPUT STAGE BIASING	US	22-Oct-1996	5568090
SE-1835-EL	INPUT STAGE IMPROVEMENT FOR CURRENT FEEDBACK AMPLIFIERS	US	31-Dec-1996	5589798
SE-1836-EL	CIRCUIT COMPENSATING FOR CAPACITANCE AT INVERTING INPUT OF CURRENT FEEDBACK AMPLIFIER	US	26-Nov-1996	5578967
SE-1837-EL	ULTRA HIGH GAIN AMPLIFIER	US	25-Mar-1997	5614866
SE-1838-EL	SEMICONDUCTOR WAFER HAVING SLICES AND LIMITED SCRIBE AREAS FOR IMPLEMENTING DIE	US	12-Aug-1997	5656851
SE-1839-EL	HIGH PRECISION POLISHING TOOL	US	28-Oct-1997	5681216
SE-1840-EL	MONOLITHIC POWER CONVERTER WITH A POWER SWITCH AS A CURRENT SENSING ELEMENT	US	03-Mar-1998	5723974
SE-1841-EL	A POWER CONTROL CHIP WITH CIRCUITRY THAT ISOLATES SWITCHING ELEMENTS AND BOND WIRES FOR TESTING	US	11-Aug-1998	5793126
SE-1842-EL	VIDEO SYNC SIGNAL SLICING USING VARIABLE GAIN CONTROL	US	02-Feb-1999	5867222
SE-1843-EL	GAS AGITATED LIQUID ETCHER	US	08-Dec-1998	5846374
SE-1844-EL	DIFFERENTIAL AMPLIFIER WITH IMPROVED VOLTAGE GAIN	US	22-Sep-1998	5812026
SE-1845-EL	CONSTANT AMPLITUDE VS. FREQUENCY OSCILLATOR	US	23-Feb-1999	5874865
SE-1846-EL	REDUCED OUTPUT CAPACITANCE CIRCUIT FOR DRIVING A GROUNDED LOAD IN RESPONSE TO A STEPPED INPUT	US	27-Apr-1999	5898334
SE-1847-EL	SILICON ON INSULATOR PROCESS WITH RECOVERY OF A DEVICE LAYER FROM AN ETCH STOP LAYER	US	16-Nov-1999	5985728
SE-1848-EL	DIFFERENTIAL AMPLIFIER WITH IMPROVED VOLTAGE GAIN USING OPERATIONAL AMPLIFIERS TO ELIMINATE DIODE VOLTAGE DROPS	US	29-Feb-2000	6031424
SE-1849-EL	METHOD FOR MAKING A CONTACT STRUCTURE FOR A POLYSILICON FILLED TRENCH ISOLATION	US	14-Mar-2000	6037239
SE-1850-EL	POLYSILICON FILLED TRENCH ISOLATION STRUCTURE FOR SOI INTEGRATED CIRCUITS	US	01-Aug-2000	6096621
SE-1851-EL	METHOD OF FORMING RETROGRADE WELLS IN BONDED WAFERS	US	31-Oct-2000	6140205

ClientRef	InvTitle	Country	IssDate	Pat #
SE-1853-EL	LOAD CURRENT CONTROL CIRCUITRY FOR POWER SUPPLIES DRIVING A COMMON LOAD FOR PROVIDING A UNIFORM TEMPERATURE DISTRIBUTION	US	20-Mar-2001	6204720
SE-1854-EL	HIGH PERFORMANCE INTERMEDIATE STAGE CIRCUIT FOR A RAIL-TO-RAIL INPUT/OUTPUT CMOS OPERATIONAL AMPLIFIER	US	09-Oct-2001	6300834
SE-1854-EL	HIGH PERFORMANCE INTERMEDIATE STAGE CIRCUIT FOR A RAIL-TO-RAIL INPUT/OUTPUT CMOS OPERATIONAL AMPLIFIER	US	07-May-2002	6384683
SE-1855-EL	REDUCED SUBSTRATE CAPACITANCE HIGH PERFORMANCE SOI PROCESS	US	11-Jun-2002	6403447
SE-1855-EL	REDUCED SUBSTRATE CAPACITANCE HIGH PERFORMANCE SOI PROCESS	US	09-Sep-2003	6617646
SE-1856-EL	AN ALTERNATIVE VIDEO SYNC DETECTOR	US	03-Jun-2003	6573943
SE-1856-EL	AN ALTERNATIVE VIDEO SYNC DETECTOR	US	20-Dec-2005	6977692
SE-1856-EL	AN ALTERNATIVE VIDEO SYNC DETECTOR	US	08-May-2007	7215379
SE-1856-EL	AN ALTERNATIVE VIDEO SYNC DETECTOR	US	24-Aug-2010	7782397
SE-1857-EL	A HIGH SPEED, LOW-POWER CMOS CIRCUIT WITH CONSTANT OUTPUT SWING AND VARIABLE TIME DELAY FOR A VOLTAGE CONTROLLED OSCILLATOR	US	31-Dec-2002	6501317
SE-1857-EL	A HIGH SPEED, LOW-POWER CMOS CIRCUIT WITH CONSTANT OUTPUT SWING AND VARIABLE TIME DELAY FOR A VOLTAGE CONTROLLED OSCILLATOR	CN	31-Aug-2005	ZL02809718.1
SE-1858-EL	DISTORTION IMPROVEMENT IN AMPLIFIERS	US	10-Sep-2002	6448853
SE-1859-EL	CURRENT FEEDBACK AMPLIFIER EXHIBITING REDUCED DISTORTION	US	18-Mar-2003	6535064
SE-1860-EL	COMPENSATION METHOD IN A CLASS B AMPLIFIER	US	25-Mar-2003	6538514
SE-1861-EL	SIMPLIFIED MULTI-OUTPUT DIGITAL TO ANALOG CONVERTER (DAC) FOR A FLAT PANEL DISPLAY	US	24-Aug-2004	6781532
SE-1861-EL	SIMPLIFIED MULTI-OUTPUT DIGITAL TO ANALOG CONVERTER (DAC) FOR A FLAT PANEL DISPLAY	US	09-Aug-2005	6927712
SE-1861-EL	SIMPLIFIED MULTI-OUTPUT DIGITAL TO ANALOG CONVERTER (DAC) FOR A FLAT PANEL DISPLAY	JP	14-Aug-2009	4359504
SE-1861-EL	SIMPLIFIED MULTI-OUTPUT DIGITAL TO ANALOG CONVERTER (DAC) FOR A FLAT PANEL DISPLAY	TW	01-Dec-2006	I267818
SE-1862-EL	ANALOG DEMULTIPLEXING	US	24-May-2005	6897800
SE-1862-EL	ANALOG DEMULTIPLEXING	US	21-Feb-2006	7002498
SE-1862-EL	ANALOG DEMULTIPLEXING	JP	18-Apr-2008	4111915
SE-1862-EL	ANALOG DEMULTIPLEXING	KR	27-Jul-2009	10-0910481
SE-1862-EL	ANALOG DEMULTIPLEXING	TW	01-Jan-2004	NI-193308
SE-1863-EL	A CMOS RELAXATION OSCILLATOR CIRCUIT WITH IMPROVED SPEED AND REDUCED PROCESS/TEMPERATURE VARIATIONS	US	13-Apr-2004	6720836
SE-1864-EL	CLASS G AMPLIFIERS	US	03-Jan-2006	6982600
SE-1865-EL	COMMON- MODE AND DIFFERENTIAL-MODE COMPENSATION FOR OPERATIONAL AMPLIFIER CIRCUITS	US	15-Mar-2005	6867649
SE-1946-EL	SYSTEM AND METHODS FOR AUTOMATIC POWER CONTROL OF LASER DIODES	US	13-Jun-2006	7061951
SE-1946-EL	SYSTEM AND METHODS FOR AUTOMATIC POWER CONTROL OF LASER DIODES	US	03-Jun-2008	7382807
SE-1956-EL	METHODS AND SYSTEMS FOR DRIVING DISPLAYS INCLUDING CAPACITIVE DISPLAY ELEMENTS	US	25-Apr-2006	7034781
SE-1964-EL	LASER DRIVERS THAT PROVIDE DOUBLE BUFFERING OF SERIAL TRANSFERS	US	17-Jul-2007	7246199
SE-1964-EL	LASER DRIVERS THAT PROVIDE DOUBLE BUFFERING OF SERIAL TRANSFERS	US	01-Apr-2008	7353333

ClientRef	InvTitle	Country	IssDate	Pat #
SE-1964-EL	LASER DRIVERS THAT PROVIDE DOUBLE BUFFERING OF SERIAL TRANSFERS	US	14-Jul-2009	7562187
SE-1965-EL	METHODS FOR EXTRACTING THE MEAN (COMMON MODE) VOLTAGE OF TWO INPUT SIGNALS	US	15-Mar-2005	6867643
SE-1965-EL	METHODS FOR EXTRACTING THE MEAN (COMMON MODE) VOLTAGE OF TWO INPUT SIGNALS	US	30-May-2006	7053698
SE-1977-EL	PROGRAMMABLE DAMPING FOR LASER DRIVERS	TW	01-Jun-2012	1365580
SE-1978-EL	ADJUSTABLE POWER CONTROL FOR LASER DRIVERS AND LASER DIODES	US	12-Sep-2006	7106769
SE-1978-EL	ADJUSTABLE POWER CONTROL FOR LASER DRIVERS AND LASER DIODES	TW	01-Aug-2011	1346430
SE-2105-EL	NOVEL NOISE CANCELLATION CIRCUIT AND METHODS	US	14-Feb-2006	6998905
SE-2105-EL	NOVEL NOISE CANCELLATION CIRCUIT AND METHODS	US	11-Jul-2006	7075362
SE-2105-EL	NOVEL NOISE CANCELLATION CIRCUIT AND METHODS	JP	13-Nov-2009	4405854
SE-2108-EL	CURRENT OUTPUT STAGE	US	30-May-2006	7053699
SE-2118-EL	CURRENT MODE PREAMPLIFIERS	US	07-Feb-2006	6995615
SE-2135-EL	BIAS CURRENT CANCELLATION FOR DIFFERENTIAL AMPLIFIERS	US	27-Jun-2006	7068106
SE-2135-EL	BIAS CURRENT CANCELLATION FOR DIFFERENTIAL AMPLIFIERS	US	05-Dec-2006	7145391
SE-2150-EL	INPUT STAGE FOR MULTIPLEXING AND METHODS FOR USING AN INPUT STAGE FOR MULTIPLEXING	US	18-Apr-2006	7030679
SE-2150-EL	INPUT STAGE FOR MULTIPLEXING AND METHODS FOR USING AN INPUT STAGE FOR MULTIPLEXING	US	31-Jul-2007	7250805
SE-2150-EL	INPUT STAGE FOR MULTIPLEXING AND METHODS FOR USING AN INPUT STAGE FOR MULTIPLEXING	US	27-May-2008	7378897
SE-1964-EL	A LASER DRIVER	TW	11-Feb-2013	1385530

Exhibit B
Patent Applications
None.

Exhibit C
Trademarks

CaseNumber	Country	MARK	STATUS	REG #	REG DATE
T499	US	ELANTEC	Registered	2566377	07-May-2002

Exhibit D
Maskworks
None

Exhibit E
Copyright
None

Exhibit F
Domain Names
None