

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM426991

|   |  |                       |  |
|---|--|-----------------------|--|
| <b>SUBMISSION TYPE:</b>   | NEW ASSIGNMENT                                     |                       |  |
| <b>NATURE OF CONVEYANCE:</b>  | ASSIGNMENT OF THE ENTIRE INTEREST AND THE GOODWILL |                       |  |
| <b>CONVEYING PARTY DATA</b>   |  |                       |  |
| <b>Name</b>   | <b>Formerly</b>                                    | <b>Execution Date</b> | <b>Entity Type</b>                     |
| Zilker Labs LLC   |  | 03/29/2013            | Limited Liability Company:<br>DELAWARE |
| <b>RECEIVING PARTY DATA</b>   |  |                       |  |
| <b>Name:</b>  | Intersil Americas LLC                              |                       |  |
| <b>Street Address:</b>  | 1001 Murphy Ranch Road                             |                       |  |
| <b>City:</b>  | milpitas   |                       |  |
| <b>State/Country:</b>   | CALIFORNIA   |                       |  |
| <b>Postal Code:</b>   | 95035  |                       |  |
| <b>Entity Type:</b>   | Limited Liability Company: DELAWARE                |                       |  |
| <b>PROPERTY NUMBERS Total: 2</b>  |  |                       |  |
| <b>Property Type</b>  | <b>Number</b>                                      | <b>Word Mark</b>      |  |
| <b>Registration Number:</b>   | 3091147  | Z                     |  |
| <b>Registration Number:</b>   | 3088496  | ZILKER LABS           |  |
| <b>CORRESPONDENCE DATA</b>  |  |                       |  |
| <b>Fax Number:</b>  | 3128324700   |                       |  |
| <i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i> |  |                       |  |
| <b>Phone:</b>   | 312-832-4552                                       |                       |  |
| <b>Email:</b>   | delder@foley.com,jolsen@foley.com                  |                       |  |
| <b>Correspondent Name:</b>  | Diane G. Elder                                     |                       |  |
| <b>Address Line 1:</b>  | 321 North Clark Street, Suite 2800                 |                       |  |
| <b>Address Line 4:</b>  | Chicago, ILLINOIS 60654                            |                       |  |
| <b>NAME OF SUBMITTER:</b>   | Diane G. Elder                                     |                       |  |
| <b>SIGNATURE:</b>   | /Diane Grace Elder;/dge/61590/                     |                       |  |
| <b>DATE SIGNED:</b>   | 05/10/2017   |                       |  |
| <b>Total Attachments: 12</b>  |  |                       |  |
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## INTELLECTUAL PROPERTY ASSIGNMENT

This Intellectual Property Assignment ('Assignment') is made between:

Zilker Labs LLC (formerly known as Zilker Labs, Inc.), a limited liability company organized and existing under the laws of the State of Delaware, having a principal place of business at 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignor," and

Intersil Americas LLC (formerly known as Intersil Americas Inc.), a limited liability company organized and existing under the laws of the State of Delaware, having a principal business address of 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignee."

### DEFINITIONS

"Intellectual Property Rights" means all intellectual property, industrial property or other proprietary rights that may exist or be created under the laws of any jurisdiction throughout the world including all of the following, whether registered or unregistered, all applications and registrations therefor (whether pending, existing, abandoned or expired), and any physical embodiments thereof: (i) inventions or discoveries, whether or not patentable, reduced to practice or made the subject of one or more pending patent applications, and whether or not under design or development, invention disclosures, improvements, confidential and proprietary information, know-how and technology, (ii) U.S. and foreign patents, patent applications, patent disclosures, utility and industrial models or other rights relating to the protection of inventions worldwide and all rights related thereto, including all original applications, provisional applications, divisional applications, reissues, re-examinations, extensions, continuations, continuations-in-part, continuing applications, or renewals thereof, all counterparts claiming priority therefrom, (iii) trademarks, service marks, certification marks, trade dress (including packaging and package designs, product inserts, labels or associated artwork), logos, slogans, domain names, internet addresses, uniform resource locators, keywords and purchased search terms, identifying symbols, designs, product names, business and company names, trade names, corporate names, insignia and general intangibles of a similar nature (whether registered or not registered) in the United States and all other nations throughout the world, including all variations, derivations, combinations, registrations and applications for registration or renewals of the foregoing and all goodwill associated therewith, (iv) copyrights in both published and unpublished works (whether or not registered) and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, including all works, derivative works, moral rights, renewals, extensions, reversions or restorations associated with such copyrights, now or hereafter provided by law, regardless of the medium of fixation or means of expression, and any other rights of authorship in any other published and unpublished works, including all moral rights in any of the foregoing (v) mask works and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, (vi) computer software (including source code, object code, firmware, operating systems and, development tools, files, records, specifications and all media on which any of the foregoing is

recorded), (vii) information that derives economic value from not being generally known to other Persons and all information that is proprietary or confidential to Assignor, including all trade secrets and, whether or not confidential, business information (including pricing and cost information, business and marketing plans and customer and supplier lists) and technology and know-how (including manufacturing and production processes and techniques and, research and development information, patterns, drawings, blueprints, bills of materials, specifications, products in development, processes, applications, and circuits), (viii) industrial designs (whether or not registered), (ix) databases and data collections, and all rights therein throughout the world, and (x) copies and tangible embodiments of any of the foregoing, in whatever form or medium (including electronic media).

## ASSIGNMENT

Assignor is the owner of all right, title, and interest in and to Intellectual Property Rights and Assignee is desirous of acquiring any and all such interest in and to Assignor's said Intellectual Property Rights. Assignor and Assignee are sister companies directly or indirectly owned by the same corporate entity.

NOW, THEREFORE, be it known, that for good and valuable consideration, the receipt of which is hereby acknowledged by Assignor, Assignor's entire right, title and interest in and to said Intellectual Property Rights, including without limitation:

- a. the patents specified in Exhibit A attached hereto and made part of this Assignment;
- b. the patent applications specified in Exhibit B attached hereto and made part of this Assignment;
- c. the registered trademarks specified in Exhibit C attached hereto and made part of this Assignment;
- d. the registered mask works specified in Exhibit D attached hereto and made part of this Assignment;
- e. the registered copyrights specified in Exhibit E attached hereto and made part of this Assignment;
- f. the domain names specified in Exhibit F attached hereto and made part of this Assignment;
- g. the right to file applications for patents, trademarks, or registrations of service marks, trade dress, copyrights, and mask works;
- h. the right to file reissue, re-examination, continuation, or divisional applications;
- i. the right to file extensions;
- j. the right to file statements of use;
- k. the right to sue (i) for, recover and collect damages and costs and attorneys' fees for past, present and future, and (b) for injunctive relief, for infringement or misappropriation; and
- l. the right to license, and collect royalties, license fees, and all other forms of payment on account of such Intellectual Property Rights

is hereby assigned, conveyed and transferred to Assignee.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, trademark registrations, copyright registrations, mask work registrations, or other governmental grants or issuances that may be granted upon any of the Intellectual Property Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Intellectual Property Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

Assignee hereby accepts all right, title and interest granted to it herein.

Assignor agrees that Assignor will make, execute and deliver any and all other instruments in writing including any and all further application papers, affidavits, assignments and other documents, and will communicate to the Assignee, its successors and representatives all facts known to Assignor relating to the Intellectual Property Rights and generally do all things which may be necessary or desirable more effectually to secure to and vest in the Assignee, its successors or assigns the entire right, title and interest in and to the Intellectual Property Rights.

This Agreement contains the complete and entire agreement between the Parties related to the subject matter of this Agreement, and supersedes any previous communications, representations, or agreements, whether verbal or written.

The provisions of this Agreement shall be deemed separable. Therefore, if any part of this Agreement is rendered void, invalid or unenforceable, such rendering shall not affect the validity or enforceability of the remainder of this Agreement.

**{END OF CLAUSES}**

IN WITNESS whereof the parties hereto have executed this Assignment.

**Zilker Labs LLC (Assignor)**

By: Douglas A. Balog  
Name: Douglas A. Balog  
Title: Assistant Corporate Secretary

Date: March 29, 2013

.....  
State of Florida            )  
                                      ) SS  
County of Brevard        )

On March 29, 2013 before me, Patricia M. Wiles, Notary Public, personally appeared Douglas A. Balog, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his capacity as Assistant Corporate Secretary of Zilker Labs LLC, and has proven to me on the basis of satisfactory evidence that he/she had and has the full authority to execute documents on behalf of Zilker Labs LLC and without any countersignature by any other individual, and that by his signature on the instrument the entity, upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.

Patricia M Wiles

Notary Public



**Intersil Americas LLC (Assignee)**

By: *Paul A. Bernkopf*  
Name: Paul A. Bernkopf  
Title: Vice President and Asst. Secretary  
Date: March 29<sup>p.k.</sup>, 2013

State of Florida )  
County of Brevard ) SS

On March 29, 2013 before me, Patricia M. Wiles, Notary Public, personally appeared Paul A. Bernkopf, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his capacity as Vice President and Asst. Secretary of Intersil Americas LLC, and has proven to me on the basis of satisfactory evidence that he/she had and has the full authority to execute documents on behalf of Intersil Americas LLC and without any countersignature by any other individual, and that by his signature on the instrument the entity, upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.



*Patricia M. Wiles*  
Notary Public

Exhibit A  
Patents

| Intersil # | Title  | Country | Issue Date  | Pat #   |
|------------|--|---------|-------------|---------|
| SE-2575-IP | METHOD AND APPARATUS FOR IMPROVED DC POWER DELIVERY, MANAGEMENT AND CONFIGURATION                        | US      | 17-Mar-2009 | 7506179 |
| SE-2575-IP | METHOD AND APPARATUS FOR IMPROVED DC POWER DELIVERY, MANAGEMENT AND CONFIGURATION                        | US      | 16-Dec-2008 | 7467309 |
| SE-2575-IP | METHOD AND APPARATUS FOR IMPROVED DC POWER DELIVERY, MANAGEMENT AND CONFIGURATION                        | US      | 01-Jun-2010 | 7730332 |
| SE-2576-IP | METHOD FOR USING A MULTI-MASTER MULTI-SLAVE BUS FOR POWER MANAGEMENT                                     | US      | 26-Jan-2010 | 7653757 |
| SE-2577-IP | METHOD FOR ACCURATELY SETTING PARAMETERS INSIDE INTEGRATED CIRCUITS USING INACCURATE EXTERNAL COMPONENTS | US      | 06-Nov-2007 | 7292019 |
| SE-2577-IP | METHOD FOR ACCURATELY SETTING PARAMETERS INSIDE INTEGRATED CIRCUITS USING INACCURATE EXTERNAL COMPONENTS | US      | 23-Feb-2010 | 7668607 |
| SE-2578-IP | METHODS FOR ACCURATE CURRENT SENSING IN POWER CONVERTERS   | US      | 15-Sep-2009 | 7589514 |
| SE-2579-IP | METHOD FOR PRE-BIAS CORRECTION DURING TURN-ON OF SWITCHING POWER REGULATORS                              | US      | 25-May-2010 | 7723970 |
| SE-2580-IP | METHOD FOR CONTROL OF OVERLAP TIMES IN SWITCHING POWER CONVERTERS  | US      | 16-Oct-2012 | 8289010 |
| SE-2581-IP | HARDWARE EFFICIENT DIGITAL CONTROL LOOP ARCHITECTURE FOR A POER CONVERTER                                | US      | 03-Jul-2007 | 7239257 |
| SE-2582-IP | ADAPTIVE THRESHOLDING TECHNIQUE FOR POWER SUPPLIES DURING MARGINING EVENTS                               | US      | 28-Jul-2009 | 7568117 |
| SE-2584-IP | POWER CONVERTER WITH TRANSIENT PROCESSING CAPABILITY   | US      | 09-Jun-2009 | 7545131 |
| SE-2584-IP | POWER CONVERTER WITH TRANSIENT PROCESSING CAPABILITY   | US      | 13-Jul-2010 | 7755343 |
| SE-2584-IP | POWER CONVERTER WITH TRANSIENT PROCESSING CAPABILITY   | US      | 29-Mar-2011 | 7915864 |



| Intersil # | Title  | Country | Issue Date  | Pat #          |
|------------|--|---------|-------------|----------------|
| SE-2585-IP | INTEGRATED MULTI-FUNCTION POINT-OF-LOAD REGULATOR CIRCUIT        | US      | 07-Sep-2010 | 7793005        |
| SE-2585-IP | INTEGRATED MULTI-FUNCTION POINT-OF-LOAD REGULATOR CIRCUIT        | US      | 15-Mar-2011 | 7908402        |
| SE-2586-IP | AUTONOMOUS SEQUENCING AND FAULT SPREADING                        | US      | 23-Mar-2010 | 7685320        |
| SE-2587-IP | CURRENT SENSE METHOD   | US      | 05-Feb-2008 | 7327130        |
| SE-2589-IP | CONTROL SYSTEM OPTIMIZATION VIA INDEPENDENT PARAMETER ADJUSTMENT | US      | 02-Nov-2010 | 7825642        |
| SE-2593-IP | ADAPTIVE COMPENSATION FOR DIGITAL POWER CONTROLLERS              | US      | 11-Oct-2011 | 8036762        |
| SE-2595-IP | CONTROL SYSTEM OPTIMIZATION VIA DIGITAL DIODE EMULATION          | US      | 06-Dec-2011 | 8072204        |
| SE-2597-IP | TRANSIENT PROCESSING MECHANISM FOR POWER CONVERTERS              | DE      | 29-Jul-2010 | 202009013410.5 |

Exhibit B  
Patent Applications

| Intersil # | Title  | Country | Filing Date | Apl. #           |
|------------|--|---------|-------------|------------------|
| SE-2576-IP | METHOD FOR USING A MULTI-MASTER MULTI-SLAVE BUS FOR POWER MANAGEMENT                                   | US      | 17-Apr-2006 | 11/405294        |
| SE-2579-IP | METHOD FOR PRE-BIAS CORRECTION DURING TURN-ON OF SWITCHING POWER REGULATORS                            | US      | 04-May-2010 | 12/773459        |
| SE-2583-IP | METHOD AND APPARATUS FOR CONTROLLING OPERATING PARAMETERS OF COMPONENTS IN A POWER DISTRIBUTION SYSTEM | US      | 16-Feb-2006 | 11/355703        |
| SE-2594-IP | CONTROL SYSTEM OPTIMIZATION VIA ADAPTIVE FREQUENCY ADJUSTMENT  | US      | 09-May-2008 | 12/118260        |
| SE-2597-IP | TRANSIENT PROCESSING MECHANISM FOR POWER CONVERTERS  | CN      | 23-Oct-2009 | CN200910246842.4 |

Exhibit C  
Trademarks

| Case Number | Country | MARK                           | REG.#          | REG. DATE   |
|-------------|---------|--------------------------------|----------------|-------------|
| T549        | US      | ZILKER<br>LABS (AND<br>DESIGN) | 3088496        | 02-May-2006 |
| T549-TW     | TW      | ZILKER<br>LABS (AND<br>DESIGN) | 1248765        | 01-Feb-2007 |
| T549-KR     | KR      | ZILKER<br>LABS (AND<br>DESIGN) | 40-<br>0703327 | 19-Jan-2007 |
| T549-CN     | CN      | ZILKER<br>LABS (AND<br>DESIGN) | 5056317        | 28-Dec-2008 |
| T549-JP     | JP      | ZILKER<br>LABS (AND<br>DESIGN) | 5017410        | 12-Jan-2007 |
| T549-HK     | HK      | ZILKER<br>LABS (AND<br>DESIGN) | 300536238      | 24-Nov-2005 |
| T549-1      | US      | Z (AND<br>DESIGN)              | 3091147        | 02-May-2006 |
| T550        | US      | POWERING<br>YOUR<br>IDEAS      | 3178371        | 28-Nov-2006 |

Exhibit D  
Maskworks  
*None*

Exhibit E  
Copyright  
*None*

Exhibit F  
Domain Names  
www.zilker.us  
www.zilkerlabs.us  
www.zilkerlabs.net  
www.zilkerlabs.tw