

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM426890

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	ASSIGNMENT OF THE ENTIRE INTEREST AND THE GOODWILL		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
D2Audio LLC		03/29/2013	Limited Liability Company: DELAWARE
RECEIVING PARTY DATA			
Name:	Intersil Americas LLC		
Street Address:	1001 Murphy Ranch Road		
City:	Milpitas		
State/Country:	CALIFORNIA		
Postal Code:	95035		
Entity Type:	Limited Liability Company: DELAWARE		
PROPERTY NUMBERS Total: 1			
Property Type	Number	Word Mark	
Registration Number:	2846558	D2AUDIO	
CORRESPONDENCE DATA			
Fax Number:	3128324700		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	312-832-4552		
Email:	delder@foley.com,jolsen@foley.com		
Correspondent Name:	Diane G. Elder		
Address Line 1:	321 North Clark Street, Suite 2800		
Address Line 4:	Chicago, ILLINOIS 60654		
NAME OF SUBMITTER:	Diane G. Elder		
SIGNATURE:	/Diane Grace Elder;/dge/61590/		
DATE SIGNED:	05/09/2017		
Total Attachments: 16			
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INTELLECTUAL PROPERTY ASSIGNMENT

This Intellectual Property Assignment ('Assignment') is made between:

D2Audio LLC (formerly known as D2Audio Corporation), a limited liability company organized and existing under the laws of the State of Delaware, having a principal place of business at 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignor," and

Intersil Americas LLC (formerly known as Intersil Americas Inc.), a limited liability company organized and existing under the laws of the State of Delaware, having a principal business address of 1001 Murphy Ranch Road, Milpitas, CA 95035 USA, hereinafter referred to as "the Assignee."

DEFINITIONS

"Intellectual Property Rights" means all intellectual property, industrial property or other proprietary rights that may exist or be created under the laws of any jurisdiction throughout the world including all of the following, whether registered or unregistered, all applications and registrations therefor (whether pending, existing, abandoned or expired), and any physical embodiments thereof: (i) inventions or discoveries, whether or not patentable, reduced to practice or made the subject of one or more pending patent applications, and whether or not under design or development, invention disclosures, improvements, confidential and proprietary information, know-how and technology, (ii) U.S. and foreign patents, patent applications, patent disclosures, utility and industrial models or other rights relating to the protection of inventions worldwide and all rights related thereto, including all original applications, provisional applications, divisional applications, reissues, re-examinations, extensions, continuations, continuations-in-part, continuing applications, or renewals thereof, all counterparts claiming priority therefrom, (iii) trademarks, service marks, certification marks, trade dress (including packaging and package designs, product inserts, labels or associated artwork), logos, slogans, domain names, internet addresses, uniform resource locators, keywords and purchased search terms, identifying symbols, designs, product names, business and company names, trade names, corporate names, insignia and general intangibles of a similar nature (whether registered or not registered) in the United States and all other nations throughout the world, including all variations, derivations, combinations, registrations and applications for registration or renewals of the foregoing and all goodwill associated therewith, (iv) copyrights in both published and unpublished works (whether or not registered) and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, including all works, derivative works, moral rights, renewals, extensions, reversions or restorations associated with such copyrights, now or hereafter provided by law, regardless of the medium of fixation or means of expression, and any other rights of authorship in any other published and unpublished works, including all moral rights in any of the foregoing (v) mask works and registrations and applications for registration or renewals thereof in the United States and all other nations throughout the world, (vi) computer software (including source code, object code, firmware, operating systems and, development tools, files, records, specifications and all media on which any of the foregoing is

recorded), (vii) information that derives economic value from not being generally known to other Persons and all information that is proprietary or confidential to Assignor, including all trade secrets and, whether or not confidential, business information (including pricing and cost information, business and marketing plans and customer and supplier lists) and technology and know-how (including manufacturing and production processes and techniques and, research and development information, patterns, drawings, blueprints, bills of materials, specifications, products in development, processes, applications, and circuits), (viii) industrial designs (whether or not registered), (ix) databases and data collections, and all rights therein throughout the world, and (x) copies and tangible embodiments of any of the foregoing, in whatever form or medium (including electronic media).

ASSIGNMENT

Assignor is the owner of all right, title, and interest in and to Intellectual Property Rights and Assignee is desirous of acquiring any and all such interest in and to Assignor's said Intellectual Property Rights. Assignor and Assignee are sister companies directly or indirectly owned by the same corporate entity.

NOW, THEREFORE, be it known, that for good and valuable consideration, the receipt of which is hereby acknowledged by Assignor, Assignor's entire right, title and interest in and to said Intellectual Property Rights, including without limitation:

- a. the patents specified in Exhibit A attached hereto and made part of this Assignment;
- b. the patent applications specified in Exhibit B attached hereto and made part of this Assignment;
- c. the registered trademarks specified in Exhibit C attached hereto and made part of this Assignment;
- d. the registered mask works specified in Exhibit D attached hereto and made part of this Assignment;
- e. the registered copyrights specified in Exhibit E attached hereto and made part of this Assignment;
- f. the domain names specified in Exhibit F attached hereto and made part of this Assignment;
- g. the right to file applications for patents, trademarks, or registrations of service marks, trade dress, copyrights, and mask works;
- h. the right to file reissue, re-examination, continuation, or divisional applications;
- i. the right to file extensions;
- j. the right to file statements of use;
- k. the right to sue (i) for, recover and collect damages and costs and attorneys' fees for past, present and future, and (b) for injunctive relief, for infringement or misappropriation; and
- l. the right to license, and collect royalties, license fees, and all other forms of payment on account of such Intellectual Property Rights

is hereby assigned, conveyed and transferred to Assignee.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, trademark registrations, copyright registrations, mask work registrations, or other governmental grants or issuances that may be granted upon any of the Intellectual Property Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Intellectual Property Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

Assignee hereby accepts all right, title and interest granted to it herein.

Assignor agrees that Assignor will make, execute and deliver any and all other instruments in writing including any and all further application papers, affidavits, assignments and other documents, and will communicate to the Assignee, its successors and representatives all facts known to Assignor relating to the Intellectual Property Rights and generally do all things which may be necessary or desirable more effectually to secure to and vest in the Assignee, its successors or assigns the entire right, title and interest in and to the Intellectual Property Rights.

This Agreement contains the complete and entire agreement between the Parties related to the subject matter of this Agreement, and supersedes any previous communications, representations, or agreements, whether verbal or written.

The provisions of this Agreement shall be deemed separable. Therefore, if any part of this Agreement is rendered void, invalid or unenforceable, such rendering shall not affect the validity or enforceability of the remainder of this Agreement.

{END OF CLAUSES}

IN WITNESS whereof the parties hereto have executed this Assignment.

D2Audio LLC (Assignor)

By: Douglas A. Balog

Name: Douglas A. Balog

Title: Assistant Corporate Secretary

Date: March 29, 2013

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State of Florida)
) SS
County of Brevard)

On March 29, 2013 before me, Patricia M. Wiles, Notary Public, personally appeared Douglas A. Balog, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his capacity as Assistant Corporate Secretary of D2Audio LLC , and has proven to me on the basis of satisfactory evidence that he/she had and has the full authority to execute documents on behalf of D2Audio LLC and without any countersignature by any other individual, and that by his signature on the instrument the entity, upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.



Patricia M Wiles

Notary Public

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Exhibit A
Patents

Intersil #	Title	Country	Issue Date	Pat #
SE-2477-IP	SYSTEMS AND METHODS FOR AUTOMATICALLY ADJUSTING CHANNEL TIMING	US	04-Apr-2006	7023268
SE-2478-IP	INTEGRATED PULSHI MODE WITH SHUTDOWN	US	18-Jul-2006	7078963
SE-2479-IP	MULTI-CHIP PWM SYNCHRONIZATION AND COMMUNICATION	US	01-Sep-2009	7584009
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	US	27-Apr-2010	7706545
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	CN	29-Apr-2009	ZL200480010567.1
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	DE	21-May-2008	602004013940
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	EP	21-May-2008	1606875
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	FR	21-May-2008	1606875
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	GB	21-May-2008	1606875
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	JP	05-Nov-2010	4620043
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	US	23-Jan-2007	7167112
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	US	26-Jun-2012	RE43489
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	CN	15-Jun-2011	ZL200480010769.6
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	CN	08-Jul-2009	ZL200480010769.6
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	DE	12-Dec-2007	602004010632
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	EP	12-Dec-2007	1609241

Intersil #	Title	Country	Issue Date	Pat #
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	FR	12-Dec-2007	1609241
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	GB	12-Dec-2007	1609241
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	JP	13-Mar-2009	4276258
SE-2482-IP	STREAMING MULTI-CHANNEL AUDIO AS PACKETIZED DATA OR PARALLEL DATA WITH A SEPARATE INPUT FRAME SYNC	US	15-Jun-2010	7738613
SE-2483-IP	SRC WITH MULTIPLE SETS OF FILTER COEFFICIENTS IN MEMORY AND A HIGH ORDER COEFFICIENT INTERPOLATOR	US	15-Mar-2011	7908306
SE-2484-IP	SYSTEMS AND METHODS FOR SAMPLE RATE CONVERSION	US	06-Jan-2009	7474722
SE-2484-IP	SYSTEMS AND METHODS FOR SAMPLE RATE CONVERSION	US	28-Jun-2011	7970088
SE-2485-IP	PHASE ALIGNMENT OF AUDIO OUTPUT DATA IN A MULTI-CHANNEL CONFIGURATION	US	01-Jun-2010	7729790
SE-2486-IP	OUTPUT STAGE SYNCHRONIZATION	US	14-Oct-2008	7436918
SE-2487-IP	SYSTEMS AND METHODS FOR SWITCHING AND MIXING SIGNALS IN A MULTI-CHANNEL AMPLIFIER	US	19-Apr-2011	7929718
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	US	15-Sep-2009	7590251
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	CN	15-Jun-2011	ZL200480009689.9
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	HK	30-Mar-2012	HK1094626
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	JP	29-Oct-2010	4617298
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	US	13-Jun-2006	7061312
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	CN	24-Jun-2009	ZL200480010526.2
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	DE	12-May-2008	602004005178
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	EP	12-May-2008	1623501
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	FR	12-May-2008	1623501

Intersil #	Title	Country	Issue Date	Pat #
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	GB	12-May-2008	1623501
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	JP	14-Aug-2009	4358860
SE-2490-IP	SYSTEMS AND METHODS FOR LOAD DETECTION AND CORRECTION IN A DIGITAL AMPLIFIER	US	12-Jun-2012	RE43461
SE-2491-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	US	19-Jan-2010	7649410
SE-2491-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	CN	20-Apr-2011	ZL200780002954.4
SE-2491-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	JP	16-Mar-2012	4950223
SE-2492-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	US	01-Jan-2008	7315264
SE-2492-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	CN	16-Jan-2012	ZL200780002413.1
SE-2492-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	JP	22-Nov-2012	5139325
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	US	23-Oct-2007	7286010
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	CN	17-Aug-2011	ZL200780003547.5
SE-2494-IP	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	US	16-Sep-2008	7425853
SE-2494-IP	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	CN	31-Jan-2013	ZL200780004155.0
SE-2494-IP	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	JP	09-Dec-2011	4878376
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	US	14-Apr-2009	7518444
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	CN	07-Feb-2012	ZL200780005026.3
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	DE	07-Feb-2011	602007008064
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	DK	07-Feb-2011	1985014

Intersil #	Title	Country	Issue Date	Pat #
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	EP	28-Jul-2010	1985014
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	GB	07-Feb-2011	1985014
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	JP	20-Jan-2012	4909363
SE-2495-IP	PWM FEEDBACK/FEEDFORWARD PROTECTION	NL	07-Feb-2011	1985014
SE-2496-IP	SYSTEMS AND METHODS FOR MINIMIZING DELAY IN A CONTROL PATH	US	22-Apr-2008	7362254
SE-2497-IP	POWER SUPPLY FEED FORWARD ANALOG INPUT FILTER COMPONENT MISMATCH	US	14-Oct-2008	7436255
SE-2498-IP	SYSTEMS AND METHODS FOR CORRECTING ERRORS RESULTING FROM COMPONENT MISMATCH IN A FEEDBACK PATH	US	27-Jan-2009	7482865
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	US	23-Oct-2007	7286009
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	US	18-Aug-2009	7576606
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	US	01-Jun-2010	7728658
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	US	12-Oct-2010	7812666
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	CN	15-Aug-2012	ZL200680053489.2
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	JP	22-Nov-2012	5139321
SE-2500-IP	SYSTEMS AND METHODS FOR MAINTAINING MINIMUM PULSE WIDTH DURING SHUTDOWN	US	14-Oct-2008	7436256
SE-2501-IP	SYSTEMS AND METHODS TO MINIMIZE STARTUP TRANSIENTS IN CLASS D AMPLIFIERS	US	07-Sep-2010	7791427
SE-2501-IP	SYSTEMS AND METHODS TO MINIMIZE STARTUP TRANSIENTS IN CLASS D AMPLIFIERS	US	14-Feb-2012	8115564
SE-2502-IP	SYSTEMS AND METHODS FOR CONTROLLING AUDIO VOLUME IN THE PROCESSOR OF A HIGH DEFINITION AUDIO CODEC	US	17-Jul-2012	8224469
SE-2503-IP	SYSTEMS AND METHODS FOR COMMUNICATION BETWEEN A PC APPLICATION AND THE DSP IN AN HDA AUDIO CODEC	CN	04-Jul-2012	ZL200880105062.1
SE-2504-IP	SYSTEMS AND METHODS FOR OVERRIDING HARDWIRED RESPONSES IN AN HDA CODEC	US	10-Jul-2012	8219226
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	US	20-Dec-2011	8082438
SE-2507-IP	SYSTEMS AND METHODS FOR SHADOWING AN HDA CODEC	US	21-Aug-2012	8249730
SE-2507-IP	SYSTEMS AND METHODS FOR SHADOWING AN HDA CODEC	CN	17-Oct-2012	ZL200880105207.8

Intersil #	Title	Country	Issue Date	Pat #
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	US	27-Sep-2011	8028101
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	US	03-Jul-2012	8214543
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	CN	30-May-2012	ZL200880105194.4
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	TW	30-May-2012	0880105194
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	DE	07-Mar-2012	602006028111
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	EP	07-Mar-2012	1972055
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	JP	21-Dec-2012	5161792

Exhibit B
Patent Applications

Intersil #	Title	Country	Filing Date	Apl. #
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	US	11-Aug-2011	13/207232
SE-2503-IP	SYSTEMS AND METHODS FOR COMMUNICATION BETWEEN A PC APPLICATION AND THE DSP IN AN HDA AUDIO CODEC	US	01-Sep-2008	12/202356
SE-2511-IP	SYSTEMS AND METHODS FOR IMPROVED OVER-CURRENT CLIPPING	US	28-Oct-2011	13284723
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	CN	01-Sep-2008	CN200880105063.6
SE-2502-IP	SYSTEMS AND METHODS FOR CONTROLLING AUDIO VOLUME IN THE PROCESSOR OF A HIGH DEFINITION AUDIO CODEC	CN	01-Sep-2008	CN200880105045.8
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	EP	27-Sep-2005	04757922.2
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	EP	25-Jan-2007	07717391.2
SE-2494-IP	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	EP	31-Jan-2007	07710437.0
SE-2492-IP	SYSTEMS AND METHODS FOR CONTROLLING TRANSIENT RESPONSE IN THE OUTPUT OF A NOISE SHAPER	EP	31-Jul-2008	07717294.8
SE-2491-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	EP	22-Aug-2008	07717387.0
SE-2503-IP	SYSTEMS AND METHODS FOR COMMUNICATION BETWEEN A PC APPLICATION AND THE DSP IN AN HDA AUDIO CODEC	EP	01-Sep-2008	08799039
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	EP	01-Sep-2008	08828228
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	EP	01-Sep-2008	08828356
SE-2510-IP	SWITCHING AMPLIFIER OPTIMIZED FOR MINIMAL AND STABLE OUTPUT SEMICONDUCTOR DEAD TIMES	EP	14-Nov-2008	08849951
SE-2489-IP	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	HK	27-Sep-2005	06108265.5
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	HK	25-Jul-2006	06108266.4
SE-2481-IP	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	HK	01-Nov-2007	07111872.3

Intersil #	Title	Country	Filing Date	Apl. #
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	JP	01-Sep-2008	2010-523192
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	JP	01-Sep-2008	2010-523193
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	TW	01-Sep-2008	097133406
SE-2507-IP	SYSTEMS AND METHODS FOR SHADOWING AN HDA CODEC	TW	01-Sep-2008	097133408
SE-2503-IP	SYSTEMS AND METHODS FOR COMMUNICATION BETWEEN A PC APPLICATION AND THE DSP IN AN HDA AUDIO CODEC	TW	01-Sep-2008	097133405
SE-2488-IP	CLIP DETECTION IN PWM AMPLIFIER	WO	07-Oct-2004	WO2004/086616
SE-2480-IP	SYSTEMS AND METHODS FOR PROTECTION OF AUDIO AMPLIFIER CIRCUITS	WO	07-Oct-2004	WO2004/086609
SE-2499-IP	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	WO	12-Jul-2007	WO2007/079396
SE-2492-IP	SYSTEMS AND METHODS FOR CONTROLLING TRANSIENT RESPONSE IN THE OUTUP OF A NOISE SHAPER	WO	26-Jul-2007	WO2007/084894
SE-2491-IP	SYSTEMS AND METHODS FOR IMPROVING PERFORMANCE IN A DIGITAL AMPLIFIER BY ADDING AN ULTRASONIC SIGNAL TO AN INPUT AUDIO SIGNAL	WO	02-Aug-2007	WO2007/087568
SE-2493-IP	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	WO	02-Aug-2007	WO2007/087586
SE-2494-IP	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	WO	09-Aug-2007	WO2007/090164
SE-2495-IP	PWM FEEDBACK/FEED-FORWARD PROTECTION	WO	04-Oct-2007	WO2007/112156
SE-2508-IP	SYSTEMS AND METHODS FOR CONTROLLING HDA SYSTEM CAPABILITIES	WO	05-Mar-2009	WO2009/029920
SE-2507-IP	SYSTEMS AND METHODS FOR SHADOWING AN HDA CODEC	WO	05-Mar-2009	WO2009/029919
SE-2505-IP	SYSTEMS AND METHODS FOR BOOTING A CODEC PROCESSOR OVER A HIGH DEFINITION AUDIO BUS	WO	05-Mar-2009	WO2009/029918
SE-2502-IP	SYSTEMS AND METHODS FOR CONTROLLING AUDIO VOLUME IN THE PROCESSOR OF A HIGH DEFINITION AUDIO CODEC	WO	05-Mar-2009	WO2009/029916
SE-2503-IP	SYSTEMS AND METHODS FOR COMMUNICATION BETWEEN A PC APPLICATION AND THE DSP IN AN HDA AUDIO CODEC	WO	05-03-2009	WO2009/029917
SE-2502-IP	SYSTEMS AND METHODS FOR CONTROLLING AUDIO VOLUME IN THE PROCESSOR OF A HIGH DEFINITION AUDIO CODEC	TW	01-Sep-2008	097133404

Exhibit C
Trademarks

CaseNumber	Country	MARK	STATUS	REG #	REG DATE
T543	US	D2AUDIO	Registered	2846558	25-May-2004
T544	US	D2A	Registered	2841655	11-May-2004
T543-JP	JP	D2AUDIO	Registered	5016987	12-Jan-2007
T543-CN	CN	D2AUDIO	Registered	5338068	29-Aug-2009
T543EP	EM	D2AUDIO	Registered	3319911	31-Mar-2005
T543-TW	TW	D2AUDIO	Registered	01225736	01-Sep-2006

Exhibit D
Maskworks
None

Exhibit E
Copyright
None

Exhibit F
Domain Names
www.d2audio.com