

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM452970

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
Gold Hill Capital 2008, LP		08/01/2017	Corporation: DELAWARE
RECEIVING PARTY DATA			
Name:	eSilicon Corporation		
Street Address:	2130 Gold Street, Suite 100		
City:	San Jose		
State/Country:	CALIFORNIA		
Postal Code:	95002		
Entity Type:	Corporation: DELAWARE		
PROPERTY NUMBERS Total: 7			
Property Type	Number	Word Mark	
Registration Number:	2969847	ESILICON	
Registration Number:	3412021	ESILICON	
Registration Number:	3591594	ESILICON	
Registration Number:	3603342	ESILICON ACCESS	
Registration Number:	2887715	XPEEDIUM	
Registration Number:	2964234	XPEEDIUM2	
Registration Number:	2965270	XPEEDIUM3	
CORRESPONDENCE DATA			
Fax Number:	6509888500		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	6509888500		
Email:	trademarks@fenwick.com		
Correspondent Name:	Moria Lion		
Address Line 1:	Fenwick & West		
Address Line 2:	801 California Street		
Address Line 4:	Mountain View, CALIFORNIA 94041		
ATTORNEY DOCKET NUMBER:	31557-70007-5244		
NAME OF SUBMITTER:	Moirra Lion		
SIGNATURE:	/moira lion/		

CH \$190.00 2969847

DATE SIGNED:	12/01/2017
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Total Attachments: 9

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LIEN RELEASE

August __, 2017

Reference is hereby made to that certain Intellectual Property Security Agreement (the "Agreement"), dated as of August 8, 2012, made among eSilicon Corporation ("Grantor"), Gold Hill Capital 2008, LP ("Gold Hill"), and Silicon Valley Bank ("SVB", and together with Gold Hill, the "Secured Parties"), which was recorded with the Trademark Division of the United States Patent and Trademark Office ("USPTO") on August 14, 2012 on Reel 4841 Frame 0896, the Patent Division of the USPTO on August 14, 2012 on Reel 28782 Frame 0322, and the United States Copyright Office ("USCO") on August 16, 2016 in Volume 3608 Document 517, and pursuant to which the Grantor assigned and granted to Secured Parties a security interest in and to all of Grantor's right, title and interest in and to those certain (i) copyrights specifically listed on Exhibit A attached hereto, (ii) patents specifically listed on Exhibit B attached hereto, and (iii) trademarks, and the goodwill associated therewith, specifically listed on Exhibit C attached hereto (collectively, the "Intellectual Property"); and

WHEREAS, Gold Hill wishes to release its security interest in and to the Intellectual Property;


NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, Gold Hill hereby releases any and all liens, security interests, right, title and interest of Gold Hill in and to the Intellectual Property.

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IN WITNESS WHEREOF, Gold Hill has executed this Release as of the date first above written.

GOLD HILL CAPITAL 2008, LP

By: Gold Hill Capital 2008, LLC,
Its: General Partner

By: 

Name:
Title: Tim Waterson
Partner
Gold Hill Capital

Release

Exhibit A

Copyright Titles

Owner	Country	Copyright Title	Registration No.	Registration Date
eSilicon Corporation	USA	Component Autogenerator System for Test Chip: End-User Manual, Version 1.0.	TX0007537285	6/5/2012

BN 30259293v1

Exhibit B

Patents

(see attached listing)

BN 30259293v1



United States Patent and Trademark Office

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Assignments on the Web > Patent Query**Patent Assignment Details****NOTE: Results display only for issued patents and published applications.
For pending or abandoned applications please consult USPTO staff.**Reel/Frame: 028782/0322

Pages: 14

Recorded: 08/14/2012

Attorney Dkt #: 311857- 015

Conveyance: SECURITY AGREEMENT

Total properties: 19

1	Patent #: <u>6604203</u> Title: APPARATUS AND METHOD FOR SELF-SYNCHRONIZATION OF DATA TO A LOCAL CLOCK	Issue Dt: 08/05/2003	Application #: 09394376	Filing Dt: 09/10/1999
2	Patent #: <u>6625151</u> Title: MULTICASTING METHOD AND ARRANGEMENT	Issue Dt: 09/23/2003	Application #: 09420909	Filing Dt: 10/20/1999
3	Patent #: <u>6754742</u> Title: QUEUE MANAGEMENT SYSTEM PERFORMING ONE READ ONE WRITE DURING ONE CYCLE BY USING FREE QUEUES	Issue Dt: 06/22/2004	Application #: 09428285	Filing Dt: 10/27/1999
4	Patent #: <u>6134135</u> Title: MASK ARRANGEMENT FOR SCALABLE CAM/RAM STRUCTURES	Issue Dt: 10/17/2000	Application #: 09480827	Filing Dt: 01/10/2000
5	Patent #: <u>7215678</u> Title: METHOD AND APPARATUS FOR DISTRIBUTION OF BANDWIDTH IN A SWITCH	Issue Dt: 05/08/2007	Application #: 09546494	Filing Dt: 04/10/2000
6	Patent #: <u>6977940</u> Title: METHOD AND ARRANGEMENT FOR MANAGING PACKET QUEUES IN SWITCHES	Issue Dt: 12/20/2005	Application #: 09560105	Filing Dt: 04/28/2000
7	Patent #: <u>6330177</u> Title: CAM/RAM memory device with a scalable structure	Issue Dt: 12/11/2001	Application #: 09574354	Filing Dt: 05/19/2000
8	Patent #: <u>6944171</u> Publication #: <u>US20020126683</u> Title: SCHEDULER METHOD AND DEVICE IN A SWITCH	Issue Dt: 09/13/2005 Pub Dt: 09/12/2002	Application #: 09804591	Filing Dt: 03/12/2001
9	Patent #: <u>6748287</u> Title: ADAPTIVE REAL-TIME WORK-IN-PROGRESS TRACKING, PREDICTION, AND OPTIMIZATION SYSTEM FOR A SEMICONDUCTOR SUPPLY CHAIN	Issue Dt: 06/08/2004	Application #: 09912028	Filing Dt: 07/23/2001
10	Patent #: <u>7218980</u> Title: PREDICTION BASED OPTIMIZATION OF A SEMICONDUCTOR SUPPLY CHAIN USING AN ADAPTIVE REAL TIME WORK-IN-PROGRESS TRACKING SYSTEM	Issue Dt: 05/15/2007	Application #: 09912030	Filing Dt: 07/23/2001
11	Patent #: <u>7474933</u> Publication #: <u>US20050060212</u> Title: SYSTEM AND METHOD FOR AUTOMATING INTEGRATION OF SEMICONDUCTOR WORK IN PROCESS UPDATES	Issue Dt: 01/06/2009 Pub Dt: 03/17/2005	Application #: 10619738	Filing Dt: 07/14/2003
12	Patent #: <u>7158529</u> Publication #: <u>US20060034312</u> Title: DEVICE FOR DATASTREAM DECODING	Issue Dt: 01/02/2007 Pub Dt: 02/16/2006	Application #: 11255759	Filing Dt: 10/21/2005
13	Patent #: <u>7603509</u> Title: CROSSBAR SWITCH WITH GROUPED INPUTS AND OUTPUTS	Issue Dt: 10/13/2009	Application #: 12069037	Filing Dt: 02/06/2008
14	Patent #: <u>7701255</u>	Issue Dt: 04/20/2010	Application #: 12265571	Filing Dt: 11/05/2008

<http://legacy-assignments.uspto.gov/assignments/q?db=pat&reel=028782&frame=0322>

08/25/17

- Publication #: US20090115469 Pub Dt: 05/07/2009
 Title: VARIABILITY-AWARE SCHEME FOR ASYNCHRONOUS CIRCUIT INITIALIZATION
- 15 Patent #: 8572539 Issue Dt: 10/29/2013 Application #: 12265585 Filing Dt: 11/05/2008
 Publication #: US20090115503 Pub Dt: 05/07/2009
 Title: VARIABILITY-AWARE SCHEME FOR HIGH-PERFORMANCE ASYNCHRONOUS CIRCUIT VOLTAGE REGULATION
- 16 Patent #: 7756588 Issue Dt: 07/13/2010 Application #: 12346651 Filing Dt: 12/30/2008
 Publication #: US20090106066 Pub Dt: 04/23/2009
 Title: SYSTEM AND METHOD FOR AUTOMATING INTEGRATION OF SEMICONDUCTOR WORK IN PROCESS UPDATES
- 17 Patent #: 8355269 Issue Dt: 01/15/2013 Application #: 12592472 Filing Dt: 11/24/2009
 Title: PUSHED-RULE BIT CELLS WITH NEW FUNCTIONALITY
- 18 Patent #: 8433875 Issue Dt: 04/30/2013 Application #: 12711909 Filing Dt: 02/24/2010
 Publication #: US20110204932 Pub Dt: 08/25/2011
 Title: ASYNCHRONOUS SCHEME FOR CLOCK DOMAIN CROSSING
- 19 Patent #: 8446224 Issue Dt: 05/21/2013 Application #: 13181362 Filing Dt: 07/12/2011
 Publication #: US20120013408 Pub Dt: 01/19/2012
 Title: NETWORK OF TIGHTLY COUPLED PERFORMANCE MONITORS FOR DETERMINING THE MAXIMUM FREQUENCY OF OPERATION OF A SEMICONDUCTOR IC

Assignor1 ESILICON CORPORATION

Exec Dt: 08/10/2012

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ATTN: GLENN MARASIGAN
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Search Results as of: 08/25/2017 03:35 PM

If you have any comments or questions concerning the data displayed, contact PRD / Assignments at 571-272-3350, v.2.5
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Exhibit C
Trademarks
(see attached listing)

BN 30259293v1

TRADEMARKS

<u>Description</u>	<u>Registration Number</u>	<u>Application Number</u>	<u>Registration/ Application Date</u>
ESILICON		(PRC) 6,740,909	5/23/2008
ESILICON	(EU) 008,493,258		5/27/2008
ESILICON	(HK) 301,009,737		12/6/2007
ESILICON	(Israel) 208,418		12/9/2007
ESILICON	(Japan) 5,262,675	2007/122035	8/4/2008
ESILICON		(Taiwan) 87,000,320	1/3/2008
ESILICON	(US) 2,958,847	78/256197	7/19/2005
ESILICON (Child)	(US) 3,412,021	78/978826	4/15/2008
ESILICON (Chinese Characters)		(PRC) 6,740,930	5/23/2008
ESILICON (Chinese Characters)	(PRC) 301,135,340		6/10/2008
ESILICON (Parent)	(US) 3,591,594	78/177028	3/17/2009
ESILICON ACCESS	(US) 3,803,342		4/7/2008
SWITCHCORE & DESIGN	(US) 2887715	78/209698	
XPEDIUM	(US) 2887715	78/209698	9/21/2004
XPEDIUM2	(US) 2884234	78/423509	6/28/2005
XPEDIUM3	(US) 2885270	78/423511	7/5/2005
SWITCHCORE	(Madrid) 713327		1999-04-19
SWITCHCORE	(Singapore) T99/03887E		1999-04-20
SWITCHCORE	(Ireland) 219796	99/1316	1999-04-20
SWITCHCORE	(Sweden) 340834	99-00488	2000-09-29
SWITCHCORE, SWITCHCORE LOGO	(Japan) 4439010	11-34579	2000-12-08

SWITCHCORE, SWITCHCORE LOGO	(South Korea) 475815	89-12829	2000-08-22
SWITCHCORE, SWITCHCORE LOGO	(Sweden) 340843	99-02988	2000-09-29
SWITCHCORE	(Taiwan) 054485	98017776	2001-08-16
SWITCHCORE	(Hong Kong) 2000B14824	99/04920	2000-11-03