

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM514595

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	SECURITY INTEREST		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
ESILICON CORPORATION		03/15/2019	Corporation: DELAWARE
RECEIVING PARTY DATA			
Name:	SILICON VALLEY BANK		
Street Address:	505 Howard Street, Third Floor		
City:	San Francisco		
State/Country:	CALIFORNIA		
Postal Code:	94105		
Entity Type:	Corporation: CALIFORNIA		
PROPERTY NUMBERS Total: 1			
Property Type	Number	Word Mark	
Serial Number:	87950807	NEUASIC	
CORRESPONDENCE DATA			
Fax Number:	2136305788		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	213-891-5935		
Email:	hpanneck@buchalter.com		
Correspondent Name:	Helen Panneck		
Address Line 1:	1000 Wilshire Boulevard, Suite 1500		
Address Line 4:	Los Angeles, CALIFORNIA 90017		
ATTORNEY DOCKET NUMBER:	S4137-0145		
NAME OF SUBMITTER:	Helen Panneck		
SIGNATURE:	/Helen Panneck/		
DATE SIGNED:	03/15/2019		
Total Attachments: 4			
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SUPPLEMENT NO. 2 TO
INTELLECTUAL PROPERTY SECURITY AGREEMENT

March 15, 2019

This SUPPLEMENT NO. 2 TO INTELLECTUAL PROPERTY SECURITY AGREEMENT (this "Supplement") supplements and shall be considered a part of that certain Intellectual Property Security Agreement (the "Agreement"), entered into between ESILICON CORPORATION ("Grantor") and SILICON VALLEY BANK ("Secured Party"), which was filed and recorded in the Patent Division of the United States Patent and Trademark Office (the "USPTO") on or about August 14, 2012, in Reel 28782, Frame 0322 and filed and recorded in the Trademark Division of the USPTO on or about August 14, 2012, in Reel 4841, Frame 0896. Grantor and Secured Party agree as follows:

1. Exhibit B is hereby supplemented by adding those certain patents, patent registrations and patent applications described on Exhibit B-1 attached hereto to the list of patents set forth in that exhibit.
2. Exhibit C is hereby supplemented by adding those certain trademarks, trademark registrations and trademark applications described on Exhibit C-1 attached hereto to the list of trademarks set forth in that exhibit.
3. Except as expressly supplemented or modified pursuant to this Supplement, the Agreement shall remain otherwise unchanged and in full force and effect.

[Signature page follows]

IN WITNESS WHEREOF, Grantor has executed this Supplement as of the date herein referenced above.

ESILICON CORPORATION,
a Delaware corporation

By: 
Name: CORY GARTIN
Title: VP and GENERAL COUNSEL

SUPPLEMENT NO. 2 TO
INTELLECTUAL PROPERTY SECURITY AGREEMENT

TRADEMARK
REEL: 006592 FRAME: 0818

Exhibit B-1

Patents

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
DUO CONTENT ADDRESSABLE MEMORY (CAM) USING A SINGLE CAM	10032516	July 24, 2018
Wireless Probes	10018670	July 10, 2018
Elongated Pad Structure	10050003	August 14, 2018
Communication Interface Architecture Using Serializer/Deserializer	9984997	May 29, 2018
Analog Baseline Wander Compensation for ADC based Serdes	16/270,512	February 7, 2019
Tuning Range for Voltage Controlled Oscillators	16/273,047	February 6, 2019
Successive Approximation Register (SAR) Analog to Digital Converter (ADC) with Partial Loop-Unrolling	16/239,421	January 3, 2019
Inductor Design for Electromagnetic Coupling Reduction	16/229,825	December 21, 2018
Bandgap Circuits with Voltage Calibration	16/222,929	January 14, 2019
Trans-impedance Amplifier (TIA) with a T-coil Feedback Loop	16/298,945	March 11, 2019

Exhibit C-1

Trademarks

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
NEUASIC	87950807	June 6, 2018