

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM525906

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
Bishopsgate Holdings Corporation		05/15/2019	Corporation: DELAWARE
RECEIVING PARTY DATA			
Name:	GEO SEMICONDUCTOR INC.		
Street Address:	101 Metro Drive		
Internal Address:	Suite 620		
City:	San Jose		
State/Country:	CALIFORNIA		
Postal Code:	95110		
Entity Type:	Corporation: DELAWARE		
PROPERTY NUMBERS Total: 6			
Property Type	Number	Word Mark	
Serial Number:	85903196	AUTOCAL	
Serial Number:	85391156	GEO	
Serial Number:	85391047	EWARP	
Serial Number:	85391040	GEO	
Serial Number:	78605942	VERICALL EDGE	
Serial Number:	78369523	REALTA	
CORRESPONDENCE DATA			
Fax Number:	2022987570		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	202-625-3538		
Email:	allen.rose@kattenlaw.com		
Correspondent Name:	Katten Muchin Rosenmann LLP / Allen Rose		
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ATTORNEY DOCKET NUMBER:	391708-00004		
NAME OF SUBMITTER:	Allen Rose		

CH \$165.00 85903196

SIGNATURE:	/allen rose/
DATE SIGNED:	05/31/2019
Total Attachments: 8 source=executed-Bishopsgate-IP-Security-Agreement-Termination#page1.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page2.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page3.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page4.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page5.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page6.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page7.tif source=executed-Bishopsgate-IP-Security-Agreement-Termination#page8.tif	

**TERMINATION AND RELEASE OF
INTELLECTUAL PROPERTY SECURITY AGREEMENT**

This Termination and Release of Intellectual Property Security Agreement (this "**Release**") is made as of May 15, 2019, by Bishopsgate Holdings Corporation (the "**Grantee**"), in favor of **GEO SEMICONDUCTOR INC.**, a Delaware corporation (the "**Grantor**").

WHEREAS, pursuant to the Intellectual Property Security Agreement, dated as of August 9, 2013, as amended, made by the Grantor in favor of the Grantee and the Intellectual Property Security Agreement dated as of May 10, 2012 (collectively, the "**Security Agreement**") the Grantor granted to the Grantee a security interest in and lien on (a) the patents and patent applications identified on Schedule 1 attached hereto; and (b) the trademark registrations and applications identified on Schedule 2 attached hereto (together with all other intellectual property Collateral (as defined in the Security Agreement), collectively the "**Collateral**").

WHEREAS, the Grantee wishes to release the security interest in and lien on the Collateral.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are acknowledged, each Grantee states as follow.

The Grantee terminates, releases, and discharges its security interest in and lien on the Collateral and assigns and transfers to the Grantor, without recourse or representation or warranty, express or implied, of any kind, all of its right, title, and interest in and to the Collateral together with the goodwill of the business symbolized thereby.

The Grantee understands that this Release may be recorded by the Grantor with the United States Copyright Office and the United States Patent and Trademark Office or any similar office or agency within or outside the United States. The Grantee shall, at the Grantor's sole expense, take all further actions reasonably requested in writing by the Grantor to effectuate the purposes of this Release.

IN WITNESS WHEREOF, the Grantee has executed this Release as of the date set forth above.

GRANTEE:

BISHOPSGATE HOLDINGS CORPORATION

By: _____

Name: Rocco Schiralli

Title: President

**Schedule 1
Patents**

Patent Registrations Description	Patent No.	Registration Date
Ultra-Thin Image Projection System	7714943	1/11/2010
Single-Pass Image Resampling System and Method with Anisotropic Filtering	7064770	6/20/2006
Single-Pass Image Warping System and Method with Anisotropic Filtering	7064770	1/20/2006
Cache Memory Management System and Method	7589738	9/15/2009
Detection of Progressive Frames in a Video Field Sequence	6563550	5/13/2003
Processing Sequential Video Images to Detect Image Motion Among Interlaced Video Fields or Progressive Video Images	6532264	3/11/2003
Parallel Data Processor	5421019	5/30/1995
Real-Time Image Processor	5606707	2/25/1997
Methods and Apparatus for Performing Fast Floating Point Operations	5966085	10/12/1999
Pattern Generation and Shift Plane Operations for a Mesh Connected Computer	6067609	5/23/2000
Parallel Data Processor	6073185	6/6/2000
Interface for Processing Element Array	6085304	7/4/2000
Methods and Apparatus for Performing Fast Division Operations in Bit-Serial Processors	6138137	10/24/2000
Data Flow Integrated Architecture	6145073	11/7/2000
Methods and Apparatus for Performing Fast Multiplication Operations in Bit-Serial Processors	6167421	12/26/2000
Directly Accessing Local Memories of Array Processors for Improved Real-Time Corner Turning Processing	6173388	1/9/2001

Input/Output Support for Processing in a Mesh Connected Computer	6185667	2/6/2001
Mesh Connected Computer	6212628	4/3/2001
Mesh Connected Computer	6275920	8/14/2001
System and Method for Performing Optimized Quantization Via Quantization Re-Scaling	8031768	10/4/2001
Distribution, Processing and Reconstruction of Variable-Sized Images Using Multiple Processor Arrays	6425026	7/23/2002
Method for Measuring and Analyzing Digital Video Quality	6577764	6/10/2003
Automatic Keystone Correction System and Method	6877863	4/12/2005
Multithreaded Data/Context Flow Processing Architecture	6889310	5/3/2005
Multithreaded Data / Context Flow Processing Architecture	6889310	5/3/2005
Method and System for Processing Two-Dimensional Image Data	6917363	7/12/2005
System, Software and Method for Implementing and Integrated, Device Independent, Packet Telephony Framework Software Solution	6985480	1/10/2006
System, Software and Method for Implementing an Integrated, Device Independent, Packet Telephony Framework Solution	6985480	1/10/2006
Automatic Code Generation for Integrated Circuit Design	6996799	2/7/2006
Random Access Memory Controller with Out of Order Execution	7093094	8/15/2006
Method and System for Processing a Non-Linear Two Dimensional Spatial Transformation	7126616	10/24/2006
Method and Apparatus for Entropy Coding	7199735	4/3/2007
Short Throw Projection System and Method	7239360	7/3/2007
Random Access Memory Controller with Out of Order Execution	7281110	10/9/2007

Method and Apparatus for Entropy Coding	7281771	10/16/2007
System and Method for Representing a General Two Dimensional Spatial Transformation	7324706	1/29/2008
System and Method for Correcting Multiple Axis Displacement Distortion	7352913	4/1/2008
Dynamic Warp Map Generation System and Method	7359575	4/15/2008
Image Projection System and Method	7384158	6/10/2008
System and Method for Electronic Correction of Optical Anomalies	7474799	1/6/2009
Method and Apparatus for Reading and Writing Pizel-Aligned Subframes in a Frame Buffer	7564462	7/21/2009
Method and Apparatus for Reading and Writing Pixel-Aligned Subframes in a Frame Buffer	7564462	7/21/2009
Hardware Multithreading Systems and Methods	7573481	8/11/2009
Method and Apparatus for Management of Bit Plane Resources	7573481	8/11/2009
Panoramic Vision System and Method	7576767	8/18/2009
Method and/or Apparatus for Detecting Edges of Blocks in an Image Processing System	7590288	9/15/2009
Method and Apparatus for High Density Storage and Handling of Bit-Plane Data	7593016	9/22/2009
System and Method for Generating a Fixed Point Approximation to Nonlinear Functions	7657589	2/2/2010
Method and Apparatus for Image Decoder Post-Processing Using Image Pre-Processing and Image Encoding Information	7684626	3/23/2010
Method and/or Apparatus for Detecting Homogeneity of Blocks in an Image Processing System	7715652	5/11/2010
Method and/or Architecture for Controlling Encoding Parameters Using Integrated Information from Camera ISP	7733380	6/8/2010
Method and Apparatus for Dependent Coding in Low-Delay Video Compression	7734106	6/8/2010
Hardware Multithreading Systems and Methods	7765547	7/27/2010

Hardware Multithreading Systems with State Registers Having Thread Profiling Data	7765547	7/27/2010
System and Method for Transform Coding Randomization	7778476	8/17/2010
Integrated Camera Image Signal Processor and Video Encoder	7859574	12/28/2010
Iteration Based Method and/or Apparatus for Offline High Quality Encoding of Multimedia Content	7953147	5/31/2011
Color and Geometry Distortion Correction System and Method	8055070	11/8/2011
Video Encoding Mode Decisions According to Content Categories	8081682	12/20/2011
Method and Apparatus of Adaptive Lambda Estimation in Lagrangian Rate-Distortion Optimization for Video Coding	8094716	1/10/2012
System and Method for Lens Performance Optimization Using Electronic Aberration Correction	8116008	2/14/2012
Method for Lens Performance Optimization Using Electronic Aberration Correction	8116008	2/14/2012
Video Encoding Statistics Extraction Using Non-Exclusive Content Categories	8126283	2/28/2012
Method and Apparatus for QP Modulation Based on Perceptual Models for Picture Encoding	8135062	3/13/2012
Method and/or Architecture for Motion Estimation using Integrated Information from Camera ISP	8135068	3/13/2012
Video Encoding Control Using Non-Exclusive Content Categories	8149909	4/3/2012
Method and/or Apparatus for Multiple Pass Digital Image Stabilization	8149911	4/3/2012
Memory Word Array Organization and Prediction Combination for Memory Access	8165219	4/24/2012
Method and Apparatus for Implementing Rate Distortion Optimization in Video Compression	8175150	5/8/2012
Systems and Methods for Adaptive Echo Cancellation	8175261	5/8/2012
Method, System and Device for Improving Video Quality Through In-Loop Temporal Pre-Filtering	8218655	7/10/2012
Method and/or Architecture for Interlaced Video Resampling and Color Format Conversion Using Combined Vertical-	8253856	8/28/2012

Temporal Resolution Extensions		
Method and Apparatus for Applying Accurate Parameter Estimation and Efficient Fade Detection in Weighted Prediction for Video Compression	8270483	9/18/2012
High Quality Low Bitrate Video Coding by Enabling and Disabling a Non-Residual Mode	8306118	11/6/2012
Method and Apparatus for Modeling Quantization Matrices for Image/Video Encoding	8326068	12/4/2012
Adaptive Motion Estimation Cache Organization	8325798	12/4/2012
Multi-Service Cellular Phone Network Access Device	8326349	12/4/2012
System and Method of Reduced-Temporal-Resolution Update for video Coding and Quality Control	8325797	12/4/2012
System and Method for Generating a Fixed Point Approximation to Nonlinear Functions	8356066	1/15/2013
System and Method for Automated Calibration and Correction of Display Geometry and Color	8406562	3/26/2013
System and Method for Improving Color and Brightness Uniformity of Backlit LCD Displays	8442316	5/14/2013
Method and Apparatus for Frame, Field and Macroblock Adaptive Progressive/Interface Coding Selection	8483268	7/9/2013
System and Method for Calibrating Ultra Wide-Angle Lenses	8619248	12/31/2013
Presence and/or Capability Based Communication	8625766	1/7/2014
Hardware Multithreading Systems and Methods	8640129	1/28/2014
Method and/or Apparatus for Optimized Video Coding	8687699	4/1/2014
Memory Word Array Organization and Prediction Combination for Memory Access	8687706	4/1/2014
System and Method for Automated Calibration and Correction of Display Geometry and Color	8768094	7/1/2014
Video Quality by Controlling Inter Frame Encoding According to Frame Position in GOP	8774272	7/8/2014

Skipped Video Data Recovery Using Multiple Alternative Recovery Modes	8811495	8/10/2014
Integrated Camera Image Signal Processor and Video Encoder	8803995	8/12/2014
I-Frame Size Estimation Based on Edge Strength	8804822	8/12/2014
Video Encoding Control Using Non-Exclusive Content Categories	8831093	9/9/2014
Method and Apparatus for Selecting Optimal Video Encoding Parameter Configurations	8831089	9/9/2014
Folded SIMD Array Organized in Groups (PEGs) of Respective Array Segments, Control Signal Distribution Logic, and Local Memory	8898432	11/25/2014
Method and System for Folding a SIMD Array	8898432	11/25/2014
Method and/or Apparatus for Multiple Pass Digital Image Stabilization	8923400	12/20/2014
Block and Mode Reordering to Facilitate Parallel Intra-Prediction and Motion Vector Prediction	8976870	3/10/2015
Methods and/or Apparatus for Controlling Zero-Residual Coding in Predictive Image/Video Coding	9036712	5/19/2015
Method and Apparatus for QP Modulation Based on Perceptual Models for Picture Encoding	9282336	3/8/2016
Method and System for Adaptive Perspective Correction of Ultra Wide-Angle Lens Images	9998659	6/12/2018

Patent Applications

Description	Application No.	Application Date
Method and/or Architecture for Motion Estimation Using Integrated Information from Camera ISP	13419281	3/13/2012
Designer-Style Dimmer Apparatus and Method	12027189	2/6/2008
Reduced Field Angle Projection Display System	11331043	1/13/2006

**Schedule 3
Trademarks**

Serial Number	Reg. Number	Word Mark	Word Only	Filing Date	Status	Effective Date
85903196	4,480,188	AUTOCAL	Word Only	12-Apr-13	Registered	2/11/2014
85391156	4125415	GEO	Mark (color)	5-Aug-11	Registered	4/10/2012
85391047	4352340	EWARP	Word Only	5-Aug-11	Registered	6/18/2013
85391040	4125410	GEO	Mark (B&W)	5-Aug-11	Registered	4/10/2012
78605942	3205074	VERICALL EDGE	Word Only	11-Apr-05	Dead	9/13/2013
78369523	3002865	REALTA	Word Only	17-Feb-02	Registered	9/25/2005