

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM558057

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
SILICON VALLEY BANK		01/15/2020	Corporation: CALIFORNIA
RECEIVING PARTY DATA			
Name:	INEDA SYSTEMS, INC.		
Street Address:	3400 GARRETT DRIVE		
City:	SANTA CLARA		
State/Country:	CALIFORNIA		
Postal Code:	95054		
Entity Type:	Corporation: DELAWARE		
PROPERTY NUMBERS Total: 4			
Property Type	Number	Word Mark	
Serial Number:	86792822	IOT PROCESSING UNIT (IPU)	
Serial Number:	86259042	INEDA	
Serial Number:	86259015	INEDA SYSTEMS	
Serial Number:	86232461	WEARABLE PROCESSING UNIT (WPU)	
CORRESPONDENCE DATA			
Fax Number:	8585506420		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	858-550-6472		
Email:	dmonteblanco@cooley.com		
Correspondent Name:	DEREK MONTEBLANCO		
Address Line 1:	C/O COOLEY LLP		
Address Line 2:	4401 EASTGATE MALL		
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ATTORNEY DOCKET NUMBER:	194491-1685		
NAME OF SUBMITTER:	DEREK MONTEBLANCO		
SIGNATURE:	/DEREK MONTEBLANCO/		
DATE SIGNED:	01/17/2020		
Total Attachments: 3			

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RELEASE OF SECURITY INTEREST

This Release of Security Interest is made as of January 15, 2020, by SILICON VALLEY BANK, a California corporation ("Bank") in favor of INEDA SYSTEMS, INC., a Delaware corporation ("Grantor") with its principal place of business located at 3400 Garrett Drive, Santa Clara, CA 95054.

Recital

WHEREAS Grantor granted to Bank a security interest in the patents and trademarks described on Exhibits A - B respectively, attached hereto (collectively, the "Intellectual Property Collateral") under an Intellectual Property Security Agreement dated as of January 31, 2017, by and between Grantor and Bank, as amended from time to time (the "Security Agreement") and recorded with the US Patent and Trademark Office as set forth on Exhibits A - B.

WHEREAS Grantor has no outstanding obligations to Bank under the terms of the Security Agreement, Bank agrees to release its security interest in the Intellectual Property Collateral.


Agreement

Now therefore, Bank agrees that it terminates and releases its security interest in the Intellectual Property Collateral and reassigns to Grantor, without warranty or recourse, all interest of Bank in the Intellectual Property Collateral.

Recordation. Bank authorizes the Commissioner for Patents, the Commissioner for Trademarks and the Register of Copyrights and any other government officials to record and register this Agreement up request by Grantor.

BANK:

SILICON VALLEY BANK


Name: Bellet Eliasnia
Title: Director - Advisory Services

3003 Tasman Drive
Santa Clara, CA 95054-1191

EXHIBIT A

PATENTS

Atty Docket	Title	Appl. No.	Patent No.	Filing or 371(c) Date
19974.1	Multi-Host Ethernet Controller	14/112410	9590920	6/5/2014
19974.2	Wireless Interface Sharing	14/112401	9918270	10/17/2013
19974.3	Secure Digital Host Controller Virtualization	14/112393	9432446	10/17/2013
19974.4	Optimized Multi-Root Input Output Virtualization Aware Switch	14/112386	9430432	10/17/2013
19974.5	Multi-Root Peripheral Connect Interface Manager	14/112379	9772858	10/17/2013
19974.6	Peripheral Device Sharing in Multi Host Computing Systems	14/114743	8990459	10/29/2013
19974.7	Systems and Method for Providing Hierarchical Computing in Wearable Processing Units	14/285532	9430028	5/22/2014
19974.8	Sharing Single Root IO Virtualization Peripheral Component Interconnect Express Devices	14/285550	9268717	5/22/2014
19974.9	Virtualized Storage Systems	14/285561	9740409	5/22/2014
19974.10	Network Interface Sharing	14/285578	9772968	5/22/2014
19974.12	I/O Virtualization and Switching System	13/817822	8996734	2/19/2013
19974.13	Application Sharing in Multi Host Computing Systems	14/111023	9940283	10/10/2013
19974.14	File System Sharing	14/111058	9836478	10/10/2013
19974.15	Audio Controller	14/111071	9086843	10/10/2013
19974.16	Multi-Host Sata Controller	14/111379	9189166	10/11/2013
19974.17	USB Virtualization	14/111404	8972624	10/11/2013
19974.18	Low Pin Count Controller	14/111432	9047264	10/11/2013
19974.19	Multi-Processor Electronic Systems	13/817815	8938568	2/19/2013
19974.22	Portable Electronic Device	15/052811	10,146,913	2/24/2016

Bank's security interest recorded at the US Patent and Trademark Office on February 2, 2017 at Reel Number 041158 and Frame Number 0665.

EXHIBIT B

Trademarks

Description	Registration/ Serial Number	Registration/ Application Date
IOT PROCESSING UNIT (IPU)	86/792,822	10/20/15
INEDA	86/259,042	04/22/14
INEDA SYSTEMS	86/259,015	04/22/14
WEARABLE PROCESSING UNIT (WPU)	86/232,461	03/26/14

Bank's security interest recorded at the US Patent and Trademark Office on February 2, 2017 at Reel Number 5977 and Frame Number 0601.