

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM707195

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|---|---|-----------------------|---------------------|
| <b>SUBMISSION TYPE:</b>   | NEW ASSIGNMENT  |                       |                     |
| <b>NATURE OF CONVEYANCE:</b>  | RELEASE OF SECURITY INTEREST  |                       |                     |
| <b>CONVEYING PARTY DATA</b>   |   |                       |                     |
| <b>Name</b>   | <b>Formerly</b>   | <b>Execution Date</b> | <b>Entity Type</b>  |
| BDC Capital Inc.  |   | 02/08/2022            | Corporation: CANADA |
| <b>RECEIVING PARTY DATA</b>   |   |                       |                     |
| <b>Name:</b>  | D-Wave Systems Inc.   |                       |                     |
| <b>Street Address:</b>  | 3033 Beta Avenue,   |                       |                     |
| <b>City:</b>  | Burnaby   |                       |                     |
| <b>State/Country:</b>   | CANADA  |                       |                     |
| <b>Postal Code:</b>   | V5G 4M9   |                       |                     |
| <b>Entity Type:</b>   | Corporation: CANADA   |                       |                     |
| <b>PROPERTY NUMBERS Total: 4</b>  |   |                       |                     |
| <b>Property Type</b>  | <b>Number</b>   | <b>Word Mark</b>      |                     |
| <b>Serial Number:</b>   | 77368747  | D-WAVE                |                     |
| <b>Serial Number:</b>   | 88186210  | D-WAVE                |                     |
| <b>Serial Number:</b>   | 88186228  | D-WAVE                |                     |
| <b>Serial Number:</b>   | 88027287  | LEAP                  |                     |
| <b>CORRESPONDENCE DATA</b>  |   |                       |                     |
| <b>Fax Number:</b>  | 6462193046  |                       |                     |
| <i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i> |   |                       |                     |
| <b>Phone:</b>   | 2123732594  |                       |                     |
| <b>Email:</b>   | rlyne@paulweiss.com, mangelopoulos@paulweiss.com,<br>mmcguire@paulweiss.com |                       |                     |
| <b>Correspondent Name:</b>  | Rebecca B. Lyne   |                       |                     |
| <b>Address Line 1:</b>  | 1285 Avenue of the Americas   |                       |                     |
| <b>Address Line 2:</b>  | Paul Weiss Rifkind Wharton & Garrison LLP                                   |                       |                     |
| <b>Address Line 4:</b>  | New York, NEW YORK 10019  |                       |                     |
| <b>ATTORNEY DOCKET NUMBER:</b>  | 021641-00002  |                       |                     |
| <b>NAME OF SUBMITTER:</b>   | Rebecca B. Lyne   |                       |                     |
| <b>SIGNATURE:</b>   | /s/ Rebecca Lyne  |                       |                     |
| <b>DATE SIGNED:</b>   | 02/08/2022  |                       |                     |
| <b>Total Attachments: 14</b>  |   |                       |                     |

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**NOTICE OF RELEASE OF  
SECURITY INTEREST IN INTELLECTUAL PROPERTY**

This NOTICE OF RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY (this "Release"), effective as of February 8, 2022 is made by BDC Capital Inc., in its capacity as a security agent for itself and on behalf of the other Lenders (referred to herein as the "Secured Party"), in favor of D-Wave Systems Inc., a corporation incorporated under the Canada Business Corporations Act (the "Company"), pursuant to that certain secured convertible note purchase agreement, dated November 19, 2019 (and as the same may be further amended or modified from time to time, the "NPA"), among the Company, the Secured Party and other parties thereto.

**WITNESSETH:**

WHEREAS, in connection with the NPA, the Company executed and delivered the Intellectual Property Security Agreement, dated as of November 19, 2019, in favor of the Secured Party (the "IPSA");

WHEREAS, pursuant to the IPSA, the Company pledged and granted to the Secured Party for the benefit of the other Lenders a lien on and continuing security interest in all of its intellectual property, including but not limited to the: (i) trademarks, service marks and trademark and service mark registrations and applications set forth on Schedule A attached hereto, together with the goodwill of the business symbolized thereby, (ii) the patents and patent applications set forth on Schedule B, and the copyright registrations and applications set forth on Schedule C (the "Released Collateral");

WHEREAS, the Secured Party recorded its security interest in the Released Collateral with the United States Patent and Trademark Office (the "USPTO") on November 29, 2019, at Reel/Frame No. 6805/0825; and

WHEREAS, the Secured Party has duly authorized the execution, delivery and performance of this Release.

NOW THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Secured Party agrees, for the benefit of the Company, as follows:

SECTION 1. Definitions. Unless otherwise defined herein or the context otherwise requires, terms used in this Release, including its preamble and recitals, have the meanings provided or provided by reference in the NPA.

SECTION 2. Release of Security Interest. The Secured Party does hereby release, relinquish and discharge its lien on and security interest in, and right of setoff against, all of the Company's right, title and interest in, to and under the Released Collateral.

SECTION 3. Termination. The Secured Party, without any recourse, representation or warranty, hereby terminates and cancels the IPSA.

SECTION 4. Further Assurances. The Secured Party hereby authorizes the Company or the Company's authorized representative to (i) record this Release with the USPTO

and the United States Copyright Office, as applicable, (ii) file UCC financing statement amendments with the applicable filing office in order to memorialize the release of the security interest of the Secured Party in the Released Collateral and/or (iii) otherwise record or file this Release in the applicable governmental office or agency. The Secured Party further agrees to execute and deliver to the Company any and all further documents and instruments, and do any and all further acts which the Company (or their agents or designees) reasonably request (at the Company's sole cost and expense) in order to confirm this Release and the Company's right, title and interest in, to and under the Released Collateral.

SECTION 5. Choice of Law. This Release shall be governed by, and construed in accordance with, the laws of the state of New York, but giving effect to federal laws applicable to national banks.

SECTION 6. Counterparts. This Release may be executed in counterparts, each of which will be deemed an original, but all of which together constitute one and the same original.

*[Signatures Follow on Next Page.]*

IN WITNESS WHEREOF, the Secured Party has caused this Release to be duly executed and delivered by its officer thereunto duly authorized as of the date set forth above.

BDC Capital Inc.,  
as Secured Party

DocuSigned by:  
*Geoff Catherwood*  
By: \_\_\_\_\_  
Name: Geoff Catherwood  
Title: Partner, ICE Venture Fund

DocuSigned by:  
*Sean Brownlee*  
By: \_\_\_\_\_  
Name: Sean Brownlee  
Title: Partner - BDC ICE Fund

**SCHEDULE A****U.S. Trademark Applications and Registrations**

| Mark                | Application No./<br>Registration No. | Application Date | Registration Date | Status     |
|---------------------|--------------------------------------|------------------|-------------------|------------|
| "D-Wave" (Wordmark) | 77368747 /<br>4708470                | 10-Jan-08        | 24-Mar-15         | Registered |
| "D-Wave" (Wordmark) | 88186210                             | 8-Nov-2018       | N/A               | Pending    |
| "D-Wave" (Wordmark) | 88186228                             | 8-Nov-2018       | N/A               | Pending    |
| "LEAP" (Wordmark)   | 88027287                             | 05-Jul-2018      | N/A               | Pending    |

**SCHEDULE B****U.S. Patents and Patent Applications**

|     | Patent Title  | Serial No. | Patent No.   | Issue Date  |
|-----|---|------------|--------------|-------------|
| 1.  | Quantum Computing Method Using Magnetic Flux States at a Josephson Junction                                 | 09/855,817 | US 6,563,311 | 2003-May-13 |
| 2.  | Qubit Using a Josephson Junction Between S-wave and D-wave Superconductors                                  | 09/479,336 | US 6,459,097 | 2002-Oct-1  |
| 3.  | Quantum Computing Method Using Josephson Junction between S-Wave and D-wave Superconductors                 | 09/855,487 | US 6,563,310 | 2003-May-13 |
| 4.  | Shaped Josephson Junction Qubits  | 09/637,514 | US 6,627,915 | 2003-Sep-30 |
| 5.  | Superconducting DOT/Anti-DOT Flux Qubit Based on Time-Reversal Symmetry Breaking Effects                    | 09/810,818 | US 6,504,172 | 2003-Jan-07 |
| 6.  | Method of Forming Superconducting DOT/Anti-DOT Flux Qubits Based on Time-Reversal Symmetry Breaking Effects | 10/058,181 | US 6,537,847 | 2003-Mar-25 |
| 7.  | Quantum Bit with a Multi-Terminal Junction and Loop with a Phase Shift                                      | 09/839,636 | US 6,987,282 | 2006-Jan-17 |
| 8.  | Quantum Bit with a Multi-Terminal Junction and Loop with a Phase Shift                                      | 09/839,637 | US 6,919,579 | 2005-Jul-19 |
| 9.  | High Sensitivity, Directional DC-Squid Magnetometer   | 09/823,895 | US 6,627,916 | 2003-Sep-30 |
| 10. | High Sensitivity, Directional DC-Squid Magnetometer   | 10/192,623 | US 6,905,887 | 2005-Jun-14 |

|     | Patent Title   | Serial No. | Patent No.   | Issue Date  |
|-----|--|------------|--------------|-------------|
| 11. | Method of Fluxon Injection into Annular Josephson Junction           | 10/117,696 | US 6,728,131 | 2004-Apr-27 |
| 12. | Four Terminal Readout System for Reading the State of a Phase Qubit. | 09/875,776 | US 6,580,102 | 2003-Jun-17 |
| 13. | Four Terminal Readout System for Reading the State of a Phase Qubit. | 10/155,746 | US 6,573,202 | 2003-Jun-03 |
| 14. | Four Terminal Readout System for Reading the State of a Phase Qubit  | 10/194,704 | US 6,576,951 | 2003-Jun-10 |
| 15. | Quantum Processing System for a Superconducting Qubit                | 09/872,495 | US 6,803,599 | 2004-Oct-12 |
| 16. | Methods for Controlling Qubits                                       | 10/791,617 | US 6,936,841 | 2005-Aug-30 |
| 17. | Superconducting Low Inductance Qubit                                 | 10/232,136 | US 6,979,836 | 2005-Dec-27 |
| 18. | Trilayer Heterostructure Josephson Junctions                         | 10/231,385 | US 6,753,546 | 2004-Jun-22 |
| 19. | Finger Squid Qubit Device  | 10/025,848 | US 6,614,047 | 2003-Sep-02 |
| 20. | Finger Squid Qubit Device  | 10/025,818 | US 6,791,109 | 2004-Sep-14 |
| 21. | Finger Squid Qubit Device  | 10/351,631 | US 6,812,484 | 2004-Nov-02 |
| 22. | Finger Squid Qubit Device  | 10/351,632 | US 6,822,255 | 2004-Nov-23 |
| 23. | Characterization and Measurement of Superconducting Structures       | 10/321,065 | US 7,002,174 | 2006-Feb-21 |
| 24. | Quantum Phase-Charge Coupled Device                                  | 10/121,817 | US 7,332,738 | 2008-Feb-19 |
| 25. | Quantum Phase-Charge Coupled Device                                  | 10/121,810 | US 6,605,822 | 2003-Aug-12 |
| 26. | Quantum Phase-Charge Coupled Device                                  | 10/121,800 | US 6,670,630 | 2003-Dec-30 |
| 27. | Multi-Junction Phase Qubit   | 10/321,941 | US 6,784,451 | 2004-Aug-31 |
| 28. | Extra-Substrate Control System                                       | 10/746,992 | US 7,042,005 | 2006-May-09 |
| 29. | Extra Substrate Control System                                       | 10/134,665 | US 6,911,664 | 2005-Jun-28 |
| 30. | Sub-Flux Quantum Generator   | 10/445,096 | US 6,885,325 | 2005-Apr-26 |
| 31. | Resonant Controlled Qubit System                                     | 10/419,024 | US 6,900,454 | 2005-May-31 |
| 32. | Resonant Controlled Qubit System                                     | 10/801,335 | US 6,897,468 | 2005-May-24 |

|     | Patent Title   | Serial No. | Patent No.    | Issue Date  |
|-----|--|------------|---------------|-------------|
| 33. | Resonant Controlled Qubit System   | 10/801,340 | US 6,900,456  | 2005-May-31 |
| 34. | Resonant Controlled Qubit System   | 10/798,737 | US 6,930,320  | 2005-Aug-16 |
| 35. | Resonant Controlled Qubit System   | 10/801,336 | US 6,960,780  | 2005-Nov-01 |
| 36. | Quantum Logic Using Three Energy Levels                                    | 10/719,925 | US 6,943,368  | 2005-Sep-13 |
| 37. | Conditional Rabi Oscillation Readout for Quantum Computing                 | 10/845,638 | US 7,230,266  | 2007-Jun-12 |
| 38. | Superconducting Phase-Charge Qubits  | 10/934,049 | US 7,335,909  | 2008-Feb-26 |
| 39. | Adiabatic Quantum Computation with Superconducting Qubits                  | 12/845,352 | US 8,504,497  | 2013-Aug-06 |
| 40. | Adiabatic Quantum Computation with Superconducting Qubits                  | 11/092,953 | US 7,418,283  | 2008-Aug-26 |
| 41. | Adiabatic Quantum Computation with Superconducting Qubits                  | 11/093,205 | US 7,135,701  | 2006-Nov-14 |
| 42. | Methods of Adiabatic Quantum Computation comprising of Hamiltonian scaling | 11/625,702 | US 7,788,192  | 2010-Aug-31 |
| 43. | Methods for Quantum Processing   | 11/089,650 | US 7,613,764  | 2009-Nov-03 |
| 44. | Bus Architectures for Quantum Processing                                   | 11/089,653 | US 7,613,765  | 2009-Nov-03 |
| 45. | Superconducting Qubits Having a Plurality of Capacitive Couplings          | 11/267,459 | US 7,253,654  | 2007-Aug-07 |
| 46. | A Superconducting Qubit with a Plurality of Capacitive Couplings           | 11/267,478 | US 7,268,576  | 2007-Sep-11 |
| 47. | Analog Processor Comprising Quantum Devices                                | 11/317,838 | US 7,533,068  | 2009-May-12 |
| 48. | Analog Processor Comprising Quantum Devices                                | 11/608,214 | US 7,624,088  | 2009-Nov-24 |
| 49. | Analog Processor Comprising Quantum Devices                                | 12/397,999 | US 8,008,942  | 2011-Aug-30 |
| 50. | Analog Processor Comprising Quantum Devices                                | 13/210,169 | US 8,283,943  | 2012-Oct-09 |
| 51. | Analog Processor Comprising Quantum Devices                                | 13/608,836 | US 8,686,751  | 2014-Apr-01 |
| 52. | Analog Processor Comprising Quantum Devices                                | 14/175,731 | US 9,069,928  | 2015-Jun-30 |
| 53. | Analog Processor Comprising Quantum Devices                                | 14/727,521 | US 9,727,527  | 2017-Aug-08 |
| 54. | Analog Processor Comprising Quantum Devices                                | 15/635,735 | US 10,140,248 | 2018-Nov-27 |

|     | Patent Title   | Serial No. | Patent No.   | Issue Date  |
|-----|--|------------|--------------|-------------|
| 55. | Coupling Methods and Architectures for Information Processing  | 11/247,857 | US 7,619,437 | 2009-Nov-17 |
| 56. | Coupling Methods and Architectures for Information Processing  | 12/575,345 | US 7,969,805 | 2011-Jun-28 |
| 57. | Qubit State Copying  | 11/411,051 | US 7,639,035 | 2009-Dec-29 |
| 58. | Systems, devices, and methods for controllably coupling qubits   | 12/618,554 | US 7,898,282 | 2011-Mar-01 |
| 59. | Method and System for Solving Integer Programming and Discrete Optimization Problems Using Analog Processors | 11/850,437 | US 7,877,333 | 2011-Jan-25 |
| 60. | Systems, Methods and Apparatus for Factoring Numbers   | 11/484,368 | US 7,844,656 | 2010-Nov-30 |
| 61. | Systems and Methods for Factoring Numbers  | 12/848,764 | US 8,386,554 | 2013-Feb-26 |
| 62. | Systems and Methods for Solving Computational Problems   | 12/849,588 | US 8,560,282 | 2013-Oct-15 |
| 63. | Graph Embedding Techniques   | 11/932,248 | US 7,984,012 | 2011-Jul-19 |
| 64. | Graph Embedding Techniques   | 13/156,172 | US 8,244,662 | 2012-Aug-14 |
| 65. | Systems, Devices and Method for Solving Computational Problems   | 11/765,361 | US 8,195,726 | 2012-Jun-05 |
| 66. | Systems, Devices and Method for Solving Computational Problems   | 13/462,494 | US 8,874,629 | 2014-Oct-28 |
| 67. | Systems, Methods and Apparatus for Quasi-Adiabatic Quantum Computation                                       | 11/777,910 | US 7,899,852 | 2011-Mar-01 |
| 68. | Processing Relational Database Problems Using Analog Processors  | 11/932,261 | US 7,870,087 | 2011-Jan-11 |
| 69. | Processing Relational Database Problems Using Analog Processors  | 12/946,643 | US 8,032,474 | 2011-Oct-04 |
| 70. | Systems, Devices and Methods for Controllably Coupling Qubits  | 12/238,147 | US 7,880,529 | 2011-Feb-01 |
| 71. | Superconducting Shielding for use with an Integrated Circuit for Quantum Computing                           | 11/948,817 | US 7,687,938 | 2010-Mar-30 |
| 72. | Superconducting Shielding for use with an Integrated Circuit for Quantum Computing                           | 12/703,534 | US 8,247,799 | 2012-Aug-21 |

|     | Patent Title   | Serial No. | Patent No.   | Issue Date  |
|-----|--|------------|--------------|-------------|
| 73. | Quantum Processor  | 12/194,282 | US 7,932,515 | 2011-Apr-26 |
| 74. | Systems, Methods and Apparatus for Digital-to-Analog Conversion of Superconducting Magnetic Flux Signals | 12/120,354 | US 8,098,179 | 2012-Jan-17 |
| 75. | Systems, Methods and Apparatus for Digital-to-Analog Conversion of Superconducting Magnetic Flux Signals | 13/325,785 | US 8,786,476 | 2014-Jul-22 |
| 76. | Systems, Methods and Apparatus for Programming Quantum Processor Elements                                | 11/950,276 | US 7,876,248 | 2011-Jan-25 |
| 77. | Systems Methods and Apparatus for Local Programming of Quantum Processor Elements                        | 12/944,509 | US 8,035,540 | 2011-Oct-11 |
| 78. | Systems Methods and Apparatus for Local Programming of Quantum Processor Elements                        | 13/228,219 | US 8,604,944 | 2013-Dec-10 |
| 79. | Architecture for Local Programming of Quantum Processor Elements using Latching Qubits                   | 12/109,847 | US 7,843,209 | 2010-Nov-30 |
| 80. | Adiabatic Superconducting Qubit Logic Devices and Methods  | 12/909,682 | US 8,018,244 | 2011-Sep-13 |
| 81. | Systems, Devices and Methods for Interconnected Processor Topology                                       | 12/013,192 | US 8,195,596 | 2012-Jun-05 |
| 82. | Input/Output System and Devices for use with Superconducting Devices                                     | 12/016,801 | US 8,441,329 | 2013-May-14 |
| 83. | Systems, Methods and Apparatus for Electrical Filters  | 12/016,709 | US 8,008,991 | 2011-Aug-30 |
| 84. | Systems, Devices, and Methods for Controllably Coupling Qubits   | 12/113,753 | US 7,800,395 | 2010-Sep-21 |
| 85. | Physical Realizations of a Universal Adiabatic Quantum Computer  | 12/098,348 | US 8,234,103 | 2012-Jul-31 |
| 86. | Systems, Methods and Apparatus for Anti-Symmetric Qubit-Coupling   | 12/098,347 | US 7,605,600 | 2009-Oct-20 |
| 87. | Physical Realizations of a Universal Adiabatic Quantum Computer  | 13/539,039 | US 9,162,881 | 2015-Oct-20 |
| 88. | Physical Realizations of a Universal Adiabatic Quantum Computer  | 14/806,087 | US 9,984,333 | 2018-May-29 |
| 89. | Systems, Methods and Apparatus for Automatic Image Recognition   | 12/106,024 | US 8,073,808 | 2011-Dec-6  |

|      | Patent Title   | Serial No. | Patent No.   | Issue Date  |
|------|--|------------|--------------|-------------|
| 90.  | Systems, Methods, and Apparatus for Solving Problems   | 13/284,418 | US 8,655,828 | 2014-Feb-18 |
| 91.  | Systems, Methods and Apparatus for Recursive Quantum Computing Algorithms  | 12/135,899 | US 8,244,650 | 2012-Aug-14 |
| 92.  | Systems, Methods, and Apparatus for Superconducting Magnetic Shielding   | 12/256,330 | US 7,990,662 | 2011-Aug-2  |
| 93.  | Systems, Methods, and Apparatus for Electrical Filters and Input/Output Systems  | 12/256,332 | US 8,159,313 | 2012-Apr-17 |
| 94.  | Systems, Methods, and Apparatus for Electrical Filters and Input/Output Systems  | 13/416,794 | US 8,405,468 | 2013-Mar-26 |
| 95.  | Systems, Methods and Apparatus for Controlling the Elements of Superconducting Processors  | 12/193,601 | US 8,670,807 | 2014-Mar-11 |
| 96.  | Systems, Methods and Apparatus for Controlling the Elements of Superconducting Processors  | 14/162,557 | US 9,699,266 | 2017-Jul-04 |
| 97.  | Systems, Methods, and Apparatus for Qubit State Readout  | 12/236,040 | US 8,169,231 | 2012-May-01 |
| 98.  | Systems, Methods and Apparatus for Adiabatic Quantum Computation and Quantum Annealing   | 12/473,970 | US 8,229,863 | 2012-Jul-24 |
| 99.  | Method and Apparatus for Evolving a Quantum System Using a Mixed Initial Hamiltonian Comprising Both Diagonal and Off-Diagonal Terms | 13/529,664 | US 8,560,470 | 2013-Oct-15 |
| 100. | Systems, Methods, and Apparatus for Multilayer Superconducting Printed Circuit Boards  | 12/247,475 | US 8,315,678 | 2012-Nov-20 |
| 101. | Systems, Devices and Methods for Analog Processing   | 12/266,378 | US 8,190,548 | 2012-May-29 |
| 102. | Magnetic Vacuum Systems and Devices for Use with Superconducting Based Computing Systems   | 12/865,341 | US 8,355,765 | 2013-Jan-15 |
| 103. | Systems, Methods, and Apparatus for Combined Superconducting Magnetic Shielding and Radiation Shielding                              | 12/262,417 | US 8,228,688 | 2012-Jul-24 |
| 104. | Systems, methods and apparatus for cryogenic refrigeration   | 12/811,067 | US 8,464,542 | 2013-Jun-18 |
| 105. | Systems, methods and apparatus for cryogenic refrigeration   | 13/863,218 | US 9,134,047 | 2015-Sep-15 |
| 106. | Systems, Devices and Methods for Controllably  | 12/242,133 | US 8,102,185 | 2012-Jan-24 |

|      | Patent Title  | Serial No. | Patent No.    | Issue Date  |
|------|---|------------|---------------|-------------|
|      | Coupling Qubits   |            |               |             |
| 107. | Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor | 12/991,888 | US 9,015,215  | 2015-Apr-21 |
| 108. | Systems and Devices for Quantum Processor Architecture  | 12/483,971 | US 8,063,657  | 2011-Nov-22 |
| 109. | System, Devices and Methods for Coupling Qubits   | 12/922,626 | US 8,174,305  | 2012-May-08 |
| 110. | Qubit Based Systems, Devices and Methods for Analog Processing                                | 12/934,254 | US 8,421,053  | 2013-Apr-16 |
| 111. | Systems, Devices and Methods for Analog Processing  | 13/611,672 | US 8,772,759  | 2014-Jul-8  |
| 112. | Superconducting Probe Card (as amended)   | 14/273,200 | US 9,170,278  | 2015-Oct-27 |
| 113. | Systems, Devices and Methods for Analog Processing  | 14/868,019 | US 9,406,026  | 2016-Aug-02 |
| 114. | Systems, Devices and Methods for Analog Processing  | 15/199,532 | US 9,779,360  | 2017-Oct-03 |
| 115. | Quantum and Digital Processor Hybrid Systems and Methods to Solve Problems                    | 12/945,717 | US 8,175,995  | 2012-May-08 |
| 116. | Systems, Methods and Apparatus for Active Compensation of Quantum Processor Elements          | 12/991,891 | US 8,536,566  | 2013-Sep-17 |
| 117. | Systems, Methods and Apparatus for Active Compensation of Quantum Processor Elements          | 13/958,339 | US 9,152,923  | 2015-Oct-06 |
| 118. | Systems, Methods and Apparatus for Active Compensation of Quantum Processor Elements          | 14/846,334 | US 9,607,270  | 2017-Mar-28 |
| 119. | Systems, Methods and Apparatus for Active Compensation of Quantum Processor Elements          | 15/438,296 | US 10,290,798 | 2019-May-14 |
| 120. | Systems, Methods and Apparatus for Superconducting Demultiplexer Circuits                     | 12/991,889 | US 8,611,974  | 2013-Dec-17 |
| 121. | Input/Output Systems and Devices for use with superconducting Devices                         | 12/503,671 | US 8,279,022  | 2012-Oct-02 |
| 122. | Input/Output Systems and Devices for use with superconducting Devices                         | 13/596,801 | US 9,231,181  | 2016-Jan-05 |
| 123. | Input/Output Systems and Devices for use with superconducting Devices                         | 14/959,846 | US 9,762,200  | 2017-Sep-12 |

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| 124. | Input/Output Systems and Devices for use with superconducting Devices                    | 15/672,506 | US 10,097,151 | 2018-Oct-09 |
| 125. | Systems and Methods for Fabrication of Superconducting Integrated Circuits               | 12/992,049 | US 8,951,808  | 2015-Feb-10 |
| 126. | Systems and Methods for Fabrication of Superconducting Integrated Circuits               | 14/589,574 | US 9,490,296  | 2016-Nov-08 |
| 127. | Systems and Methods for Fabrication of Superconducting Integrated Circuits               | 15/289,782 | US 9,978,809  | 2018-May-22 |
| 128. | Systems, Methods and Apparatus for Measuring Magnetic Fields                             | 12/991,893 | US 8,812,066  | 2014-Aug-19 |
| 129. | Systems, Methods and Apparatus for Measuring Magnetic Fields                             | 14/462,200 | US 9,335,385  | 2016-May-10 |
| 130. | Systems and Methods for Solving Computational Problems                                   | 12/992,047 | US 8,700,689  | 2014-Apr-15 |
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| 132. | Systems and Methods for Solving Computational Problems                                   | 15/190,608 | US 9,594,726  | 2017-Mar-14 |
| 133. | Systems and Methods for Solving Computational Problems                                   | 15/419,083 | US 9,665,539  | 2017-May-30 |
| 134. | Systems and Methods for Realizing Fault Tolerance in Physical Quantum Computing Hardware | 12/992,057 | US 8,494,993  | 2013-Jul-23 |
| 135. | Systems and Methods for Superconducting Integrated Circuits                              | 12/944,518 | US 8,738,105  | 2014-May-27 |
| 136. | Systems and Methods for Superconducting Integrated Circuits                              | 14/255,561 | US 9,355,365  | 2016-May-31 |
| 137. | Systems and Devices for Electrical Filters   | 13/011,697 | US 8,346,325  | 2013-Jan-1  |
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| 139. | Systems and Methods for Magnetic Shielding   | 13/050,742 | US 8,441,330  | 2013-May-14 |
| 140. | Systems and Methods for Magnetic Shielding   | 13/863,962 | US 9,465,401  | 2016-Oct-11 |
| 141. | Systems and Methods for Superconducting Flux Qubit Readout                               | 13/808,006 | US 8,854,074  | 2014-Oct-07 |
| 142. | Methods for Solving Computational Problems using   | 13/300,169 | US 8,977,576  | 2015-Mar-10 |

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| 143. | Quantum Processor Based Systems and Methods that Minimize an Objective Function                        | 13/806,404 | US 9,218,567  | 2015-Dec-22 |
| 144. | Systems, Methods and Apparatus for Planar Expulsion Shields  | 13/615,075 | US 9,192,085  | 2015-Nov-17 |
| 145. | Systems and Methods for Fabrication of Superconducting Integrated Circuits                             | 14/383,837 | US 9,768,371  | 2017-Sep-19 |
| 146. | Systems and Methods for Solving Computational Problems   | 13/678,266 | US 9,026,574  | 2015-May-05 |
| 147. | Systems and Devices for Quantum Processor Architectures  | 14/050,062 | US 9,178,154  | 2015-Nov-03 |
| 148. | Systems and Methods for Solving Combinatorial Problems   | 13/796,949 | US 9,396,440  | 2016-Jul-19 |
| 149. | Systems and Methods for Testing and Packaging a Superconducting Chip                                   | 14/109,604 | US 9,865,648  | 2018-Jan-09 |
| 150. | Systems and Methods for Achieving Orthogonal Control of Non-Orthogonal Qubit Parameters                | 14/339,289 | US 9,727,823  | 2017-Aug-08 |
| 151. | Systems and Methods That Formulate Problems for Solving by a Quantum Processor                         | 14/109,657 | US 9,875,215  | 23-Jan-2018 |
| 152. | Systems and Methods That Formulate Problems for Solving by a Quantum Processor                         | 14/109,663 | US 9,501,747  | 2016-Nov-22 |
| 153. | Systems and Methods for Operating a Quantum Processor to Determine Energy Eigenvalues of a Hamiltonian | 14/896,259 | US 10,068,180 | 2018-Sep-04 |
| 154. | Systems and Methods for Increasing the Energy Scale of a Quantum Processor                             | 14/340,291 | US 9,129,224  | 2015-Sep-08 |
| 155. | Systems and Methods for Interacting with a Quantum Computing System                                    | 14/250,041 | US 9,471,880  | 2016-Oct-18 |
| 156. | Systems and Methods for Real-Time Quantum Computer-Based Control of Mobile Systems                     | 14/163,838 | US 9,207,672  | 2015-Dec-08 |
| 157. | Systems and Methods for Real-Time Quantum Computer-Based Control of Mobile Systems                     | 14/874,102 | US 9,400,499  | 2016-Jul-26 |
| 158. | Systems and Methods for Error Correction in Quantum Computation  | 14/173,101 | US 9,361,169  | 2016-Jun-07 |

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| 159. | Systems and Methods for Error Correction in Quantum Computation  | 14/778,478 | US 9,870,277  | 2018-Jan-16 |
| 160. | Quantum Processor Based Systems and Methods That Minimize a Continuous Variable Objective Function         | 14/280,204 | US 9,424,526  | 2016-Aug-23 |
| 161. | Systems and Methods for Quantum Processing of Data, for Example Functional Magnetic Resonance Imaging Data | 14/316,366 | US 10,318,881 | 2019-Jun-11 |
| 162. | Systems and Methods for Quantum Processing of Data, for Example Imaging Data                               | 14/316,372 | US 9,727,824  | 2017-Aug-08 |
| 163. | Systems and Methods for Improving the Performance of a Quantum Processor by Reducing Errors                | 14/340,303 | US 9,495,644  | 2016-Nov-15 |
| 164. | Systems and Devices for Quantum Processor Architectures  | 14/453,883 | US 9,183,508  | 2015-Nov-10 |
| 165. | Systems and Devices for Quantum Processor Architectures  | 14/863,045 | US 9,547,826  | 2017-Jan-17 |
| 166. | Systems and Devices for Quantum Processor Architectures  | 15/373,910 | US 9,875,444  | 2018-Jan-23 |
| 167. | Quantum Processor with Instance Programmable Qubit Connectivity  | 14/691,268 | US 9,710,758  | 2017-Jul-18 |
| 168. | Universal Adiabatic Quantum Computing with Superconducting Qubits  | 14/520,139 | US 10,037,493 | 2018-Jul-31 |
| 169. | Systems And Methods Of Finding Quantum Binary Optimization Problems  | 14/671,862 | US 10,275,422 | 2019-Apr-30 |
| 170. | Method of Forming Low-Spread Josephson Junction  | 14/600,962 | US 9,634,224  | 2017-Apr-25 |
| 171. | Systems and Methods for Removing Couplings Between Quantum Devices   | 14/643,180 | US 10,002,107 | 2018-Jun-19 |
| 172. | Systems and Methods for Improving the Performance of a Quantum Processor Via Reduced Readouts              | 14/844,876 | US 10,031,887 | 2018-Jul-24 |
| 173. | Systems and Methods for Problem Solving, Useful for Example in Quantum Computing                           | 15/505,522 | US 9,881,256  | 2018-Jan-30 |
| 174. | Systems and Methods for Increasing Analog Processor Connectivity   | 15/418,497 | US 10,268,622 | 2019-Apr-23 |

**SCHEDULE C**

**U.S. Copyright Registrations and Applications**

None.