

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM759156

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
NeuroBlade Ltd.		09/29/2022	Company: ISRAEL
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	SILICON VALLEY BANK		
<b>Street Address:</b>	3003 TASMAN DRIVE		
<b>City:</b>	SANTA CLARA		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	95054		
<b>Entity Type:</b>	Corporation: CALIFORNIA		
<b>PROPERTY NUMBERS Total: 6</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Serial Number:</b>	90196158	IMPU	
<b>Serial Number:</b>	97246321	INSIGHTS	
<b>Serial Number:</b>	90196128	NEUROBLADE	
<b>Serial Number:</b>	90196141		
<b>Serial Number:</b>	97196650	XDIMM	
<b>Serial Number:</b>	90196166	XELERAM	
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>			
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
<b>Phone:</b>	(619) 699-2708		
<b>Email:</b>	christian.cruz@us.dlapiper.com		
<b>Correspondent Name:</b>	DLA Piper LLP (US)		
<b>Address Line 1:</b>	401 B Street		
<b>Address Line 2:</b>	Suite 1700		
<b>Address Line 4:</b>	San Diego, CALIFORNIA 92101		
<b>NAME OF SUBMITTER:</b>	Matt Schwartz		
<b>SIGNATURE:</b>	/s/ Matt Schwartz		
<b>DATE SIGNED:</b>	10/04/2022		

CH \$165.00 90196158

**Total Attachments: 19**

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## INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement (this “**Agreement**”) is entered into as of September 29, 2022 by and between (a) **SILICON VALLEY BANK**, a California corporation, with a loan production office located at 275 Grove Street, Suite 2-200, Newton, Massachusetts 02466 (“**Bank**”), (b) **KREOS CAPITAL VII AGGREGATOR SCSP**, a limited partnership incorporated in Luxembourg under registered number B264706 whose registered office is at 1 Boulevard de la Foire, Luxembourg (“**Kreos**”), and **NEUROBLADE LTD.**, a company organized under the laws of the State of Israel, with its principal place of business at 24 Raoul Wallenberg St., Tel Aviv-Jaffa, Israel (“**Grantor**”). Bank and Kreos are hereinafter each referred to as a “**Lender**” and collectively as the “**Lenders**”, as applicable.

### RECITALS

A. Lenders have agreed to make certain advances of money and to extend certain financial accommodations to Grantor and **NEUROBLADE, INC.**, a Delaware corporation (“**US Borrower**”) (Grantor and US Borrower, are hereinafter jointly and severally, individually and collectively, referred to as “**Borrower**”) (the “**Loans**”) in the amounts and manner set forth in that certain Loan and Security Agreement by and among Lenders and Borrower dated as of the date hereof (as the same may be amended, modified or supplemented from time to time, the “**Loan Agreement**”; capitalized terms used herein are used as defined in the Loan Agreement). Lenders are willing to make the Loans to Borrower, but only upon the condition, among others, that Grantor shall grant to Lenders a security interest in its Copyrights, Trademarks, Patents, and Mask Works (as each term is described below) to secure the obligations of Borrower to Lenders under the Loan Agreement.

B. Pursuant to the terms of the Loan Agreement and the ISR Debentures, Grantor has granted to Lenders a security interest in all of Grantor’s right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of Borrower’s obligations to Lenders under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

### AGREEMENT

1. Grant of Security Interest. To secure Borrower’s obligations to Lenders under the Loan Documents and subject to the terms as set forth therein, Grantor grants and pledges to Lenders a security interest in all of Grantor’s right, title and interest in, to and under its intellectual property (all of which shall collectively be called the “**Intellectual Property Collateral**”), including, without limitation, the following:

(a) Any and all copyright rights, copyright applications, copyright registrations and like protections in each work of authorship and derivative work thereof, whether published or unpublished and whether or not the same also constitutes a trade secret, now or hereafter existing, created, acquired or held, including without limitation those set forth on Exhibit A attached hereto (collectively, the “**Copyrights**”);

(b) Any and all trade secrets, and any and all intellectual property rights in computer software and computer software products now or hereafter existing, created, acquired or held;

(c) Any and all design rights that may be available to Grantor now or hereafter existing, created, acquired or held;

(d) All patents, patent applications and like protections including, without limitation, improvements, divisions, continuations, renewals, reissues, extensions and continuations-in-part of the same, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the “**Patents**”);

(e) Any trademark and servicemark rights, whether registered or not, applications to register and registrations of the same and like protections, and the entire goodwill of the business of Grantor connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the “**Trademarks**”);

(f) All mask works or similar rights available for the protection of semiconductor chips, now owned or hereafter acquired, including, without limitation those set forth on Exhibit D attached hereto (collectively, the “**Mask Works**”);

(g) Any and all claims for damages by way of past, present and future infringements of any of the rights included above, with the right, but not the obligation, to sue for and collect such damages for said use or infringement of the intellectual property rights identified above;

(h) All licenses or other rights to use any of the Copyrights, Patents, Trademarks, or Mask Works and all license fees and royalties arising from such use to the extent permitted by such license or rights;

(i) All amendments, extensions, renewals and extensions of any of the Copyrights, Trademarks, Patents, or Mask Works; and

(j) All proceeds and products of the foregoing, including, without limitation, all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

2. Recordation. Grantor authorizes the Commissioner for Patents, the Commissioner for Trademarks and the Register of Copyrights and any other government officials to record and register this Agreement upon request by Lenders.

3. Authorization. Grantor hereby authorizes Lenders solely to (a) modify this Agreement unilaterally by amending the exhibits to this Agreement to include any Intellectual Property Collateral which Grantor obtains subsequent to the date of this Agreement, and only in connection with such modification (b) to file a duplicate original of this Agreement containing amended exhibits reflecting such new Intellectual Property Collateral.

4. Loan Documents. This Agreement has been entered into pursuant to and in conjunction with the Loan Agreement and the ISR Debentures, each of which is hereby incorporated by reference. The provisions of the Loan Agreement and the ISR Debentures shall supersede and control over any conflicting or inconsistent provision herein. The rights and remedies of Lenders with respect to the Intellectual Property Collateral are as provided by the Loan Agreement, the ISR Debentures and related documents, and nothing in this Agreement shall be deemed to limit such rights and remedies.

5. Execution in Counterparts. This Agreement may be executed in counterparts (and by different parties hereto in different counterparts), each of which shall constitute an original, but all of which when taken together shall constitute a single contract. Delivery of an executed counterpart of a signature page to this Agreement by facsimile or in electronic (i.e., “pdf” or “tif” format) shall be effective as delivery of a manually executed counterpart of this Agreement.

6. Successors and Assigns. This Agreement will be binding on and shall inure to the benefit of the parties hereto and their respective successors and assigns.

7. Governing Law. This Agreement and any claim, controversy, dispute or cause of action (whether in contract or tort or otherwise) based upon, arising out of or relating to this Agreement and the transactions contemplated hereby and thereby shall be governed by, and construed in accordance with, the laws of the United States and California, without giving effect to any choice or conflict of law provision or rule (whether of the State of California or any other jurisdiction).

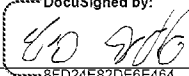
8. Termination. Upon indefeasible satisfaction in full in cash of the Obligations and at such time as all commitments by Lenders to lend to Borrower have terminated and Lenders have no further obligations to make any credit extensions to Borrower, the security interest granted hereunder shall terminate automatically. Upon such termination, if requested by Grantor, Lenders shall, at Grantor's sole cost and expense, execute all documents and other instruments as may be necessary to evidence the termination of the security interest granted hereunder.

*[Signature page follows]*

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

GRANTOR:

NEUROBLADE LTD.

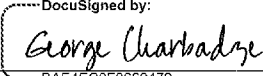
DocuSigned by:  
  
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By: Elad Sity

Title: CEO

BANK:

SILICON VALLEY BANK

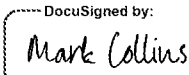
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By: George Charbadze

Title: Operational Relationship Manager

KREOS:

KREOS CAPITAL VII AGGREGATOR SCSP

DocuSigned by:  
  
12A114C12864422...

By: Mark Collins

Name: Mark Collins

Title: Director

[Signature Page – Intellectual Property Security Agreement – NEUROBLADE LTD.]

EXHIBIT A

Copyrights

None.

EXHIBIT B

Patents

See attached list:



Log of Issued/Granted Patents, Pending, PCT, and Provisional.

Last update: 2022-August-08

Neuroblade  
internal  
file  
number

**Issued (US) / Granted (rest of world)**

	<b>Patent Number</b>	<b>Date Filed</b>	<b>Date Issued</b>	<b>Official Title (Description)</b>	
	9902-US2	US 10,664,438	16-Jul-2019	26-May-2020	Memory-Based Distributed Processor Architecture (Software Synchronization of Buses) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10564438">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10564438</a>
	9902-US4	US 10,762,034	16-Jul-2019	1-Sep-2020	Memory-Based Distributed Processor Architecture (Additional Hierarchy Level for Embedded Memory Logic) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10762034">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10762034</a>
	9902-US3	US 10,885,951	16-Jul-2019	5-Jan-2021	Memory-Based Distributed Processor Architecture (Memory IC for Neural Networks) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10885951">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=10885951</a>
	9902-US5	US 11,023,336	16-Jul-2019	1-Jun-2021	Memory-Based Distributed Processor Architecture (Use of Redundant Logic in Memory Circuits) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11023336">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11023336</a>
	9902-US6	US 11,126,511	16-Jul-2019	21-Sep-2021	Memory-Based Distributed Processor Architecture (High Performance Processor for Low-way and High-Latency Memory Instances) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11126511">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11126511</a>
	9902-US1	US 11,269,743	16-Jul-2019	8-Mar-2022	Memory-Based Distributed Processor Architecture <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11269743">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11269743</a>
	9902-US3.1	US 11,301,340	4-Dec-2020	12-Apr-2022	Memory-Based Distributed Processor Architecture (Memory IC for Neural Networks) <a href="https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11301340">https://rdftblw.uspto.gov/ow?Pagenum=0&amp;docid=11301340</a>
	9903-US1	US 16,783,767	6-Feb-2020	TBD	Memory-Based Processors (Testing memory in parallel and using logic in the memory to test the memory) (APPLICATION ALLOWED, WAITING FOR ISSUE MAYBE IN OCTOBER 2022)

<u>Pending</u>	<u>Application Number</u>	<u>Date filed</u>	<u>Title</u>
9902-TW	TW 107126383	30-Jul-2018	Memory-Based Distributed Processor Architecture
9902-CN	CN 201880062664.7	26-Mar-2020	Memory-Based Distributed Processor Architecture
9902-CN4	CN 202111072866.X	14-Sep-2021	Memory-Based Distributed Processor Architecture
9902-EP	EP 18841178.9	27-Feb-2020	Memory-Based Distributed Processor Architecture
9902-JP	JP 2020-505784	29-Jan-2020	Memory-Based Distributed Processor Architecture
9902-KR	KR 10-2020-7006169	2-Mar-2020	Memory-Based Distributed Processor Architecture
9902-US6.1	US 17/397,061	9-Aug-2021	Memory-Based Distributed Processor Architecture
9902-US1.1	US 17/649,975	4-Feb-2022	Memory-Based Distributed Processor Architecture
9903-TW	TW 108132302	6-Sep-2019	Memory-Based Processors
9903-CN	CN 201980069510.5	21-Apr-2021	Memory-Based Processors
9903-EP	EP 19857466.7	18-Mar-2021	Memory-Based Processors
9903-KR	KR 10-2021-7010116	5-Apr-2021	Memory-Based Processors
9903-US2	US 17/196,422	9-Mar-2021	Memory-Based Processors
9903-US3	US 17/199,599	12-Mar-2021	Memory-Based Processors
9903-US4	US 17/199,818	12-Mar-2021	Memory-Based Processors
9903-US5	US 17/199,936	12-Mar-2021	Memory-Based Processors

9914-TW	TW 109127495	13-Aug-2020	Memory-Based Processors
9914-CN5	CN 202080071415.1	11-Apr-2022	M-B P -> Compensating For DRAM Activation Penalties
9914-EP5	EP20852497.5	28-Jan-2022	M-B P -> Compensating For DRAM Activation Penalties
9914-KR2	KR 10-2022-7008116	11-Mar-2022	Distributed Processor Memory Chip With Multi-Port Processor Subunits
9914-US1	US 17/669,642	11-Feb-2022	Cyber Security And Tamper Detection Techniques With A Distributed Processor Memory Chip
9914-US2	US 17/669,649.	11-Feb-2022	Distributed Processor Memory Chip With Multi-Port Processor Subunits
9914-US3	US 17/669,657	11-Feb-2022	In-Memory Zero Value Detection
9914-US5	US 17/668,240	9-Feb-2022	Compensating For DRAM Activation Penalties
9914-US7	US 17/668,260	9-Feb-2022	Memory Mat As A Register File
9926-TW1	TW 110147591	17-Dec-2021	Memory-Based Processors

**PCT (active)**

<u>Application Number</u>	<u>Date filed</u>	<u>Title</u>
9926-PC	PCT/US2021/055472	18-Oct-2021 Memory Appliances for Memory Intensive Operations (8 inventions supporting computational memory)

**PCT (expired)**

9902-PC	PCT/IB2018/000995	30-Jul-2018	Memory-Based Dist. Proc. Architecture (8 inventions)
9903-PC	PCT/IB2019/001005	6-Sep-2019	Memory-Based Processors (7 inventions)
9914-PC	PCT/IB2020/000665	13-Aug-2020	Memory-Based Processors (11 + inventions supporting computational memory)

**Provisionals filed (but not yet full application)**

<u>Application Number</u>	<u>Date filed</u>	<u>Title</u>	
9012-USP	US 63/296,645	5-Jan-2022	Hardware Pruner
9010-USP	US 63/304,975	31-Jan-2022	Hardware Agnostic Query Planning

9081-USP	US 63/314,618	28-Feb-2022	Metal Layer Connecting Channels
9011-USP	US 63/317,219	7-Mar-2022	Extra Power Via Standard DIMM Interface
9082-USP	US 63/342,767	17-May-2022	Dynamic Grid Routing
9030-USP	US 63/345,545	25-May-2022	Quick In-Memory Computation
9087-USP	US 63/350,579	09-Jun-2022	Inline Data Reduction (Internal: "Top-K")
9084-USP	US 63/355,763	27-Jun-2022	Distributed Storage Agents

EXHIBIT C


Trademarks

See attached list:

**Neuroblade, Ltd.  
Active Trademark Portfolio  
(as of July 20, 2022)**

Image	Trademark	Country	Status	Application No.	Filing Date	Registration No.	Registration Date	Owner Name	Classes	Goods	Action Due
	IMPU	United States of America	Allowed	90/196,158	21-Sep-2020			Neuroblade, Ltd.	09 Int., 42 Int.	<p>Class 9: Computer hardware and downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of artificial intelligence and data analytics; Computer hardware, including memories, application-specific integrated circuits (ASICs), printed circuits; Artificial intelligence computers; Computer hardware for use in data analytics and data base acceleration; Computing accelerator boards; Integrated circuits, semiconductors and computer chipsets; High performance computer hardware with specialized features for analytics and artificial intelligence learning and processing; Graphics accelerators; Computer accelerator boards; Multimedia accelerator boards; Downloadable computer software for machine learning, database management and operation; data analytics, and hardware acceleration in the field of general purpose artificial intelligence computing; Downloadable interactive computer game programs with artificial intelligence; Downloadable computer software applications for machine learning, database management and operation, and data analytics in the field of artificial intelligence; System-on-chip processors; Electronic computer vision accelerator boards; Computer hardware and downloadable software, and computer communications hardware and downloadable communications software, all for the operation, control, maintenance, and management of computer and memory resources; Random access memory (RAM) cards; Disaggregated computer servers; Computational computer storage devices, namely, blank flash drives and external computer hard drives; Downloadable compiler software and downloadable optimization software for data analytics and business processes; Memory modules, namely, Dual In-line Memory Modules (DIMMs).</p> <p>Class 42: Integration, testing and analysis services for computer systems; Development of software for acceleration of computer workloads; Design of computer hardware, software and peripherals; Technological consultancy and information services in the field of high performance computing and data distribution; Providing temporary use of non-downloadable software for parallel computing optimization; Providing temporary use of non-downloadable software for enhancing computer performance; for operation of integrated circuits, semiconductors, computer chipsets and microprocessors; Providing temporary use of non-downloadable software for load balancing computing and data; Providing temporary use of non-downloadable software for optimizing performance and speeding up data operations; Providing virtual computer systems and virtual computer environments through cloud computing; Providing temporary use of</p> <p>Class 9: Downloadable computer software for database management and operation, data analytics, and hardware acceleration in the field of data analytics; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of data analytics; Downloadable computer software applications for machine learning, database management and operation, and data analytics; Downloadable computer software and downloadable communications software for the operation, control, maintenance, and management of computer and memory resources; Downloadable compiler software and downloadable optimization software for data analytics and business processes.</p>	<p>Statement of Use or 1st Extension: 05-Oct-2022</p> <p>Statement of Use or 2nd Extension: 05-Apr-2023</p> <p>Statement of Use or 3rd Extension: 05-Oct-2023</p> <p>Statement of Use or 4th Extension: 05-Apr-2024</p> <p>Statement of Use or 5th Extension: 05-Oct-2024.</p> <p>Statement of Use Final Deadline: 05-Apr-2025</p>
	INSIGHTS	United States of America	Pending	97/246,321	31-Jan-2022			Neuroblade, Ltd.	09 Int.	<p>Class 9: Downloadable computer software for database management and operation, data analytics, and hardware acceleration in the field of data analytics; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of data analytics; Downloadable computer software applications for machine learning, database management and operation, and data analytics; Downloadable computer software and downloadable communications software for the operation, control, maintenance, and management of computer and memory resources; Downloadable compiler software and downloadable optimization software for data analytics and business processes.</p>	

NEUROBLADE	United States of America	Allowed	90/196,128	21-Sep-2020		Neuroblade, Ltd.	09 Int., 42 Int.	<p>Class 9: Computer hardware and downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of artificial intelligence and data analytics; Computer hardware, including memories, application-specific integrated circuits (ASICs), printed circuits; Artificial intelligence computers; Computer hardware for use in data analytics and data base acceleration; Computing accelerator boards, integrated circuits, semiconductors and computer chipsets; High performance computer hardware with specialized features for analytics and artificial intelligence learning and processing; Graphics accelerators; Computer accelerator boards; Multimedia accelerator boards; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of general purpose artificial intelligence computing; Downloadable interactive computer game programs with artificial intelligence; Downloadable computer software applications for machine learning, database management and operation, and data analytics in the field of artificial intelligence; System-on-chip processors; Electronic computer vision accelerator boards; Computer hardware and downloadable software, and computer communications hardware and downloadable communications software, all for the operation, control, maintenance, and management of computer and memory resources; Random access memory (RAM) cards; Disaggregated computer servers; Computational computer storage devices, namely, blank flash drives and external computer hard drives; Downloadable compiler software and downloadable optimization software for data analytics and business processes; Memory modules, namely, Dual In-line Memory Modules (DIMMs).</p> <p>Class 42: Integration, testing and analysis services for computer systems; Development of software for acceleration of computer workloads; Design of computer hardware, software and peripherals; Technological consultancy and information services in the field of high performance computing and data distribution; Providing temporary use of non-downloadable software for parallel computing optimization; Providing temporary use of non-downloadable software for enhancing computer performance, for operation of integrated circuits, semiconductors, computer chipsets and microprocessors; Providing temporary use of non-downloadable software for load balancing computing and data; Providing temporary use of non-downloadable software for optimizing performance and speeding up data operations; Providing virtual computer systems and virtual computer environments through cloud computing; Providing temporary use of</p>	<p>Statement of Use or 1st Extension: 05-Oct-2022</p> <p>Statement of Use or 2nd Extension: 05-Apr-2023</p> <p>Statement of Use or 3rd Extension: 05-Oct-2023</p> <p>Statement of Use or 4th Extension: 05-Apr-2024</p> <p>Statement of Use or 5th Extension: 05-Oct-2024,</p> <p>Statement of Use Final Deadli 05-Apr-2025</p>
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	Neuroblade Logo	United States of America	Allowed	90/196,141	21-Sep-2020		Neuroblade, Ltd.	09 Int., 42 Int.	<p>Class 9: Computer hardware and downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of artificial intelligence and data analytics; Computer hardware, including memories, application-specific integrated circuits (ASICs), printed circuits; Artificial intelligence computers; Computer hardware for use in data analytics and data base acceleration; Computing accelerator boards, integrated circuits, semiconductors and computer chipsets; High performance computer hardware with specialized features for analytics and artificial intelligence learning and processing; Graphics accelerators; Computer accelerator boards; Multimedia accelerator boards; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of general purpose artificial intelligence computing; Downloadable interactive computer game programs with artificial intelligence; Downloadable computer software applications for machine learning, database management and operation, and data analytics in the field of artificial intelligence; System-on-chip processors; Electronic computer vision accelerator boards; Computer hardware and downloadable software, and computer communications hardware and downloadable communications software, all for the operation, control, maintenance, and management of computer and memory resources; Random access memory (RAM) cards; Disaggregated computer servers; Computational computer storage devices, namely, blank flash drives and external computer hard drives; Downloadable compiler software and downloadable optimization software for data analytics and business processes; Memory modules, namely, Dual In-line Memory Modules (DIMMs).</p> <p>Class 42: Integration, testing and analysis services for computer systems; Development of software for acceleration of computer workloads; Design of computer hardware, software and peripherals; Technological consultancy and information services in the field of high performance computing and data distribution; Providing temporary use of non-downloadable software for parallel computing optimization; Providing temporary use of non-downloadable software for enhancing computer performance, for operation of integrated circuits, semiconductors, computer chipsets and microprocessors; Providing temporary use of non-downloadable software for load balancing computing and data; Providing temporary use of non-downloadable software for optimizing performance and speeding up data operations; Providing virtual computer systems and virtual computer environments through cloud computing; Providing temporary use of</p>	<p>Statement of Use or 1st Extension: 05-Oct-2022</p> <p>Statement of Use or 2nd Extension: 05-Apr-2023</p> <p>Statement of Use or 3rd Extension: 05-Oct-2023</p> <p>Statement of Use or 4th Extension: 05-Apr-2024</p> <p>Statement of Use or 5th Extension: 05-Oct-2024,</p> <p>Statement of Use Final Deadli 05-Apr-2025</p>
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XDIMM	United States of America	Pending	97/196,650	30-Dec-2021			Neuroblade, Ltd.	09 Int., 42 Int.	<p>Class 9: Computer hardware and downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of artificial intelligence and data analytics; Computer hardware, including memories, application-specific integrated circuits (ASICs), printed circuits; Artificial intelligence computers; Computer hardware for use in data analytics and data base acceleration; Computing accelerator boards, integrated circuits, semiconductors and computer chipsets; High performance computer hardware with specialized features for analytics and artificial intelligence learning and processing; Graphics accelerators; Computer accelerator boards; Multimedia accelerator boards; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of general purpose artificial intelligence computing; Downloadable interactive computer game programs with artificial intelligence; Downloadable computer software applications for machine learning, database management and operation, and data analytics in the field of artificial intelligence; System-on-chip processors; Electronic computer vision accelerator boards; Computer hardware and downloadable software, and computer communications hardware and downloadable communications software, all for the operation, control, maintenance, and management of computer and memory resources; Random access memory (RAM) cards; Disaggregated computer servers; Computational computer storage devices, namely, blank flash drives and external computer hard drives; Downloadable compiler software and downloadable optimization software for data analytics and business processes; Memory modules, namely, Dual In-line Memory Modules (DIMMs).</p> <p>Class 42: Integration, testing and analysis services for computer systems; Development of software for acceleration of computer workloads; Design of computer hardware, software and peripherals; Technological consultancy and information services in the field of high performance computing and data distribution; Providing temporary use of non-downloadable software for parallel computing optimization; Providing temporary use of non-downloadable software for enhancing computer performance, for operation of integrated circuits, semiconductors, computer chipsets and microprocessors; Providing temporary use of non-downloadable software for load balancing computing and data; Providing temporary use of non-downloadable software for optimizing performance and speeding up data operations; Providing virtual computer systems and virtual computer environments through cloud computing; Providing temporary use of</p>	
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XELERRAM	United States of America	Allowed	90/196,166	21-Sep-2020			Neuroblade, Ltd.	09 Int., 42 Int.	<p>Class 9: Computer hardware and downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of artificial intelligence and data analytics; Computer hardware, including memories, application-specific integrated circuits (ASICs), printed circuits; Artificial intelligence computers; Computer hardware for use in data analytics and data base acceleration; Computing accelerator boards, integrated circuits, semiconductors and computer chipsets; High performance computer hardware with specialized features for analytics and artificial intelligence learning and processing; Graphics accelerators; Computer accelerator boards; Multimedia accelerator boards; Downloadable computer software for machine learning, database management and operation, data analytics, and hardware acceleration in the field of general purpose artificial intelligence computing; Downloadable interactive computer game programs with artificial intelligence; Downloadable computer software applications for machine learning, database management and operation, and data analytics in the field of artificial intelligence; System-on-chip processors; Electronic computer vision accelerator boards; Computer hardware and downloadable software, and computer communications hardware and downloadable communications software, all for the operation, control, maintenance, and management of computer and memory resources; Random access memory (RAM) cards; Disaggregated computer servers; Computational computer storage devices, namely, blank flash drives and external computer hard drives; Downloadable compiler software and downloadable optimization software for data analytics and business processes; Memory modules, namely, Dual In-line Memory Modules (DIMMs).</p> <p>Class 42: Integration, testing and analysis services for computer systems; Development of software for acceleration of computer workloads; Design of computer hardware, software and peripherals; Technological consultancy and information services in the field of high performance computing and data distribution; Providing temporary use of non-downloadable software for parallel computing optimization; Providing temporary use of non-downloadable software for enhancing computer performance, for operation of integrated circuits, semiconductors, computer chipsets and microprocessors; Providing temporary use of non-downloadable software for load balancing computing and data; Providing temporary use of non-downloadable software for optimizing performance and speeding up data operations; Providing virtual computer systems and virtual computer environments through cloud computing; Providing temporary use of</p>	<p>Statement of Use or 1st Extension: 05-Oct-2022</p> <p>Statement of Use or 2nd Extension: 05-Apr-2023</p> <p>Statement of Use or 3rd Extension: 05-Oct-2023</p> <p>Statement of Use or 4th Extension: 05-Apr-2024</p> <p>Statement of Use or 5th Extension: 05-Oct-2024,</p> <p>Statement of Use Final Deadli 05-Apr-2025</p>
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EXHIBIT D

Mask Works

See attached list:

# NB1 Mask List

Process Name	Mask Code	Mask Ver.	Mask In Date	Mask Location
Cell Field	CF	A	2020/4/9	PSMC
Field	1F	A	2020/4/12	PSMC
Deep N Well	1I	A	2020/4/12	PSMC
1st P well	3I	A	2020/4/12	PSMC
N Well	2I	A	2020/4/13	PSMC
Channel Dope 1	1D	A	2020/4/15	PSMC
Channel Dope 2	2D	A	2020/4/15	PSMC
Channel Dope 3	3D	A	2020/4/15	PSMC
Channel Dope 4	4D	A	2020/4/21	PSMC
Channel Dope 7	7D	A	2020/4/21	PSMC
Channel Dope 8	8D	A	2020/4/22	PSMC
Dual Oxide	DO	A	2020/4/18	PSMC
MC LDD implant	4I	A	2020/4/25	PSMC
1st mask etch	5F	A	2020/4/14	PSMC
Metal Word Line(DP1)	RG	A	2020/4/25	PSMC
Metal Word Line(DP2)	R2	A	2020/4/24	PSMC
Peripheral open	3E	A	2020/4/25	PSMC
Cell open	5E	A	2020/4/26	PSMC
Bit Contact	1B	A	2020/5/5	PSMC
Gate Poly Implant N	GP	A	2020/4/23	PSMC
Gate Poly Implant P	GB	A	2020/4/26	PSMC
Ploymetal Gate	1G	A	2020/5/20	PSMC
LDD Spacer E/B	PE	A	2020/5/1	PSMC
N-LDD	1N	A	2020/5/3	PSMC
HV-N LDD	3N	A	2020/5/2	PSMC
P-LDD	1P	A	2020/5/3	PSMC
HV-P	3P	A	2020/5/3	PSMC
SAP device (PS)	5P	A	2020/5/4	PSMC
Thin oxide P-LDD Low Vth	6P	A	2020/5/3	PSMC
LDD Removal	2E	A	2020/5/7	PSMC
N+ SD	2N	A	2020/5/7	PSMC
P+ SD	2P	A	2020/5/12	PSMC
Storage Node Contact	2B	A	2020/5/11	PSMC
Periphery	3B	A	2020/6/2	PSMC
P+ contact I/I	1J	A	2020/6/4	PSMC
2nd mask etch	4F	A	2020/6/6	PSMC
W wiring	1W	A	2020/6/16	PSMC
Storage Node	1S	A	2020/6/10	PSMC
Storage Node	2S	A	2020/6/11	PSMC
Support for Storage Node	5S	A	2020/6/17	PSMC
Cell Plate	CP	A	2020/6/16	PSMC
Step Reduction	SR	A	2020/6/18	PSMC
Contact	1C	A	2020/6/23	PSMC
Metal 1	1M	A	2020/6/21	PSMC
Through Hole 1	1T	A	2020/6/23	PSMC
Metal 2	2M	A	2020/6/25	PSMC
Through Hole 2	2T	A	2020/6/24	PSMC
Metal 3	3M	A	2020/6/20	PSMC
Polymide	P1	A	2020/6/22	PSMC

# NB2 Mask List

Process Name	Mask Code	Mask Ver.	Mask In Date	Mask Location
Cell Field	CF	A	2021/12/22	PSMC
Field	1F	A	2021/12/23	PSMC
Deep N Well	1I	A	2022/1/1	PSMC
1st P well	3I	A	2022/1/1	PSMC
N Well	2I	A	2021/12/30	PSMC
Channel Dope 1	1D	A	2022/1/6	PSMC
Channel Dope 2	2D	A	2022/1/6	PSMC
Channel Dope 3	3D	A	2022/1/6	PSMC
Channel Dope 4	4D	A	2022/1/6	PSMC
Channel Dope 8	8D	A	2022/1/7	PSMC
AF channel	AF	A	2022/1/8	PSMC
MC LDD implant	4I	A	2022/1/1	PSMC
1st mask etch	5F	A	2021/12/28	PSMC
Metal Word Line(DP1)	RG	A	2022/1/14	PSMC
Metal Word Line(DP2)	R2	A	2022/1/9	PSMC
Peripheral open	3E	A	2022/1/23	PSMC
Dual Oxide	DO	A	2022/1/15	PSMC
Gate Poly implant N	GP	A	2022/1/16	PSMC
Gate Poly implant P	GB	A	2022/1/21	PSMC
Cell open	5E	A	2022/1/22	PSMC
Bit Contact	1B	A	2022/2/1	PSMC
Poly/metal Gate	1G	A	2022/2/28	PSMC
LDD Spacer E/B	PE	A	2022/2/17	PSMC
N-LDD	1N	A	2022/2/18	PSMC
HV-N LDD	3N	A	2022/2/21	PSMC
P-LDD	1P	A	2022/2/23	PSMC
MV P-LDD	7P	A	2022/2/28	PSMC
HV-P	3P	A	2022/3/1	PSMC
Thin oxide P-LDD Low Vth	6P	A	2022/2/27	PSMC
SAP device (PS)	5P	A	2022/2/26	PSMC
LDD Removal	2E	A	2022/2/25	PSMC
P+ SD	2P	A	2022/2/27	PSMC
N+ SD	2N	A	2022/3/3	PSMC
Storage Node Contact	2B	A	2022/2/21	PSMC
Periphery	3B	A	2022/3/14	PSMC
P+ contact I/I	1J	A	2022/3/28	PSMC
2nd mask etch	4F	A	2022/3/10	PSMC
W wiring	1W	A	2022/4/15	PSMC
Storage Node	1S	A	2022/3/15	PSMC
Storage Node	2S	A	2022/3/18	PSMC
Support for Storage Node	5S	A	2022/3/31	PSMC
Cell Plate	CP	A	2022/3/29	PSMC
Plant Reverse	SR	A	2022/3/10	PSMC
Contact	1C	A	2022/4/16	PSMC
Metal 1	1M	A	2022/5/8	PSMC
Through Hole 1	1T	A	2022/4/30	PSMC
Metal 2	2M	A	2022/5/7	PSMC
Through Hole 2	2T	A	2022/5/10	PSMC
Metal 3	3M	A	2022/5/7	PSMC
Polymide	PI	A	2022/4/18	PSMC

Update: 2022/8/2



Confidential

TRADEMARK

RECORDED: 10/04/2022

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