

TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

ETAS ID: TM778762

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST		
CONVEYING PARTY DATA			
Name	Formerly	Execution Date	Entity Type
SILICON VALLEY BANK		01/05/2023	Corporation:
RECEIVING PARTY DATA			
Name:	FUNGIBLE, INC.		
Street Address:	3201 Scott Blvd		
City:	Santa Clara		
State/Country:	CALIFORNIA		
Postal Code:	95054		
Entity Type:	Corporation: DELAWARE		
PROPERTY NUMBERS Total: 32			
Property Type	Number	Word Mark	
Serial Number:	90459084	BARE METAL DATA CENTER	
Serial Number:	90458966	EPIC	
Serial Number:	90458954	EPC	
Serial Number:	90191092	FUNGIBLE COMPOSER	
Serial Number:	90818100	OPEN AI FABRIC	
Serial Number:	90818084	AI FABRIC	
Serial Number:	90818066	OPEN DATA FABRIC	
Serial Number:	90818042	DATA FABRIC	
Serial Number:	90459089	BMDC	
Serial Number:	90458914	TRUEAGILITY	
Serial Number:	90132270	FUNGIBLE	
Serial Number:	90655430	FUNGIBLE DATA CENTER	
Serial Number:	90191115	FUNGIBLE COMPOSER	
Serial Number:	90458956	EDGE PUBLIC INFRASTRUCTURE CLOUD	
Serial Number:	90458942	EDGE PUBLIC CLOUD	
Serial Number:	88830441	FUNGIBLE BLOCK STORE	
Serial Number:	88830445	FUNGIBLE OBJECT STORE	
Serial Number:	88848499	FUNGIBLE FILE STORE	
Serial Number:	88848490	FUNGIBLE OBJECT STORE	

CH \$815.00 90459084

Property Type	Number	Word Mark
Serial Number:	88848483	FUNGIBLE BLOCK STORE
Serial Number:	88813074	ETHERFABRIC
Serial Number:	88830449	FUNGIBLE FILE STORE
Serial Number:	88698104	BARE METAL VIRTUALIZED DATA CENTERS
Serial Number:	88698109	BMVDC
Serial Number:	88698105	BM-VDC
Serial Number:	88624804	DPU-ENABLED TRUEFABRIC
Serial Number:	88624800	DYNAMICALLY COMPOSABLE DATA CENTER
Serial Number:	88624794	TRUEFABRIC
Serial Number:	87728631	FUNGIBLE DPU
Serial Number:	87728620	FUNGIBLE DATA PROCESSING UNIT
Serial Number:	87426380	FUNGIBLE
Serial Number:	87491250	FUNOS

CORRESPONDENCE DATA

Fax Number: 4159472099
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.
Phone: 4159472169
Email: qluflood@wsgr.com
Correspondent Name: Wilson Sonsini Goodrich & Rosati, P.C.
Address Line 1: One Market Plaza, Spear Tower, Suite 330
Address Line 4: San Francisco, CALIFORNIA 94105

ATTORNEY DOCKET NUMBER:	48723.027
NAME OF SUBMITTER:	Qui Lu Flood
SIGNATURE:	/Qui Lu Flood/
DATE SIGNED:	01/05/2023

Total Attachments: 11
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TERMINATION AND RELEASE OF INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Termination and Release of Intellectual Property Security Agreement (this “**Termination**”), dated as of January 5, 2023, is executed by SILICON VALLEY BANK, a California corporation (“**Bank**”), in favor of FUNGIBLE, INC., a Delaware corporation (“**Grantor**”). All capitalized terms used in this Termination and not otherwise defined herein, shall have the respective meanings given to such terms in the Security Agreement (defined below).

RECITALS

A. Pursuant to that certain Intellectual Property Security Agreement, dated as of December 16, 2021 (“**Security Agreement**”), executed by Grantor in favor of Bank, Grantor granted to Bank a security interest in the IP Collateral (defined below).

B. The Security Agreement was recorded with the Patent Division of the United States Patent and Trademark Office on December 16, 2021 at Reel 058523 and Frame 0960, to evidence the security interest granted under the Security Agreement.

C. The Security Agreement was recorded with the Trademark Division of the United States Patent and Trademark Office on December 16, 2021 at Reel 7532 and Frame 0725, to evidence the security interest granted under the Security Agreement.

D. Bank agrees to terminate and release its security interest in the IP Collateral specified below.

AGREEMENT

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Bank hereby agrees as follows:

(a) Bank expressly terminates and releases all of Bank’s right, title and interest in pursuant to the Security Agreement, to and under the following (collectively, the “**IP Collateral**”):

(i) Any and all copyright rights, copyright applications, copyright registrations and like protections in each work of authorship and derivative work thereof, whether published or unpublished and whether or not the same also constitutes a trade secret, now or hereafter existing, created, acquired or held, including without limitation those set forth on Exhibit A attached hereto (collectively, the “**Copyrights**”);

(ii) Any and all trade secrets, and any and all intellectual property rights in computer software and computer software products now or hereafter existing, created, acquired or held;

(iii) Any and all design rights that may be available to Grantor now or hereafter existing, created, acquired or held;

(iv) All patents, patent applications and like protections including, without limitation, improvements, divisions, continuations, renewals, reissues, extensions and continuations-in-part of the same, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the “**Patents**”);

(v) Any trademark and servicemark rights, whether registered or not, applications to register and registrations of the same and like protections, and the entire goodwill of the business of Grantor connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the “**Trademarks**”);

(vi) All mask works or similar rights available for the protection of semiconductor chips, now owned or hereafter acquired, including, without limitation those set forth on Exhibit D attached hereto (collectively, the “**Mask Works**”);

(vii) Any and all claims for damages by way of past, present and future infringements of any of the rights included above, with the right, but no the obligation, to, upon the occurrence and during the continuance of an Event of Default, sue for and collect such damages for said use or infringement of the intellectual property rights identified above;

(viii) All licenses or other rights to use any of the Copyrights, Patents, Trademarks, or Mask Works and all license fees and royalties arising from such use to the extent permitted by such license or rights;

(ix) All amendments, extensions, renewals and extensions of any of the Copyrights, Trademarks, Patents, or Mask Works; and

(x) All proceeds and products of the foregoing, including without limitation all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

(b) Bank authorizes and requests the Trademark and Patent Divisions of the United States Patent and Trademark Office and the United States Copyright Office to record this Termination.

[Signature Page Follows]

IN WITNESS WHEREOF, Bank has executed and delivered this Termination as of the day and year first above written.

SILICON VALLEY BANK

DocuSigned by:

Eugene Choi

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Name: **Eugene Choi**

Title: **Vice President**

EXHIBIT A

Copyrights

None.

EXHIBIT B

Patents

Title	Application No.	Filing Date	Patent No.	Issue Date
RELIABLE FABRIC CONTROL PROTOCOL EXTENSIONS FOR DATA CENTER NETWORKS WITH FAILURE RESILIENCE	17303900	06/10/2021	-	-
FABRIC CONTROL PROTOCOL WITH CONGESTION CONTROL FOR DATA CENTER NETWORKS	17303901	06/10/2021	-	-
RELIABLE FABRIC CONTROL PROTOCOL EXTENSIONS FOR DATA CENTER NETWORKS WITH UNSOLICITED PACKET SPRAYING OVER MULTIPLE ALTERNATE DATA PATHS	17303887	06/09/2021	-	-
SCALED-OUT TRANSPORT AS CONNECTION PROXY FOR DEVICE-TO-DEVICE COMMUNICATIONS	17248828	02/09/2021	-	-
EFFICIENT PACKET QUEUEING FOR COMPUTER NETWORKS	16947754	08/14/2020	-	-
EMBEDDED NETWORK PACKET DATA FOR USE OF ALTERNATIVE PATHS WITHIN A GROUP OF NETWORK DEVICES	16863730	04/30/2020	11240143	02/01/2022
FABRIC CONTROL PROTOCOL FOR DATA CENTER NETWORKS WITH PACKET SPRAYING OVER MULTIPLE ALTERNATE DATA PATHS	16774941	01/28/2020	-	-
FINITE AUTOMATA GLOBAL COUNTER IN A DATA FLOW GRAPH-DRIVEN ANALYTICS PLATFORM HAVING ANALYTICS HARDWARE ACCELERATORS	16825714	03/20/2020	-	-
DEFAULT ARC FOR COMPRESSION OF DETERMINISTIC FINITE AUTOMATA (DFA) DATA FLOW GRAPHS WITHIN A DATA FLOW GRAPH-DRIVEN ANALYTICS PLATFORM HAVING ANALYTICS HARDWARE ACCELERATORS	16825664	03/20/2020	-	-
CONGESTION CONTROL IN AN ON-CHIP NETWORK	16828778	03/24/2020	11477120	10/18/2022
RAPIDLY ESTABLISHING A CHAIN OF TRUST IN A COMPUTING SYSTEM	16556439	08/30/2019	11314868	04/26/2022
IC ROUTING FOR SILICON CIRCUITS WITH SMALLER GEOMETRIES	16810624	03/05/2020	10977410	04/13/2021
PROVIDING SCALABLE AND CONCURRENT FILE SYSTEMS	16813353	03/09/2020	-	-
DYNAMICALLY CHANGING CONFIGURATION OF DATA PROCESSING UNIT WHEN CONNECTED TO STORAGE DEVICE OR COMPUTING DEVICE	16560948	09/04/2019	11256644	02/22/2022

Title	Application No.	Filing Date	Patent No.	Issue Date
LOW LATENCY PACKET SWITCH ARCHITECTURE	16808026	03/03/2020	11258726	02/22/2022
CONTEXT VALUE RETRIEVAL PRIOR TO OR PARALLEL WITH EXPANSION OF PREVIOUS SYMBOL FOR CONTEXT-DECODING IN RANGE DECODER	16440689	06/13/2019	11188338	11/30/2021
TIMER MANAGEMENT FOR NETWORK DEVICES	16569360	09/12/2019	11038807	06/15/2021
QUERY PROCESSING USING DATA PROCESSING UNITS HAVING DFA/NFA HARDWARE ACCELERATORS	16584467	09/26/2019	-	-
DATA INGESTION AND STORAGE BY DATA PROCESSING UNIT HAVING STREAM-PROCESSING HARDWARE ACCELERATORS	16584390	09/26/2019	11263190	03/01/2022
DATA FLOW GRAPH-DRIVEN ANALYTICS PLATFORM USING DATA PROCESSING UNITS HAVING HARDWARE ACCELERATORS	16584293	09/26/2019	-	-
DATA PROCESSING UNIT WITH KEY VALUE STORE	16504112	07/05/2019	11258796	02/22/2022
REMOVABLE I/O EXPANSION DEVICE FOR DATA CENTER STORAGE RACK	16546185	08/20/2019	-	-
DIRECTED GRAPH TRAVERSAL USING CONTENT-ADDRESSABLE MEMORY	16434983	06/07/2019	11218574	01/04/2022
EARLY ACKNOWLEDGMENT FOR WRITE OPERATIONS	16434898	06/07/2019	11025445	06/01/2021
FLEXIBLE PROCESSING OF NETWORK PACKETS	16352492	03/13/2019	11038993	06/15/2021
RELIABLE COMMUNICATIONS USING A POINT TO POINT PROTOCOL	16295902	03/07/2019	10798223	10/06/2020
FLEXIBLE RELIABILITY CODING FOR STORAGE ON A NETWORK	16265606	02/01/2019	10990478	04/27/2021
DATA PROCESSING UNIT HAVING HARDWARE-BASED PARALLEL VARIABLE-LENGTH CODEWORD DECODING	16249658	01/16/2019	10862513	12/08/2020
DURABLE BLOCK STORAGE IN DATA CENTER ACCESS NODES WITH INLINE ERASURE CODING	16215178	12/10/2018	10949303	03/16/2021
NETWORK ACCESS NODE VIRTUAL FABRICS CONFIGURED DYNAMICALLY OVER AN UNDERLAY NETWORK	16147099	09/28/2018	10904367	01/26/2021
SERVICE CHAINING HARDWARE ACCELERATORS WITHIN A DATA STREAM PROCESSING INTEGRATED CIRCUIT	16198607	11/21/2018	10929175	02/23/2021

Title	Application No.	Filing Date	Patent No.	Issue Date
STATIC DICTIONARY-BASED COMPRESSION HARDWARE PIPELINE FOR DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16200484	11/26/2018	11309908	04/19/2022
WORK UNIT STACK DATA STRUCTURES IN MULTIPLE CORE PROCESSOR SYSTEM FOR STREAM DATA PROCESSING	16197179	11/20/2018	10841245	11/17/2020
MERGING TECHNIQUES IN DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16195644	11/19/2018	10812630	10/20/2020
DATA STRIPING FOR MATCHING TECHNIQUES IN DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16195617	11/19/2018	10727865	07/28/2020
MATCHING TECHNIQUES IN DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16195564	11/19/2018	10997123	05/04/2021
DATA PROCESSING UNIT HAVING HARDWARE-BASED RANGE ENCODING AND DECODING	16178373	11/01/2018	10922026	02/16/2021
DATA PROCESSING UNIT HAVING HARDWARE-BASED RANGE ENCODING AND DECODING	16178341	11/01/2018	10511324	12/17/2019
DATA PROCESSING UNIT HAVING HARDWARE-BASED RANGE ENCODING AND DECODING	16711715	12/12/2019	10771090	09/08/2020
JPEG ACCELERATOR USING LAST-NON-ZERO (LNZ) SYNTAX ELEMENT	16179558	11/02/2018	10931958	02/23/2021
MEMORY LAYOUT FOR JPEG ACCELERATOR	16179496	11/02/2018	10848775	11/24/2020
RESILIENT NETWORK COMMUNICATION USING SELECTIVE MULTIPATH PACKET FLOW SPRAYING	16147134	09/28/2018	10965586	03/30/2021
FABRIC CONTROL PROTOCOL FOR DATA CENTER NETWORKS WITH PACKET SPRAYING OVER MULTIPLE ALTERNATE DATA PATHS	16147070	09/28/2018	11178262	11/16/2021
INLINE RELIABILITY CODING FOR STORAGE ON A NETWORK	16169736	10/24/2018	10761931	09/01/2020
DATA PROCESSING UNIT FOR COMPUTE NODES AND STORAGE NODES	16031921	07/10/2018	11303472	04/12/2022
PARALLEL CODING OF SYNTAX ELEMENTS FOR JPEG ACCELERATOR	16179472	11/02/2018	10827191	11/03/2020
WORK ALLOCATION FOR JPEG ACCELERATOR	16179529	11/02/2018	10827192	11/03/2020
REALIZATION OF A PROGRAMMABLE FORWARDING PIPELINE THROUGH PACKET HEADER SUMMARIES IN A DATA PROCESSING UNIT	16160609	10/15/2018	10958770	03/23/2021

Title	Application No.	Filing Date	Patent No.	Issue Date
MULTIMODE CRYPTOGRAPHIC PROCESSOR	16157265	10/11/2018	10951393	03/16/2021
DATA PROCESSING UNIT FOR STREAM PROCESSING	16031945	07/10/2018	10725825	07/28/2020
DETERMINISTIC FINITE AUTOMATA NODE CONSTRUCTION AND MEMORY MAPPING FOR REGULAR EXPRESSION ACCELERATOR	16035457	07/13/2018	10983721	04/20/2021
INCREMENTAL COMPILATION OF FINITE AUTOMATA FOR A REGULAR EXPRESSION ACCELERATOR	16035444	07/13/2018	10635419	04/28/2020
ARC CACHING FOR DETERMINISTIC FINITE AUTOMATA OF REGULAR EXPRESSION ACCELERATOR	16035416	07/13/2018	10645187	05/05/2020
INSTRUCTION-BASED NON-DETERMINISTIC FINITE STATE AUTOMATA ACCELERATOR	16035478	07/13/2018	10656949	05/19/2020
Access Node Integrated Circuit for Data Centers which Includes a Networking Unit, a Plurality of Host Units, Processing Clusters, a Data Network Fabric, and a Control Network Fabric	16031676	07/10/2018	10659254	05/19/2020
NON-BLOCKING ANY-TO-ANY DATA CENTER NETWORK WITH PACKET SPRAYING OVER MULTIPLE ALTERNATE DATA PATHS	15939227	03/28/2018	10686729	06/16/2020
RELAY CONSISTENT MEMORY MANAGEMENT IN A MULTIPLE PROCESSOR SYSTEM	15949892	04/10/2018	10565112	02/18/2020
EFFICIENT WORK UNIT PROCESSING IN A MULTICORE SYSTEM	15949692	04/10/2018	10540288	01/21/2020
NON-BLOCKING ANY-TO-ANY DATA CENTER NETWORK HAVING MULTIPLEXED PACKET SPRAYING WITHIN ACCESS NODE GROUPS	15939041	03/28/2018	10637685	04/28/2020
DATA CENTER NETWORK HAVING OPTICAL PERMUTORS	16579520	09/23/2019	10986425	04/20/2021
NON-BLOCKING, FULL-MESH DATA CENTER NETWORK HAVING OPTICAL PERMUTORS	15938767	03/28/2018	10425707	09/24/2019
BUFFER ALLOCATION AND MEMORY MANAGEMENT USING MAPPING TABLE	15347547	11/09/2016	10209900	02/19/2019
BUFFER ALLOCATION AND MEMORY MANAGEMENT	15269457	09/19/2016	10303375	05/28/2019
EFFICIENT WORK UNIT PROCESSING IN A MULTICORE SYSTEM	16746344	01/17/2020	11048634	06/29/2021
INSTRUCTION-BASED NON-DETERMINISTIC FINITE STATE AUTOMATA ACCELERATOR	16877102	05/18/2020	11010167	05/18/2021

Title	Application No.	Filing Date	Patent No.	Issue Date
DATA CENTER NETWORK HAVING OPTICAL PERMUTORS	17301842	-	-	-
RELAY CONSISTENT MEMORY MANAGEMENT IN A MULTIPLE PROCESSOR SYSTEM	16791957	02/14/2020	11360895	06/14/2022
EFFICIENT WORK UNIT PROCESSING IN A MULTICORE SYSTEM	17360619	-	-	-
HISTORY-BASED COMPRESSION PIPELINE FOR DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16195209	-	-	-
DATA PROCESSING UNIT FOR STREAM PROCESSING	16939617	-	-	-
RELIABILITY CODING WITH REDUCED NETWORK TRAFFIC	17302173	-	-	-
FABRIC CONTROL PROTOCOL FOR LARGE-SCALE MULTI-STAGE DATA CENTER NETWORKS	17304654	-	-	-
HASHING TECHNIQUES IN DATA COMPRESSION ACCELERATOR OF A DATA PROCESSING UNIT	16195290	11/19/2018	-	-
DATA CENTER NETWORK WITH MULTIPLEXED COMMUNICATION OF DATA PACKETS ACROSS SERVERS	16859765	04/27/2020	11469922	10/11/2022
ACCESS NODE FOR DATA CENTERS	16877050	05/18/2020	-	-
DATA CENTER NETWORK WITH PACKET SPRAYING	16901991	06/15/2020	-	-
RELIABILITY CODING FOR STORAGE ON A NETWORK	17007687	08/31/2020	11340985	05/24/2022
PIPELINE USING MATCH-ACTION BLOCKS	17061725	10/02/2020	-	-
RELIABLE COMMUNICATIONS USING A POINT TO POINT PROTOCOL	17063210	10/05/2020	11272041	03/08/2022
NETWORK ACCESS NODE VIRTUAL FABRICS CONFIGURED DYNAMICALLY OVER AN UNDERLAY NETWORK	17248354	01/21/2021	11412076	08/09/2022
MULTIMODE CRYPTOGRAPHIC PROCESSOR	17249823	03/15/2021	11405179	08/02/2022
RESILIENT NETWORK COMMUNICATION USING SELECTIVE MULTIPATH PACKET FLOW SPRAYING	17301185	03/29/2021	-	-
EARLY ACKNOWLEDGMENT FOR WRITE OPERATIONS	17333401	05/28/2021	-	-

EXHIBIT C

Trademarks

Mark	Serial No.	Filing Date	Registration No.	Registration Date
BARE METAL DATA CENTER	90459084	01/11/2021	-	-
EPIC	90458966	01/11/2021	-	-
EPC	90458954	01/11/2021	-	-
FUNGIBLE COMPOSER	90191092	09/18/2020	6512920	10/05/2021
OPEN AI FABRIC	90818100	07/08/2021	-	-
AI FABRIC	90818084	07/08/2021	-	-
OPEN DATA FABRIC	90818066	07/08/2021	-	-
DATA FABRIC	90818042	07/08/2021	-	-
BMDC	90459089	01/11/2021	-	-
TRUEAGILITY	90458914	01/11/2021	-	-
FUNGIBLE	90132270	08/24/2020	-	-
FUNGIBLE DATA CENTER	90655430	04/19/2021	-	-
FUNGIBLE COMPOSER	90191115	09/18/2020	6740397	05/24/2022
EDGE PUBLIC INFRASTRUCTURE CLOUD	90458956	01/11/2021	-	-
EDGE PUBLIC CLOUD	90458942	01/11/2021	-	-
FUNGIBLE BLOCK STORE	88830441	03/11/2020	-	-
FUNGIBLE OBJECT STORE	88830445	03/11/2020	-	-
FUNGIBLE FILE STORE	88848499	03/26/2020	-	-
FUNGIBLE OBJECT STORE	88848490	03/26/2020	-	-
FUNGIBLE BLOCK STORE	88848483	03/26/2020	-	-
ETHERFABRIC	88813074	02/27/2020	-	-
FUNGIBLE FILE STORE	88830449	03/11/2020	-	-
BARE METAL VIRTUALIZED DATA CENTERS	88698104	11/19/2019	-	-
BMVDC	88698109	11/19/2019	-	-
BM-VDC	88698105	11/19/2019	-	-
DPU-ENABLED TRUEFABRIC	88624804	09/20/2019	-	-
DYNAMICALLY COMPOSABLE DATA CENTER	88624800	09/20/2019	-	-
TRUEFABRIC	88624794	09/20/2019	6246661	01/12/2021
FUNGIBLE DPU	87728631	12/20/2017	6295571	03/16/2021
FUNGIBLE DATA PROCESSING UNIT	87728620	12/20/2017	6295570	03/16/2021
FUNGIBLE	87426380	04/26/2017	5857908	09/10/2019
FUNOS	87491250	06/15/2017	5852129	09/03/2019

EXHIBIT D

Mask Works

None.