

## TRADEMARK ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

ETAS ID: TM847851

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	INTELLECTUAL PROPERTY SECURITY AGREEMENT		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
BLAIZE, INC.		07/03/2023	Corporation: DELAWARE
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	BESS VENTURES AND ADVISORY, LLC		
<b>Street Address:</b>	1928 SUNSET HARBOUR DRIVE		
<b>City:</b>	MIAMI BEACH		
<b>State/Country:</b>	FLORIDA		
<b>Postal Code:</b>	33139		
<b>Entity Type:</b>	Limited Liability Company: FLORIDA		
<b>Name:</b>	BURTECH LP LLC		
<b>Street Address:</b>	1300 PENNSYLVANIA AVENUE, SUITE 700		
<b>City:</b>	WASHINGTON D		
<b>State/Country:</b>	D.C.		
<b>Postal Code:</b>	94403		
<b>Entity Type:</b>	Limited Liability Company: DELAWARE		
<b>Name:</b>	FRANKLIN TEMPLETON VARIABLE INSURANCE PRODUCTS TRUST - FRANKLIN SMALL-MID CAP GROWTH VIP FUND		
<b>Street Address:</b>	One Franklin Parkway		
<b>Internal Address:</b>	C/O FRANKLIN ADVISERS, INC.		
<b>City:</b>	San Mateo		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	94403		
<b>Entity Type:</b>	Statutory Trust: DELAWARE		
<b>Name:</b>	FRANKLIN STRATEGIC SERIES - FRANKLIN SMALL-MID CAP GROWTH FUND		
<b>Street Address:</b>	One Franklin Parkway		
<b>Internal Address:</b>	C/O FRANKLIN ADVISERS, INC.		
<b>City:</b>	San Mateo		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	94403		
<b>Entity Type:</b>	Statutory Trust: DELAWARE		

OP \$240.00 97329926

**PROPERTY NUMBERS Total: 9**

Property Type	Number	Word Mark
Serial Number:	97329926	BLAIZE AI STUDIO
Serial Number:	97845945	BLAIZE PATHFINDER
Serial Number:	97845959	PICASSO
Serial Number:	97845957	XPLORER
Serial Number:	87553894	THINCI
Serial Number:	88723197	GSP
Serial Number:	88604078	BLAIZE
Serial Number:	88627862	BLAIZE
Serial Number:	88859414	GRAPH STREAMING PROCESSOR

**CORRESPONDENCE DATA****Fax Number:**

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

**Email:** ipdocket@lw.com, kristin.azcona@lw.com**Correspondent Name:** LATHAM & WATKINS LLP**Address Line 1:** 650 Town Center Drive, 20th Fl**Address Line 4:** Costa Mesa, CALIFORNIA 92626

<b>ATTORNEY DOCKET NUMBER:</b>	074280-000
<b>NAME OF SUBMITTER:</b>	Kristin J. Azcona
<b>SIGNATURE:</b>	/KJA/
<b>DATE SIGNED:</b>	10/23/2023

**Total Attachments: 28**

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**INTELLECTUAL PROPERTY SECURITY AGREEMENT**

**THIS INTELLECTUAL PROPERTY SECURITY AGREEMENT** (“Agreement”) dated as of July 3, 2023, is made by Blaize, Inc., a Delaware corporation (the “Grantor”), in favor of each Lender party to a Note (as defined below) that is a signatory hereto (each a “Secured Party” and collectively the “Secured Parties”).

RECITALS

A. Pursuant to that certain Note Purchase Agreement, dated as of July 3, 2023, by and between the Grantor and the Lenders party thereto from time to time (as amended and/or restated from time to time, the “Note Purchase Agreement”), Grantor has issued a Secured Convertible Promissory Note for the benefit of each Secured Party (as amended and/or restated from time to time, each a “Note” and collectively, the “Notes”). All capitalized terms used but not defined herein shall have the respective meanings given to them in the Note Purchase Agreement.

B. Pursuant to the terms of the Notes and that certain Security Agreement, dated as of July 3, 2023, by and between the Grantor and the Secured Parties party thereto (the “Security Agreement”), Grantor has granted to the Secured Parties a security interest in all of Grantor’s right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral (as defined in the Security Agreement).

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Notes, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

1. Grant of Security Interest. To secure its obligations under the Notes, Grantor grants and pledges to the Secured Parties a security interest in all of Grantor’s right, title and interest in, to and under its intellectual property (all of which shall collectively be called the “Intellectual Property Collateral”), including, without limitation, the following:

(a) Any and all copyright rights, copyright applications, copyright registrations and like protections in each work of authorship and derivative work thereof, whether published or unpublished and whether or not the same also constitutes a trade secret, now or hereafter existing, created, acquired or held, including without limitation those set forth on Exhibit A attached hereto (collectively, the “Copyrights”);

(b) Any and all trade secrets, and any and all intellectual property rights in computer software and computer software products now or hereafter existing, created, acquired or held;

(c) Any and all design rights that may be available to Grantor now or hereafter existing, created, acquired or held;

(d) All patents, patent applications and like protections including, without limitation, improvements, divisions, continuations, renewals, reissues, extensions, re-examination certificates, utility models, and continuations-in-part of the same, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the “Patents”);

(e) Any trademark and servicemark rights, whether registered or not, applications to register and registrations of the same and like protections, and the entire goodwill of the business of Grantor connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the “Trademarks”);

(f) All mask works or similar rights available for the protection of semiconductor chips, now owned or hereafter acquired, including, without limitation those set forth on Exhibit D attached hereto (collectively, the “Mask Works”);

(g) Any and all claims for damages by way of past, present and future infringements of any of the rights included above, with the right, but not the obligation, to sue for and collect such damages for said use or infringement of the intellectual property rights identified above;

(h) All licenses or other rights to use any of the Copyrights, Patents, Trademarks, or Mask Works and all license fees and royalties arising from such use to the extent permitted by such license or rights;

(i) All amendments, renewals and extensions of any of the Copyrights, Trademarks, Patents, or Mask Works; and

(j) All proceeds and products of the foregoing, including without limitation all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

Notwithstanding the foregoing, the Intellectual Property Collateral does not include any United States intent-to-use trademark or service mark application to the extent that, and solely during the period in which, the grant of a security interest therein would impair the validity or enforceability of such intent-to-use trademark or service mark application under United States federal law.

2. Recordation. Grantor authorizes the Commissioner for Patents, the Commissioner for Trademarks and the Register of Copyrights and any other government officials to record and register this Agreement upon request by the Secured Parties.

Grantor hereby authorizes the Secured Parties to (a) modify this Agreement unilaterally by amending the exhibits to this Agreement to include any Intellectual Property Collateral which Grantor obtains subsequent to the date of this Agreement and (b) file a duplicate original of this Agreement containing amended exhibits reflecting such new Intellectual Property Collateral.

3. Transaction Documents. This Agreement has been entered into pursuant to and in conjunction with the Notes and the Security Agreement, which are hereby incorporated by reference. The provisions of the Security Agreement shall supersede and control over any conflicting or inconsistent provision herein. The rights and remedies of the Secured Parties with respect to the Intellectual Property Collateral are as provided by the Security Agreement and related documents, and nothing in this Agreement shall be deemed to limit such rights and remedies.

4. Execution in Counterparts. This Agreement and any amendments, waivers, consents or supplements hereto may be executed in any number of counterparts, and by different parties hereto in separate counterparts, each of which when so delivered shall be deemed an original, but all of which counterparts shall constitute but one and the same instrument. Delivery of an executed counterpart of a signature page of this Agreement by facsimile, portable document format (.pdf) or other electronic transmission will be as effective as delivery of a manually executed counterpart hereof.

5. Successors and Assigns. The provisions of this Agreement shall inure to the benefit of the parties hereto and their respective successors and assigns. Either party may assign, transfer, or endorse its rights hereunder subject to prior notice to the other party, and all of such rights shall inure to the benefit of such party's successors and assigns.

6. Governing Law. This Agreement has been negotiated and delivered to the Secured Parties in the State of Delaware, and shall have been accepted by the Secured Parties in the State of Delaware. This Agreement shall be governed by, and construed and enforced in accordance with, the laws of the State of Delaware, excluding conflict of laws principles that would cause the application of laws of any other jurisdiction.

7. Electronic Execution of Certain Other Documents. The words "execution," "execute", "signed," "signature," and words of like import in or related to any document to be signed in connection with this Agreement and the transactions contemplated hereby (including without limitation assignments, assumptions, amendments, waivers and consents) shall be deemed to include electronic signatures, the electronic matching of assignment terms and contract formations on electronic platforms approved by the Secured Parties, or the keeping of records in

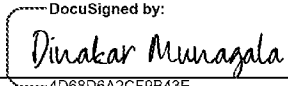
electronic form, each of which shall be of the same legal effect, validity or enforceability as a manually executed signature or the use of a paper-based recordkeeping system, as the case may be, to the extent and as provided for in any applicable law, including the Federal Electronic Signatures in Global and National Commerce Act, the New York State Electronic Signatures and Records Act, or any other similar state laws based on the Uniform Electronic Transactions Act.

[Signature page follows.]

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

GRANTOR:

**BLAIZE, INC.**

By:   
Name: 4D68D6A2CF9B43E... Dinakar Munagala  
Title: Chief Executive Officer

Address:

Blaize, Inc.  
4370 Town Center Blvd, Suite 240  
El Dorado Hills, CA 95762  
Attention: Dinakar Munagala, Chief  
Executive Officer

with a copy (which shall not constitute  
notice) to:

Latham & Watkins LLP,  
505 Montgomery Street, Suite 2000  
San Francisco, California 94111  
Attn: Attention: Brian Patterson  
Email: brian.patterson@lw.com

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

SECURED PARTY:

**BESS VENTURES AND ADVISORY, LLC, a  
Florida limited liability company**

DocuSigned by:

*Lane M. Bess*

By: \_\_\_\_\_  
767DA9C212B343A

Name: Lane Bess

Title: Owner-Manager

Address: 1928 Sunset Harbour Drive  
Miami Beach, FL 33139

Email Address:



IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

SECURED PARTY:

**BURTECH LP LLC**

By:   
Name: Shahal Khan  
Title: Managing Member

Address: 1300 Pennsylvania Avenue, Suite 700  
Washington, DC 20004

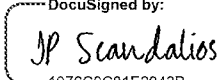
Email Address: shahal@burkhan.world

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

SECURED PARTY:

**FRANKLIN TEMPLETON VARIABLE  
INSURANCE PRODUCTS TRUST –  
FRANKLIN SMALL-MID CAP GROWTH  
VIP FUND**

**BY: FRANKLIN ADVISERS, INC., AS  
INVESTMENT MANAGER**

DocuSigned by:  
  
By: 1076C9C84E2943B  
Name: JP Scandalios  
Title: Senior Vice President

Address: c/o Franklin Advisers, Inc.

One Franklin Parkway

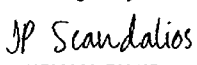
San Mateo, CA 94403

Email Address: [chris.chen@franklintempleton.com](mailto:chris.chen@franklintempleton.com);  
[jp.scandalios@franklintempleton.com](mailto:jp.scandalios@franklintempleton.com)

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

SECURED PARTY:

**FRANKLIN STRATEGIC SERIES –  
FRANKLIN SMALL-MID CAP GROWTH  
FUND  
BY: FRANKLIN ADVISERS, INC., AS  
INVESTMENT MANAGER**

DocuSigned by:  
  
By: \_\_\_\_\_  
Name: JP Scandalios  
Title: Senior Vice President

Address: c/o Franklin Advisers, Inc.

One Franklin Parkway

San Mateo, CA 94403

Email Address: [chris.chen@franklintempleton.com](mailto:chris.chen@franklintempleton.com);  
[jp.scandalios@franklintempleton.com](mailto:jp.scandalios@franklintempleton.com)

EXHIBIT A

Copyrights

None.

EXHIBIT B

Patents

[Attached]

**Exhibit B**  
**Patents**

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
THCI10001PR	61/355,768	Method and Apparatus for Graphic/Video Processing	06/17/2010		Koneru, Yin, Munagala	Lapsed	
ThinCI-1001	13/161,547	Processing of Graphics Data of a Server System for Transmission	6/16/2011	Claiming priority to 61/355,768 (06/17/2010)	Koneru, Yin, Munagala	Granted1	US Patent #8,754,900 granted on 06/17/2014
ThinCI-1001 (UK)	GB201322404D0	Processing of Graphics Data of a Server System for Transmission	12/14/2011	WO2012173650A1 claiming priority to 06/16/2011	Koneru, Yin, Munagala	Granted2	GB2510056B 2017-12-27
ThinCI-1001 (Korea)	KR1020147001108A	Processing of Graphics Data of a Server System for Transmission	12/14/2011	WO2012173650A1 claiming priority to 06/16/2011	Koneru, Yin, Munagala	Granted3	KR101898565B1 granted 2018-09-13
ThinCI-1002	14/287,036	Processing of Graphics Data of a Server System for Transmission Including Multiple Rendering Passes	5/25/2014	CIP of 13/161,547, which Claims priority to 61/355,768 (06/17/2010)	Koneru, Yin, Munagala	Granted4	Granted as US Patent #9,373,152 on 06/21/2016
ThinCI-1003P	61/166,507	Efficient Graph Processing	5/26/2015	Original Filing	Koneru, Cook, Yin, Munagala, Vennuri	Lapsed	
ThinCI-1003	15/164,848	CONFIGURABLE SCHEDULER FOR MULTI-NODE COMPUTING SYSTEMS	5/25/2016	Claiming priority to 61/355,768 (06/17/2010)	Koneru, Cook, Yin	Granted5	US Patent #10,437,637 granted on 10/08/2019
ThinCI-1004	15/159,000	Selecting Data of a Server System for Transmission	5/19/2016	CON of 14/287,036 which is a CIP of 13/161,547, which Claims priority to 61/355,768 (06/17/2010)	Koneru, Yin, Munagala	Granted6	Granted as US Patent #9,640,150 on 05/02/2017
ThinCI-1005	15/465,660	Selecting Data of a Server System for Transmission	3/22/2017	CON of 15/159,000 which is a CON of 14/287,036 which is a CIP of 13/161,547, which Claims priority to 61/355,768 (06/17/2010)	Koneru, Yin, Munagala	Abandoned - filed as a place holder for additional applications	

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
ThinCI-1006	15/679,316	Processing a Plurality of Threads of a Single Instruction Multiple Data Group	8/17/2017	CIP of 15/465,660 which is a CON of 15/159,000 which is a CON of 14/287,036 which is a CIP of 13/161,547, which Claims priority to 61/355,768 (06/17/2010)	Koneru, Yin	Abandoned - per discussion with Satyaki	
ThinCI-1007-PROV	62/712,911	Intelligent Power-Over-Ethernet (POE) Switch	7/31/2018	Original Filing	Terrill	Lapsed - Non-provisional patent application will not be filed per Patent Group 06/12/2019	
ThinCI-1008	16/270,766	Accelerated Operation of a Graph Streaming Processor	2/8/2019	CIP of 15/164,848, which Claims priority to 62/166,507 (05/26/2015)	Agarwall, Govindannagari, Puppala, Koneru	Granted <sup>7</sup>	Granted as US Patent #11,150,961 on 10/19/2021
ThinCI-CON-1008	17/399,184	Accelerated Operation of a Graph Streaming Processor	8/11/2021	CON of ThinCI-1008	Agarwall, Govindannagari, Puppala, Koneru	Granted <sup>8</sup>	Granted as US Patent #11,593,184 on 02/28/2023
ThinCI-1009	16/282,464	Cascading of Graph Streaming Processors	2/22/2019	CIP of 16/270,766, and CIP of 15/164,848, which Claims priority to 62/166,507 (05/26/2015)	Puppala, Govindannagari, Agarwall, Koneru	Granted <sup>9</sup>	Granted as US Patent #11,379,262 on 07/05/2022
ThinCI-1009-DIV	17/833,981	Cascading of Graph Streaming Processors	6/7/2022	DIV od 1009	Puppala, Govindannagari, Agarwall, Koneru	Pending -P1	
ThinCI-1003-CIP	16/383,614	CONFIGURABLE SCHEDULER IN A GRAPH STREAM PROCESSING SYSTEM	4/14/2019	CIP of 16/270,766, and CIP of 15/164,848, which Claims priority to 62/166,507 (05/26/2015)	Koneru, Cook, Yin	Granted <sup>10</sup>	Granted as US Patent #11,416,282 on 08/16/2022
ThinCI-1010	16/398,567	Reduction of a Number of Stages of a Graph Streaming Processor	4/30/2019	CIP of 16/270,766, and CIP of 15/164,848, which Claims priority to 62/166,507 (05/26/2015)	Agarwall, Govindannagari, Puppala, Koneru	Granted <sup>11</sup>	Granted as US Patent #11,436,045 on 09/06/2022

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
ThinCI-1010-CON	17/866,476	Reduction of a Number of Stages of a Graph Streaming Processor	7/16/2022	CON of ThinCI-1010	Agarwall, Govindannagari, Puppala, Koneru	Granted 12	Granted as US Patent #11,669,366 on 06/06/2023
ThinCI-1011	16/693,092	Iterating Single Instruction, Multiple-Data (SIMD) Instructions	11/22/2019		Satyaki Koneru, Kamaraj Thangam	Granted 13	Granted as US Patent #10,996,960 on 05/04/2021
ThinCI-1012	16/706,753	CONFIGURABLE LOGICAL REGISTER	12/8/2019		Kamaraj Thangam, Srinivasulu Nagisetty, Venkata Divya Bharathi Palaparthi, Aswathy Asok, Satyaki Koneru	Granted 14	Granted as US Patent #11,366,664 on 06/21/2022
ThinCI-1012-CON	17/749,268	CONFIGURABLE LOGICAL REGISTER	5/20/2022		Kamaraj Thangam, Srinivasulu Nagisetty, Venkata Divya Bharathi Palaparthi, Aswathy Asok, Satyaki Koneru	Pending -P2	Pending - response to OA due 08/17/2023
ThinCI-1014	16/535,309	Reducing Operations of Sum-Of-Multiply-Accumulate (SOMAC) Instructions	8/8/2019		Kamaraj Thangam, Palaparthi Venkata Divya Bharathi, Satyaki Koneru	Granted 15	Granted as US Patent #11,481,223 on 10/25/2022
ThinCI-1013-Prov	62/834,326	Multilevel Control of Synthetic Videos Using a Hybrid Strategy That Combines Semantic Description with Direct Access to the Latent Space	4/15/2019		Balabin, Zakharchenko, Thakur	Lapsed - per Satyaki, Ke 03/19/2020	
THINCI-P001	14/327,432	Mechanism for minimal computation and power consumption for rendering synthetic 3D images, containing pixel overdraw and dynamically generated intermediate images	7/9/2014	Claiming priority to 61/844,568 filed 07/10/2013	Val Cook, Satyaki Koneru, Ke Yin, Diakar Munegala	Granted 16	Granted as US Patent #9,589,388 on 03/07/2017



Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
THINCI-P001B	15/450,959	A Node Topology Employing Recirculating Ring Command and Data Buffers for Executing Thread Scheduling	3/6/2017	Original Filing	Val Cook, Satyaki Koneru, Ke Yin, Dinakar Munagala	Granted 17	Granted as US Patent #10,311,542 on 06/04/2019
THINCI-P001B-CON	16/416,179	A Node Topology Employing Recirculating Ring Command and Data Buffers for Executing Thread Scheduling	5/18/2019	CON of THINCI-P001B	Val Cook, Satyaki Koneru, Ke Yin, Dinakar Munagala	Granted 18	Granted as US Patent #10,540,740 on 01/21/2020
ThinCI-1003-CON	16/505,381	CONFIGURABLE SCHEDULER FOR MULTI-NODE COMPUTING SYSTEMS	7/8/2019	CON of ThinCI-1003	Satyaki Koneru, Val Cook, Ke Yin	Granted 19	Granted as US Patent #11,126,462 on 09/21/2021
ThinCI-1003-CON2	17/396,721	CONFIGURABLE SCHEDULER FOR MULTI-NODE COMPUTING SYSTEMS	8/8/2021	CON of ThinCI-1003-CON	Satyaki Koneru, Val Cook, Ke Yin	Pending - P3-Response due 06/29/2023	
THINCI-P001B-CON-CON	16/699,045	A Node Topology Employing Recirculating Ring Command and Data Buffers for Executing Thread Scheduling	11/28/2019	CON of THINCI-P001B-CON	Val Cook, Satyaki Koneru, Ke Yin, Dinakar Munagala	Granted 20	Granted as US Patent #10,740,868 on 08/11/2020
THINCI-P001B-CON3	16/907,698	Node Topology Employing Command and Data Buffers for Executing Thread Scheduling	6/22/2020	CON of THINCI-P001B-CON-CON	Val Cook, Satyaki Koneru, Ke Yin, Dinakar Munagala	Granted 21	Granted as US Patent #11,151,684 on 10/19/2021
THINCI-P001B-CON4	17/397,134	Node Topology Employing Command and Data Buffers for Executing Thread Scheduling	8/9/2021	CON of THINCI-P001B-CON3	Val Cook, Satyaki Koneru, Ke Yin, Dinakar Munagala	Granted 22	Granted as US Patent #11,593,911 on 02/28/2023
ThinCI-1015	16/930,192	A mechanism for shared loads across multiple threads	7/16/2020	CIP of ThinCI-1014	Satyaki Koneru, Kamaraj Thangam, Sruthikesh Surinemi	Granted 23	Granted as US Patent #11,416,282 on 08/16/2022
ThinCI-1016	17/091,055	CONFIGURABLE SCHEDULER WITH PREFETCH AND INVALIDATE THREADS IN A GRAPH STREAM PROCESSING SYSTEM	11/6/2020		Satyaki Koneru	Granted 24	Granted as US Patent #11,513,845 on 11/11/2022

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
ThinCI-1016-CON	17/976,817	CONFIGURABLE SCHEDULER WITH PRE-FETCH AND INVALIDATE THREADS IN A GRAPH STREAM PROCESSING SYSTEM	10/30/2022	CON of ThinCI-1016	Satyaki Koneru	Pending - P4- Allowed - Issue fee due 07/26/2023	
ThinCI-1017-Prov	63/011,408	A method for obtaining custom artificial neural network architectures	4/17/2020		Ilya A. Balabin, Adam P. Geringer, Dmitry Zakharchenko	Lapsed Converted 11/13/2020	
ThinCI-1017	17/097,249	Obtaining custom artificial neural network architectures	11/13/2020		Ilya A. Balabin, Adam P. Geringer, Dmitry Zakharchenko	Pending P5	
ThinCI-1018-Prov	63/029,509	A METHOD FOR DISCOVERING NOVEL ARTIFICIAL NEURAL NETWORK ARCHITECTURES BY MEANS OF GUIDED EVOLUTIONARY GROWTH	5/24/2020		Ilya A. Balabin, Adam P. Geringer	Lapsed Converted 02/23/2021	
ThinCI-1018	17/182,334	Discovering Novel Artificial Neural Network Architectures	2/23/2021		Ilya A. Balabin, Adam P. Geringer	Pending - P6	
ThinCI-1019 (also filed in India)K&S IP50129	16/994,082	A Method of Using Multidimensional Blockification to Optimize Computer Program and Device Thereof	8/14/2020 claiming to India filing dated 05/27/2020		Ravi Korasa, Aravind Rajulapudi, Pethikonda Datta Nagraj	Granted 25	Granted as US Patent #11,327,733 on 05/10/2022
ThinCI-1019-CON	17/713,310	A Method of Using Multidimensional Blockification to Optimize Computer Program and Device Thereof	4/5/2022		Ravi Korasa, Aravind Rajulapudi, Pethikonda Datta Nagraj	Granted 26	Granted as US Patent #11,640,285 on 05/02/2023
ThinCI-1020-Prov	63/019,279	System and Methods to classify, detect, diagnose, track and follow-up flu like symptoms (Pneumonia, COVID-19), diseases progression analysis	5/2/2020		Deepak Chandra Bijalwan, Dinakar C. Munagala	Lapsed Converted 04/23/2021	

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
ThinCI-1020	17/238,289	METHOD AND SYSTEMS FOR PREDICTING MEDICAL CONDITIONS AND FORECASTING RATE OF INFECTION OF MEDICAL CONDITIONS VIA ARTIFICIAL INTELLIGENCE MODELS USING GRAPH STREAM PROCESSORS	4/23/2021		Deepak Chandra Bijalwan, Dinakar C. Munagala	Pending - P7	
ThinCI-1021	17/219,926	Iterating Group Sum of Multiple Accumulate Operations	4/1/2021		Satyaki Koneeru, Kamaraj Thangam	Granted 27	Granted as US Patent #11,307,860 on 04/19/2022
ThinCI-1021-CON	17/696,778	Iterating Group Sum of Multiple Accumulate Operations	3/16/2022		Satyaki Koneeru, Kamaraj Thangam	Granted 28	Granted as US Patent #11,593,114 on 02/28/2023
ThinCI-1022	17/544,087	SOC Power Adjust based on Sensed Voltage Transients	12/7/2021		Sebastian Ciesluk	Pending P8	
ThinCI-1023-Priv	63/235,789	Quantization of Floating-point Neural Network to Fixed-point Neural Network for Inference	8/22/2021		Deepak Chandra Bijalwan, Pratyusha Musunuri	Lapsed	
ThinCI-1023 (K&S IP60298)	17/699,255	A METHOD AND MACHINE LEARNING SYSTEM TO PERFORM QUANTIZATION OF NEURAL NETWORK	3/21/2022		Deepak Chandra Bijalwan, Pratyusha Musunuri	Pending P9	
ThinCI-1024 (K&S IP56446)	17/668,161	A METHOD OF OPTIMIZING REGISTER MEMORY ALLOCATION FOR VECTOR INSTRUCTIONS AND A SYSTEM THEREOF	2/9/2022		Pathikonda Datta Nagaraj, Aravind Rajulapudi, Ravi korsa	Pending P10	
ThinCI-1025 (K&S IP50130)	17/668,204	A METHOD OF OPTIMIZING SCALAR REGISTER ALLOCATION AND A SYSTEM THEREOF	2/9/2022		Pathikonda Datta Nagaraj, Aravind Rajulapudi, Ravi korsa	Pending P11	

Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
ThinCI-1026-Prov	63/393,429	System for Operationalizing Computer Vision AI/ML Applications via Data Drift	7/29/2022		Adam Geringer	Pending P12	
ThinCI-1026	17/954,410	Unsupervised Data Drift Engine	9/28/2022		Adam Geringer	Pending P13	
ThinCI-1027-Prov (K&S IP62502)	63/163,342	ANN model layers datatype sensitivity estimation and automatic selection of mixed datatype using layer-specific loss for inference	3/3/2022	Pending - Conversion Date 03/03/2023	Deepak Chandra Bijalwan, Mounika Gude, Pratyusha Musumuru	Converted - see below	
ThinCI-1027 (K&S IP62502)	18/072,785	A METHOD AND SYSTEM FOR GENERATING A MIXED PRECISION MODEL	12/1/2022	Pending	Deepak Chandra Bijalwan, Mounika Gude, Pratyusha Musumuru	Pending P14	
ThinCI-India-01 (K&S IP50130) (US ThinCI-1025 - Pending)	2.02244E+11	A METHOD OF OPTIMIZING SCALAR REGISTER ALLOCATION AND A SYSTEM THEREOF	6/6/2022 Claiming priority to US Filing 02/09/2022		Pathikonda Datta Nagraj, Aravind Rajulapudi, Ravi korsa	Pending - filed by counsel in India - Request for Examination due 02/09/2026 P15	
ThinCI-India-02 (US ThinCI-1019 - Granted)	2.02041E+11	A Method of Using Multidimensional Blockification to Optimize Computer Program and Device Thereof	5/27/2020		Ravi Korsa, Aravind Rajulapudi, Pathikonda Datta Nagraj	Pending - filed by counsel in India - P16	
ThinCI-India-03 (K&S IP60298) (US ThinCI-1023 - Pending)	2.02244E+11	A METHOD AND MACHINE LEARNING SYSTEM TO PERFORM QUANTIZATION OF NEURAL NETWORK	4/29/2022		Pathikonda Datta Nagraj, Aravind Rajulapudi, Ravi korsa	Pending - filed by counsel in India - P17	
ThinCI-India-04 (K&S IP56446) (US ThinCI-1024 - Pending)	2.02244E+11	A METHOD OF OPTIMIZING REGISTER MEMORY ALLOCATION FOR VECTOR INSTRUCTIONS AND A SYSTEM THEREOF	6/6/2022 Claiming priority to US Filing 02/09/2022		Pathikonda Datta Nagraj, Aravind Rajulapudi, Ravi korsa	Pending - filed by counsel in India - P18	
ThinCI-India-05 - Provisional application filed for the Indian Patent	2.02241E+11	Method of slice wrap buffer control for graph streaming through DNN accelerator	6/8/2022			Pending - filed by counsel in India - P19	

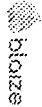
Attorney Docket Number	Application Serial Number	Title	Filing Date	Priority Date	Inventors	Status	Granted Patent Number
application no.    [Title: ]    K&S Ref: [IP64/763]							
ThinCI-India-06 - Provisional application filed for the Indian Patent application no.202241035865    [Title:    K&S Ref: [IP64/762]/HRA/sb	2.0224IE+11	Graph Streaming DNN Accelerator	6/22/2022			Pending - filed by counsel in India - P20	

EXHIBIT C

Trademarks

[Attached]

**Exhibit C**  
Trademarks

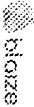
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3342_TM0001Madrid	ThinCI	Word Mark	CN, EU and JP	87553894 (TM00001)	1401160	1401160	02/02/2018	10/05/2018	02/02/2038	CN: Refusal 12/29/2018 EU: Granted 11/01/2018 (to be abandoned) JP: Cancelled 09/26/2019 per client's instruction	IC 09
3342_TM0005_CN	Blaize	Word Mark	China	88604078 (3342_TM00005)	599052777		2/28/2020			Registered	IC 09
3342_TM0006Madrid		Image Mark	Canada	88627862 (3342_TM00006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Filed 03/29/22, published for opposition	IC 09
3342_TM0008	Blaize AI Studio	Word Mark	US	None	97329926		03/24/2022			Pending; published for opposition 02/28/23 for 30 days	IC 09 and IC 042
3342_TM0008Madrid	Blaize AI Studio	Word Mark	WIPO	3342_TM0008	1662709	1662709	03/31/2022			Pending	IC 09 and IC 042
3342_TM0008Madrid	Blaize AI Studio	Word Mark	Philippines	3342_TM0008	1662709		03/31/2022			Registered - in a period of opposition from 24 March 2023 to 24 May 2023	IC 09 and IC 042
3342_TM0008Madrid	Blaize AI Studio	Word Mark	Malaysia	3342_TM0008	1662709		03/31/2022			Pending	IC 09 and IC 042









PTIP Ref.	Mark	Type	Country	Priority	Appl No.	Reg. No.	Filing Date	Regi. Date	Renewal Date	Status	Classes
3342_TM/0008/Madrid_CA	Blaize AI Studio	Word Mark	Canada	3342_TM/0008	1662709	1662709	03/31/2022			Registered - Notice of Grant and Publication received from Canadian Intellectual Property Office	IC 09 and IC 042
3342_TM/0008/Madrid_CN	Blaize AI Studio	Word Mark	China	3342_TM/0008	1662709		03/31/2022			Response filed to Provisional Rejection	IC 042
3342_TM/0008/Madrid_EU	Blaize AI Studio	Word Mark	European Union	3342_TM/0008	1662709		03/31/2022	03/24/2023		Registered	IC 09 and IC 042
3342_TM/0008/Madrid_JP	Blaize AI Studio	Word Mark	Japan	3342_TM/0008	1662709		03/31/2022	03/24/2023		Registered	IC 09 and IC 042
3342_TM/0008/Madrid_IN	Blaize AI Studio	Word Mark	India	3342_TM/0008	1662709		03/31/2022			Pending, Provisional Refusal 06/29/22; response filed 07/22; Accepted 01/25/23; published for opposition for 4 months	IC 09 and IC 042
3342_TM/0008/Madrid_KR	Blaize AI Studio	Word Mark	Korea	3342_TM/0008	1662709		03/31/2022			Pending	IC 09 and IC 042
3342_TM/0008/Madrid_SG	Blaize AI Studio	Word Mark	Singapore	3342_TM/0008	1662709	1662709	03/31/2022	03/24/2023		Registered	IC 09 and IC 042
3342_TM/0008/Madrid_GB	Blaize AI Studio	Word Mark	United Kingdom	3342_TM/0008	1662709	1662709	03/31/2022	03/24/2023		Registered	IC 09 and IC 042
3342_TM/0012	Blaize Pathfinder	Word Mark	US	None	97845945	1726398	3/19/2023	03/27/2023		Pending	IC 09
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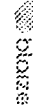
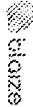
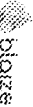
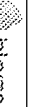
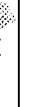

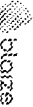


PTIP Ref.	Mark	Type	Country	Priority	Appl. No.	Reg. No.	Filing Date	Regi. Date	Renewal Date	Status	Classes
3342_TM0012Madrid CN	BlaiZe Pathfinder	Word Mark	China	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid EU	BlaiZe Pathfinder	Word Mark	European Union	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid JP	BlaiZe Pathfinder	Word Mark	Japan	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid IN	BlaiZe Pathfinder	Word Mark	India	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid KR	BlaiZe Pathfinder	Word Mark	Korea	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid SG	BlaiZe Pathfinder	Word Mark	Singapore	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid GB	BlaiZe Pathfinder	Word Mark	United Kingdon	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid PH	BlaiZe Pathfinder	Word Mark	Philippines	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid MY	BlaiZe Pathfinder	Word Mark	Malaysia	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0012Madrid CA	BlaiZe Pathfinder	Word Mark	Canada	3342_TM 0012	16978352 01	1726398	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0013	Picasso	Word Mark	US	None	97845959		3/19/2023			Pending	IC 09; IC 42
3342_TM0013Madrid	Picasso	Word Mark	WIPO	3342_TM 0013	16978332 01	1728103	03/27/2023	03/27/2023		Registered	IC 09; IC 42
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3342_TM0013Madrid JP	Picasso	Word Mark	Japan	3342_TM 0013	16978332 01	1728103	03/27/2023	03/27/2023		Registered	IC 09; IC 42
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3342_TM0013Madrid SG	Picasso	Word Mark	Singapore	3342_TM 0013	16978332 01	1728103	03/27/2023	03/27/2023		Registered	IC 09; IC 42

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3342_TM0013Madrid MY	Picasso	Word Mark	Malaysia	3342_TM 0013	16978332 01	1728103	03/27/2023	03/27/2023		Registered	IC 09, IC 42
3342_TM0013Madrid CA	Picasso	Word Mark	Canada	3342_TM 0013	16978332 01	1728103	03/27/2023	03/27/2023		Registered	IC 09, IC 42
3342_TM0014	Xplorer	Word Mark	US	None	97845957		3/19/2023			Registered	IC 09
3342_TM0014Madrid	Xplorer	Word Mark	WIPO	3342_TM 0014	16978312 01	1726397	03/27/2023	03/27/2023		Registered	IC 09
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3342_TM0014Madrid JP	Xplorer	Word Mark	Japan	3342_TM 0014	16978312 01	1726397	03/27/2023	03/27/2023		Registered	IC 09
3342_TM0014Madrid IN	Xplorer	Word Mark	India	3342_TM 0014	16978312 01	1726397	03/27/2023	03/27/2023		Registered	IC 09
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3342_TM0001	ThinCI	Word Mark	US	None	87553894	5,837,177	08/02/2017	08/20/2019	08/20/2025	Registered (to be abandoned)	IC 09

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3342_TM0002	GSP	Word Mark	US	None	88723197	6134755	12/1/2019	8/25/2020	8/25/2026 (section 8& 15)	Registered	IC 09
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3342_TM0006		Image Mark	US	None	88627862	6,083,769	9/23/2019	6/23/2020	06/23/2025 (section 8& 15)	Registered	IC 09
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3342_TM0005Madrid_IN		Word Mark	India	88604078 (3342_T M0005)	1523234	1523234	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
3342_TM0005Madrid_PH		Word Mark	Philippines	88604078 (3342_T M0005)	1523234	1523234	2/28/2020	2/28/2020	2/28/2030	Registered, actual use evidence due in 2023  ACCEPTANCE OF DECLARATION OF ACTUAL USE - 3-31-23	IC 09
3342_TM0005Madrid_MY		Word Mark	Malaysia	None	1523234	1,523,234	07/13/2021			Pending, filed 07/13/21, based on 3342_TM0005	IC 09
3342_TM0006Madrid		Image Mark	EU, UK, China, Japan, Korea, Singapore, India, and the Philippine	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
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3342_TM0006Madrid_UK		Image Mark	UK	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
3342_TM0006Madrid_CN		Image Mark	China	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09

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3342_TM0006Madrid_KR		Image Mark	South Korea	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
3342_TM0006Madrid_SG		Image Mark	Singapore	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
3342_TM0006Madrid_IN		Image Mark	India	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Registered	IC 09
3342_TM0006Madrid_PH		Image Mark	Philippines	88627862 (3342_T M0006)	1524665	1524665	2/28/2020	2/28/2020	2/28/2030	Granted@12/02/20; Actual Use Evidence in Philippine by 2/28/2023	IC 09
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3342_TM0005TW	Blazze	Word Mark	Taiwan	88604078 (3342_T M0005)	10901230	2092796	3/3/2020	10/16/2020	10/15/2030	Registered	IC 09
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3342_TM0007	GRAPH STREAMING PROCESSOR	Word Mark	US	None	88859414	6,324,571	4/3/2020	4/13/2021	4/13/2027 (Sec 8 &15)	Registered	IC 09


PTIP Ref.	Mark	Type	Country	Priority	Appl. No.	Reg. No.	Filing Date	Regt. Date	Renewal Date	Status	Classes
3342_TM0009TM_C N_百智思	百智思 (Bai Zhi Si)	Word Mark	China	None	56771483	56771483	06/08/2021	12/14/2021	12/14/2031	Registered	IC 09
3342_TM0010TM_C N_百智思 Logo		Image Mark	China	None	56752012	56752012	06/08/2021	12/14/2021	12/14/2031	Registered	IC 09
3342_TM0005Madrid _CA	Blaize	Word Mark	Canada	88604078 (3342_T M0005)	1523234	1,523,234	2/28/2020	2/28/2020		Filed 03/29/22, published for opposition	IC 09

EXHIBIT D

Mask Works

None.