

05-14-2003

FORM PTO-1594  
(Rev. 03/01)  
OMB No. 0651-0027 (exp. 05/31/2002)



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HEET

U.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

5.17.03

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

<p>1. Name of conveying party(ies): <b>LAYER N NETWORKS, INC.</b></p> <p><input type="checkbox"/> Individual(s)                      <input type="checkbox"/> Association  <input type="checkbox"/> General Partnership              <input type="checkbox"/> Limited Partnership  <input checked="" type="checkbox"/> Corporation-State  <input type="checkbox"/> Other</p> <p>Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	<p>2. Name and address of receiving party(ies): Name: <b>Silicon Valley Bank</b> Internal Address: HA155</p> <p>Street Address: 3003 Tasman Drive</p> <p>City: Santa Clara                      State: CA                      ZIP: 95054</p>
<p>3. Nature of conveyance:</p> <p><input type="checkbox"/> Assignment                      <input type="checkbox"/> Merger  <input checked="" type="checkbox"/> Security Agreement              <input type="checkbox"/> Change of Name  <input type="checkbox"/> Other _____</p> <p>4/24/03</p>	<p><input type="checkbox"/> Individual(s) citizenship  <input type="checkbox"/> Association  <input type="checkbox"/> General Partnership  <input type="checkbox"/> Limited Partnership  <input checked="" type="checkbox"/> Corporation-State-<b>DE</b>  <input type="checkbox"/> Other</p> <p>If assignee is not domiciled in the United States, a domestic representative designation is attached: <input type="checkbox"/> Yes <input type="checkbox"/> No  Additional name(s) &amp; address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>

4. Application number(s) or registration number(s):	
A. Trademark Application No.(s) <b>76-408,523</b> <b>76-407,739</b>	B. Trademark No.(s)

RECEIVED  
MAY 12 AM 7:04  
FINANCE SECTION

Additional numbers attached?  Yes  No

<p>5. Name and address of party to whom correspondence concerning document should be mailed:</p> <p>Name: <b>Silicon Valley Bank</b></p> <p>Internal Address: Loan Documentation HA155</p> <p>Street Address: 3003 Tasman Dr.</p> <p>City: Santa Clara                      State: Ca                      ZIP: 95054</p>	<p>6. Total number of applications and registrations involved: <b>2</b></p> <p>7. Total fee (37 CFR 3.41):                      <b>\$ 65.00</b></p> <p><input checked="" type="checkbox"/> Enclosed  <input type="checkbox"/> Authorized to be charged to deposit account</p>
8. Deposit account number: (Attach duplicate copy of this page if paying by deposit account)	

DO NOT USE THIS SPACE

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01 FC:0521                      40.00 UP

02 FC:0522                      25.00 UP

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

*Mariel Artega*  
M Shannon Hubbard

Name of Person Signing                      *Mariel Artega*                      Signature                      **5/2/03**                      Date

Total number of pages including cover sheet, attachments, and document: **4**  
Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patent & Trademarks, Box Assignments  
Washington, D.C. 20231

**TRADEMARK**  
**REEL: 002732 FRAME: 0308**

# ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated June 29, 2001, by and between Layer N Networks, Inc. ("Grantor") with its principal office at 12401 Research Boulevard, Building 2, Suite 275, Austin, TX 78759, and Silicon Valley Bank ("Bank") with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Grantor, the owner of the intellectual property identified in the exhibit(s) attached hereto, has granted to Bank a security interest in the intellectual property described on exhibits(s) attached hereto, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Grantor has executed this Addendum to Intellectual Property Security Agreement as of April 24, 2003.

**GRANTOR:**

**LAYER N NETWORKS, INC.**

By: 

Name: Scott J. Perry

Title: V. of Finance / Treasurer

EXHIBIT "B"  
PATENTS

DESCRIPTION DATE	REGISTRATION/ APPLICATION NUMBER	REGISTRATION/ APPLICATION
METHOD FOR PERFORMING RING ARITHMETIC OPERATIONS	10/068,294	2/5/02
APPLICATION-SPECIFIC INFORMATION-PROCESSING METHOD, SYSTEM AND APPARATUS	10/068,295	2/5/02
EFFICIENT USE OF DRAM-BASED DEVICES FOR SMALL DISCONTIGUOUS MEMORY ACCESSES	10/078,253	2/16/02
COMPUTATIONAL METHOD, SYSTEM AND APPARATUS	10/078,252	2/16/02
SYSTEM AND METHOD FOR SMALL-AREA SYSTEM DATA PROCESSING	10/180,209	6/26/02
ENCRYPTED PACKET INSPECTION	10/165,426	6/7/02
CISPONENTIATION METHOD, SOFTWARE AND DEVICE FOR EXPONENTIATION	10/260,744	9/30/02
HIGH THROUGHPUT STATE MANAGEMENT FOR TCP NA- provisional		10/15/02

EXHIBIT "C"

TRADEMARKS

<u>TRADEMARK DESCRIPTION</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
LAYER N NETWORKS AND DESIGN	76-408,523		PENDING
ULTRALOCK	76-407,739		PENDING

Exhibit "A" attached to that certain Addendum to Intellectual Property Security Agreement.

EXHIBIT "A"  
COPYRIGHTS

SCHEDULE A - ISSUED COPYRIGHTS

<u>COPYRIGHT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
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SCHEDULE B - PENDING COPYRIGHT APPLICATIONS

<u>COPYRIGHT DESCRIPTION</u>	<u>APPLICATION NUMBER</u>	<u>DATE OF FILING</u>	<u>DATE OF CREATION</u>	<u>FIRST DATE OF PUBLIC DISTRIBUTION</u>
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SCHEDULE C - UNREGISTERED COPYRIGHTS (Where No Copyright Application is Pending)

<u>DESCRIPTION</u>	<u>CREATION</u>	<u>DISTRIBUTION FROM ASSIGNOR</u>	<u>ASSIGNOR</u>
Layer N on chip software	Currently in creation	Will be distributed with the chip	
Layer N MIB, PLI, CLI software	Currently in test	Will be distributed with the chip	

**RECORDED: 05/12/2003**

**TRADEMARK  
REEL: 002732 FRAME: 0313**