

02-13-2004

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FORM PTO-1594

REC

U.S. DEPARTMENT OF COMMERCE

(Rev. 03/01)



Patent and Trademark Office

OMB No. 0651-0027 (exp. 05/31/2002)

102670467

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

VIRTUAL SILICON TECHNOLOGY, INC. 2-9-04

- Individual(s)
- General Partnership
- Corporation-State
- Other
- Association
- Limited Partnership

Additional name(s) of conveying party(ies) attached?  Yes  No

3. Nature of conveyance:

- Assignment
- Security Agreement
- Other
- Merger
- Change of Name

Execution Date: 09/22/2003

2. Name and address of receiving party(ies):

Name: Silicon Valley Bank  
Internal Address: HA155

Street Address: 3003 Tasman Drive

City: Santa Clara State: CA ZIP: 95054

- Individual(s) citizenship
- Association
- General Partnership
- Limited Partnership
- Corporation-State-Delaware
- Other

If assignee is not domiciled in the United States, a domestic representative designation is attached:  Yes  No

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or registration number(s):

A. Trademark Application No.(s)

78/248,673

78/248,635

B. Trademark No.(s)

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Silicon Valley Bank

Internal Address: Loan Documentation HA155

Street Address: 3003 Tasman Dr.

City: Santa Clara State: Ca ZIP: 95054

6. Total number of applications and registrations involved: 2

7. Total fee (37 CFR 3.41): \$65.00

Enclosed

Authorized to be charged to deposit account

8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

MARIBEL ARTEAGA

01/26/04

Name of Person Signing

*Maribel Arteaga*  
Signature

Date

02/12/2004 MGETACHE 00000061 78248673

Total number of pages including cover sheet, attachments, and document:

40.00 Mail documents to be recorded with required cover sheet information to:

25.00 DP Commissioner of Patent & Trademarks, Box Assignments

Washington, D.C. 20231

01 FC:8521

02 FC:8522

TRADEMARK

REEL: 002913 FRAME: 0313

## ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated August 6, 1998. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Virtual Silicon Technology, Inc. ("Assignor"), with its principal office at 1322 Orleans Drive, Sunnyvale, CA 94089, the owner of the intellectual property identified in the exhibits attached hereto, has granted to SILICON VALLEY BANK ("Assignee"), with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054, a security interest in the intellectual property, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Assignor has executed this Addendum to Intellectual Property Security Agreement as of September 22, 2003.

VIRTUAL SILICON TECHNOLOGY, INC.

By: 

Name: Howard Hideshima

Title: CEO

**Exhibit A - Copyrights**

**Description**

**Registration/Applications #**

**Registration/  
Application Date**

NONE

## Exhibit B – Patents

<u>Description</u>	<u>Registration/ Applications #</u>	<u>Registration/ Application Date</u>
<u>CONTROL CIRCUITRY FOR A NON-VOLATILE MEMORY</u>	10/247,767	9/18/02
<u>INTEGRATED CIRCUIT HAVING A REDUCED SPACING BETWEEN A BUS AND ADJACENT CIRCUITRY</u>	10/245,794	9/16/02
<u>SRAM BIT LINE ARCHITECTURE</u>	6,657,880	12/2/03
<u>COMMON SOURCE EEPROM AND FLASH MEMORY</u>	6,606,265	8/12/03
<u>METHOD FOR FORMING AN EEPROM CELL TOGETHER WITH TRANSISTOR FOR PERIPHERAL CIRCUITS</u>	6,451,652	9/17/02

**Exhibit C – Trademarks**

<b><u>Description</u></b>	<b><u>Registration/ Applications #</u></b>	<b><u>Registration/ Application Date</u></b>
<u>YOUR SOURCE FOR IP</u>	78/248,673	5/12/03
<u>MICRO AMP</u>	78/248,635	5/12/03

**Exhibit D – Mask Works**

**Description**

**Registration/  
Applications #**

**Registration/  
Application Date**

NONE