

**TRADEMARK ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	ASSIGNS THE ENTIRE INTEREST AND THE GOODWILL		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
Enable Growth Partners, LP		06/25/2007	LIMITED PARTNERSHIP:

**RECEIVING PARTY DATA**

<b>Name:</b>	Tripath Technology Inc.
<b>Street Address:</b>	2560 Orchard Parkway
<b>City:</b>	San Jose
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95131
<b>Entity Type:</b>	CORPORATION:

**PROPERTY NUMBERS Total: 10**

Property Type	Number	Word Mark
Registration Number:	2809670	CLASS-T
Registration Number:	2526206	DIGITAL POWER PROCESSING
Registration Number:	2453669	DPP
Registration Number:	2398029	TRIPATH
Registration Number:	2685346	TRIPATH
Serial Number:	75545470	COMBINANT DIGITAL
Serial Number:	75545868	
Serial Number:	76096294	TIO
Serial Number:	76096234	TIO
Serial Number:	75501525	TRIPATH TECHNOLOGY

**CORRESPONDENCE DATA**

Fax Number: (973)422-2927  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
 Email: ageller@lowenstein.com

CH \$265.00 2809670

Correspondent Name: Lowenstein Sandler PC  
Address Line 1: 65 Livingston Avenue  
Address Line 4: Roseland, NEW JERSEY 07068

ATTORNEY DOCKET NUMBER:	18880-16
NAME OF SUBMITTER:	S. Jason Teele, Esq.
Signature:	/S. Jason Teele/
Date:	06/26/2007

**Total Attachments: 15**

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## TRADEMARK AND PATENT ASSIGNMENT

THIS TRADEMARK AND PATENT ASSIGNMENT (the "Assignment") is made by and between Enable Growth Partners, LP, having an address at One Ferry Building, Suite 255, San Francisco, California 94111 ("Assignor"), and Tripath Technology Inc., a Delaware corporation having an address at 2560 Orchard Parkway, San Jose, California 95131 ("Assignee").

### WITNESSETH:

WHEREAS, pursuant to various documents, including but not limited to (i) a certain Securities Purchase Agreement dated as of November 8, 2005 among Assignor, Assignee and certain other purchasers (with the Assignor, the "Purchasers") of the Assignee's 6% Senior Secured Convertible Debentures due November 8, 2007 in the original aggregate principal amount of \$5,000,000 (the "Debentures"), (ii) each writing evidencing the Debentures with an original issue date of November 8, 2005 issued by Assignee to the Purchasers and (iii) the Security Agreement dated as of November 8, 2005 (the "Security Agreement") among Assignee, all subsidiaries of the Assignee organized under the laws of the various states of the United States of America, the Purchasers and the Assignor as agent for the Purchasers, the Purchasers have extended the loans evidenced by the Debentures to the Assignee;

WHEREAS, pursuant to the Security Agreement, the Assignor perfected its security interest in the Assignee's patents set forth on Schedule A appended hereto (the "Patents") and set forth on Schedule F to the Security Agreement by filing a UCC-1 with the Delaware Department of State;

WHEREAS, Enable Capital Management, LLC ("Enable Capital Management"), is listed as the assignee of the patents of Assignor in a filing with the United States Patent and Trademark Office made on November 17, 2005 as evidenced on Reel 016793 and Frame 0178 (the "Patent Filing");

WHEREAS, Enable Capital Management is listed as the assignee of the trademarks (the "Trademarks") of Assignee in a filing with the United States Patent and Trademark Office made on November 17, 2005 as evidenced on Reel 003196 and Frame 0326 (the "Trademark Filing");

WHEREAS, Enable Capital Management assigned its rights and obligations under the Patent Filing and the Trademark Filing to Assignor Enable Growth Partners, L.P. in a certain Assignment of Rights Under Filings in the United States Patent and Trademark Office dated December 1, 2005;

WHEREAS, Section 15 of the Security Agreement provides inter alia that upon a default the Assignee authorizes and appoints the Assignor its lawful attorney-in-fact to transfer and assign any of the Assignee's trademarks, patents, copyrights or other Intellectual Property (as defined in the Security Agreement);

WHEREAS, pursuant to Section 15 of the Security Agreement, Assignor filed a Trademark and Patent Assignment with the United States Patent and Trademark Office made on January 12, 2007 as evidenced on Reel 018757 and Frame 0049 (the “2007 Filing”);

WHEREAS, Assignor has not assigned, transferred, conveyed, sold or liquidated any of the Patents and Trademarks and, at all times relevant to this Assignment, Assignee has utilized the Patents and Trademarks in the ordinary course of its business without interference, objection, or conditions placed upon such utilization by Assignor;

WHEREAS, on February 8, 2007, Assignee filed a voluntary petition for relief pursuant to chapter 11 of title 11 of the United States Code in the United States Bankruptcy Court for the Northern District of California (San Jose) (the “Bankruptcy Court”), Case No. 07-50359 (MM);

WHEREAS, pursuant to authority granted by the Bankruptcy Court, Assignee noticed an auction sale of substantially all of its assets including, without limitation, the Patents and Trademarks;

WHEREAS, on May 3, 2007, an auction was conducted by the Bankruptcy Court at which Cirrus Logic, Inc. (“Cirrus”) submitted the highest and best offer to buy the Patents and Trademarks (the “Cirrus Bid”), and such offer was accepted by the Debtor, Assignor and approved by the Bankruptcy Court (pending the entry of a final order);

WHEREAS, to ensure that Assignee has the appropriate right, title and interest in and to the Patents and Trademarks, and the requisite authority to transfer, convey and assign the Patents and Trademarks to Cirrus, subject to the terms and conditions of this Trademark and Patent Assignment, the Assignor desires to assign to Assignee, and Assignee desires to receive, all right, title, and interest in and to the trademark registrations set forth on Schedule A and all common law and other rights, worldwide, in and to the Trademarks that are the subject of such registrations;

NOW, THEREFORE, for good and valuable consideration, including the provisions and covenants herein and therein and the Cirrus Bid, the receipt and sufficiency of which is hereby acknowledged (except with respect to the Cirrus Bid, the receipt of which will occur on the date that the sale to Cirrus is consummated), Assignor and Assignee hereby agree as follows:

1. Subject to Assignor’s liens upon and security interests in the Patents granted by Assignee pursuant to the Security Agreement, all of which rights shall continue hereunder, Assignor hereby grants, assigns, transfers, and delivers to Assignee all of its rights, title and interests, to the extent the same was obtained by Assignor in the 2007 Filing, in and to the Patents, including all contracts, rights and obligations relating thereto, and all continuing applications, reissues, divisions, continuations, continuations in part, extensions, renewals and reexaminations of any of the Patents, to be held and enjoyed by Assignee for its own use and benefit and for the use and benefit of its successors, assigns and legal representatives, to be used as fully and entirely as said rights would have been held and enjoyed by Assignor had this

assignment and sale not been made, together with all claims for damage by reason of past or future infringement of the Patents with the right to sue and collect the same for its own use or for the use of its successors, assigns or other legal representatives.

2. Subject to Assignor's liens upon and security interests in the Trademarks granted by Assignee pursuant to the Security Agreement, all of which rights shall continue hereunder, Assignor hereby grants, assigns, transfers, and delivers to Assignee all of its rights, title and interests, to the extent the same was obtained by Assignor in the 2007 Filing, in and to the Trademarks, including without limitation the goodwill of the business appurtenant thereto and which is symbolized thereby, and the right to renew any registration therefor, to be held and enjoyed by Assignee for its own use and benefit and for the use and benefit of its successors, assigns and legal representatives, to be used as fully and entirely as said rights would have been held and enjoyed by Assignor had this assignment and sale not been made, together with all claims for damage by reason of past, present or future infringement of said Trademarks with the right to sue and collect the same for its own use or for the use of its successors, assigns or other legal representatives.

3. Assignor represents and warrants to Assignee that from January 12, 2007 and continuing to the date of this Assignment, except for the 2007 Filing Assignor has not transferred or encumbered any of the Patents or Trademarks.

4. This Trademark and Patent Assignment shall be construed, performed and enforced in accordance with, and governed by, the laws of the State of New York, without giving effect to the principles of conflicts of laws thereof, and to the United States Bankruptcy Code, to the extent applicable.

5. This Trademark and Patent Assignment may be executed simultaneously in one or more counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.

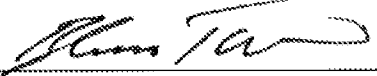
6. All capitalized terms used herein and not otherwise defined shall have the meanings ascribed thereto in the Security Agreement.

7. Assignor hereby requests the Commissioner of Patents and Trademarks, and the corresponding entities or agencies in any applicable foreign countries, to record Assignee as the assignee and owner of the Patents.

**IN WITNESS WHEREOF**, each of the undersigned has caused this Trademark and Patent Assignment to be executed by its officer thereunto duly authorized, as of this 25th day of June, 2007.

**ASSIGNOR**

Enable Growth Partners, LP

By:   
Name: Brendan O'Neil  
Title: Principal and Portfolio Manager

STATE OF CALIFORNIA            )  
  )  
COUNTY OF SAN FRANCISCO    )

On this 25th day of June, 2007, before me personally appeared, Brendan O'Neil, known to me (~~or proved to me~~ on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacities, and that by his signature on the instrument, the entities upon behalf of which the person acted, executed the instrument.

*see attached*

WITNESS my hand and official seal.

  
\_\_\_\_\_  
NOTARY PUBLIC

My commission expires: 12-9-2009

[signature page to Trademark and Patent Assignment]

**CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT**

State of California

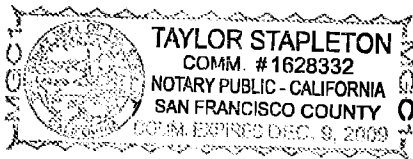
County of San Francisco

On 10/25/2007 before me, Taylor Stapleton, Notary Public

personally appeared Brendan O'Neil

personally known to me

(or proved to me on the basis of satisfactory evidence)



to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and official seal.

Taylor Stapleton  
Signature of Notary Public

Place Notary Seal Above

**OPTIONAL**

*Though the information below is not required by law, it may prove valuable to persons relying on the document and could prevent fraudulent removal and reattachment of this form to another document.*

**Description of Attached Document**

Title or Type of Document: \_\_\_\_\_

Document Date: \_\_\_\_\_ Number of Pages: \_\_\_\_\_

Signer(s) Other Than Named Above: \_\_\_\_\_

**Capacity(ies) Claimed by Signer(s)**

Signer's Name: \_\_\_\_\_

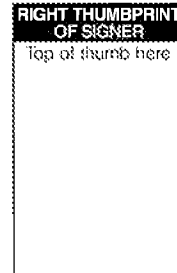
- Individual
- Corporate Officer — Title(s): \_\_\_\_\_
- Partner —  Limited  General
- Attorney in Fact
- Trustee
- Guardian or Conservator
- Other: \_\_\_\_\_



Signer Is Representing: \_\_\_\_\_

Signer's Name: \_\_\_\_\_

- Individual
- Corporate Officer — Title(s): \_\_\_\_\_
- Partner —  Limited  General
- Attorney in Fact
- Trustee
- Guardian or Conservator
- Other: \_\_\_\_\_



Signer Is Representing: \_\_\_\_\_

ASSIGNEE

Tripath Technology, Inc.

By: 

Name: Gary Sawka

Title: CFO and Responsible Individual

STATE OF CALIFORNIA )

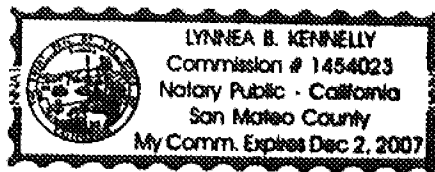
COUNTY OF SANTA CLARA )

On this 25<sup>th</sup> day of <sup>June</sup>~~January~~, 2007, before me personally appeared, Gary Michael Sawka, known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacities, and that by his signature on the instrument, the entities upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.

  
NOTARY PUBLIC

My commission expires: 12-2-2007



[signature page to Trademark and Patent Assignment]



Schedule A  
Trademarks and Patents

See the attached excerpt from Schedule F to the Security Agreement.

Tripath Technology Inc. Patents

Title	Serial No./ Patent No.	Filing Date/ Issue Date
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	08/858,544 5,974,089	7/22/97 10/26/99
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	88937827.5	7/20/98
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	2003-504469	7/20/98
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	20000340-0 70509	7/20/98 2/7/02
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	88/019,217 5,999,153	2/5/88 6/1/99
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	88107176 135686	4/30/99 10/26/01
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	99982754.0	2/5/99

Title	Serial No./ Patent No.	Filing Date/ Issue Date
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	2000-530980	2/5/99
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	2000-04232-5 74974	2/5/99 8/16/02
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	09/162,243 8,107,844	9/28/99 8/22/03
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	8811635 129936	9/28/99 8/1/01
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	99949856.7	9/24/99
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	2000-572990	9/24/99
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	99/156,262 6,127,893	9/18/99 10/3/00
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	88116025 130682	9/16/99 8/16/01
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	99948313.4	9/17/99
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	2080-571560	9/17/99
POWER EFFICIENT LINE DRIVER	09/432,507 6,245,283	11/2/99 6/12/01
POWER EFFICIENT LINE DRIVER	09/769,234 6,281,747	1/24/01 6/28/01

Title	Serial No./ Patent No.	Filing Date/ Issue Date
POWER EFFICIENT LINE DRIVER	89194095	3/7/90
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	09/432,296 6,229,390	11/2/99 5/8/03
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	09/739,005 6,297,697	1/11/01 12/2/01
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	89104952 146839	3/7/2000 5/8/02
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	80915985.6	3/1/90
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	2000-604520	3/1/00
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	09/906,319 6,329,876	9/27/99 12/11/01
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	99965342.1	12/28/99
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	2000-592932	12/28/99
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	88123145 146300	12/28/99 5/5/02
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	09/908,862 6,566,946	7/18/01 8/28/03
POWER SUPPLY TOPOLOGY TO REDUCE THE EFFECTS OF SUPPLY PUMPING	09/487,084 6,169,681	9/28/99 3/2/01
POWER SUPPLY TOPOLOGY TO REDUCE THE EFFECTS OF SUPPLY PUMPING	89164095 146798	3/2/00 4/10/02
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	09/624,503 6,335,992	7/24/00 11/13/03

Title	Serial No./ Patent No.	Filing Date/ Issue Date
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	89115266 191538	7/29/00 3/29/04
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	00948943.6	7/25/00
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	2001-514530	7/25/00
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	09/624,521 6,362,683	7/24/00 3/25/03
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	89115265 148952	7/29/00 5/9/02
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	2001-514535	7/26/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	09/624,509 6,353,184	7/24/00 2/28/03
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	2001-514534	7/26/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	89115264	7/29/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	10/057,790 6,580,322	1/24/02 6/17/03
OVERVOLTAGE PROTECTION CIRCUIT	09/736,267 6,840,703	12/15/00 5/5/03

Title	Serial No./ Patent No.	Filing Date/ Issue Date
IMPROVED POWER FET DRIVER CIRCUIT	09/755,833 6,362,679	1/19/01 3/26/03
IMPROVED POWER FET DRIVER CIRCUIT	90194210 173326	2/23/01 7/2/03
METHOD AND CIRCUIT TO OBTAIN HIGH FREQUENCY SWITCHING POWER FET STAGE FOR INDUCTIVE LOADS	09/590,926 6,517,642	10/17/00 9/9/03
METHOD AND CIRCUIT TO OBTAIN HIGH FREQUENCY SWITCHING POWER FET STAGE FOR INDUCTIVE LOADS	90103276 168866	2/23/01 4/23/03
RF COMMUNICATION SYSTEM USING AN RF DIGITAL AMPLIFIER	09/796,735 6,828,166	2/28/01 5/30/03
RF COMMUNICATION SYSTEM USING AN RF DIGITAL AMPLIFIER	90109025 163582	3/5/01 2/7/03
RESONANT GATE DRIVE TECHNIQUE FOR A DIGITAL POWER AMPLIFIER	09/796,734 6,577,154	2/29/01 6/18/03
SELF-TIMED SWITCHING FOR A DIGITAL POWER AMPLIFIER	09/796,733 6,549,669	2/29/01 4/15/03
DUAL INDEPENDENTLY CLOCKED ANALOG-TO- DIGITAL CONVERSION FOR A DIGITAL POWER AMPLIFIER	09/796,845 6,348,838	2/28/01 2/19/02
LOOP DELAY COMPENSATION FOR AN RF DIGITAL POWER AMPLIFIER	09/796,634 6,418,568	2/28/01 7/2/02
DYNAMICALLY DELAY COMPENSATION VERSUS AVERAGE SWITCHING FREQUENCY IN A MODULAR LOOP	09/836,188 6,518,849	4/16/01 2/11/03
ACTIVE COMMON MODE FEEDBACK	09/836,623 6,411,165	4/16/01 6/25/02
ACTIVE COMMON MODE FEEDBACK	10/137,105 6,603,355	5/1/02 8/5/03
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	09/836,134 6,693,491	4/16/01 2/17/04

Title	Serial No./ Patent No.	Filing Date/ Issue Date
DIGITAL SIGNAL PROCESSING UNIT WITH IMPROVED DISTORTION AND NOISE	05/835,622 6,515,654	4/15/01 2/4/03
A MUTE-IN-SILENCE SCHEME FOR AUDIO AMPLIFIERS	05/759,044 6,785,302	1/11/01 8/31/04
METHODS AND APPARATUS FOR ADAPTIVE EQUALIZATION	10/084,588	2/27/02
SCHEME FOR REDUCING TRANSMIT-BAND NOISE FLOOR AND ADJACENT CHANNEL POWER WITH POWER BACKOFF	09/988,967 6,577,189	7/18/01 6/18/03
SCHEME FOR MAXIMIZING EFFICIENCY OF POWER AMPLIFIER UNDER POWER BACKOFF CONDITIONS	09/968,879 6,630,899	7/18/01 10/7/03
METHOD FOR OPTIMAL OPERATION OF LOOP STRUCTURE OF CLASS-T AMPLIFIERS FOR FDD SYSTEMS	09/863,874 6,798,288	9/25/01 9/28/04
AN IMPROVED DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING AMPLIFIER	10/127,357 6,734,348	4/19/02 4/20/04
AN IMPROVED DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING AMPLIFIER	2002-584479	4/19/02
SUBSTRATE CONNECTION IN INTEGRATED POWER CIRCUIT	10/189,284 6,737,713	7/2/02 5/18/04
METHODS AND APPARATUS FOR FACILITATING NEGATIVE FEEDBACK, PROVIDING LOOP STABILITY, AND IMPROVING AMPLIFIER EFFICIENCY	10/107,524 6,621,319	3/26/02 9/16/03
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	10/454,789 6,781,458	6/3/03 8/24/04
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	*CT/US03/ 25153	8/12/03

Title	Serial No./ Patent No.	Filing Date/ Issue Date
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	63824347.4	8/12/03
A DC OFFSET SELF- CALIBRATION SYSTEM FOR A SWITCHING POWER AMPLIFIER	10/887,905	3/24/04
DIGITAL-TO-ANALOG CONVERTER WITH LEVEL CONTROL	10/900,500	7/28/04
INDUCTOR-BASED CURRENT SENSING	10/890,267	11/15/04
INDUCTOR-BASED CURRENT SENSING	PCT/US2004/ 038358	11/16/04
OVERCURRENT PROTECTION IN AMPLIFIER TOPOLOGIES EMPLOYING DC ISOLATION	10/990,268	11/15/04
OVERCURRENT PROTECTION IN AMPLIFIER TOPOLOGIES EMPLOYING DC ISOLATION	PCT/US04/35 359	11/16/04
OFFSET CANCELLATION IN A SWITCHING AMPLIFIER	11/000,215	11/28/04
INDUCTORLESS ARCHITECTURE FOR A SWITCHING AMPLIFIER	11/008,396	12/2/04
MODIFIED SIGMA-DELTA ARCHITECTURE WITH FREQUENCY LOCK	60/581,362	3/12/05
WIRELESS TRANSMITTER FRONT-END TOPOLOGY EMPLOYING AUXILIARY TRANSMIT-PATH FOR LOWER-POWERED SIGNALS TO ENHANCE RF POWER AMPLIFIER EFFICIENCY	60/583,123	5/17/05

**Tripath Technology Inc. Trademarks**

COUNTRY	MARK	APPL. NO.	FILING DATE	REG. NO.	REG. DATE
Canada	COMBINANT DIGITAL	1,006,794	25-Feb-1999		
Canada	DESIGN (T)	1,006,800	25-Feb-1999		
Canada	TRIPATH	1,006,801	25-Feb-1999		
China	COMBINANT	990019747	01-Mar-1999	1814827	28-Jan-2001
China	COMBINANT	990019749	01-Mar-1999	1448562	14-Sep-2000
China	DESIGN (T)	9900019748	01-Mar-1999	1505739	14-Jan-2001
China	DESIGN (T)	9900019746	01-Mar-1999	1475811	21-Aug-2000
China	TRIPATH	230018279	20-Aug-2000	3654287	21-Oct-2001
China	TRIPATH TECHNOLOGY	9900020211	03-Mar-1999		
China	TRIPATH TECHNOLOGY	9900020210	03-Mar-1999		
China	TRIPATH TECHNOLOGY	9900170589	17-Mar-1999	1519082	14-Mar-2000
European Union	TRIPATH	1812890	17-Aug-2000	1812890	17-Dec-2000
European Union	TRIPATH TECHNOLOGY	1089028	26-Feb-1999	1089024	06-Jan-2000
European Union	COMBINANT DIGITAL	1089066	26-Feb-1999	1089066	26-Feb-1999
European Union	DESIGN (T)	1089063	26-Feb-1999	901099661	21-Dec-2001
Hong Kong	COMBINANT DIGITAL	23141999	26-Feb-1999	20112301	31-Aug-1999
Hong Kong	COMBINANT DIGITAL	23141999	26-Feb-1999		
Hong Kong	DESIGN (T)	23141999	26-Feb-1999	200016785	31-Aug-1999
Hong Kong	DESIGN (T)	23141999	26-Feb-1999	43002507	11-Aug-1999
Hong Kong	TRIPATH	23161999	26-Feb-1999		
Hong Kong	TRIPATH	23161999	26-Feb-1999		
Japan	COMBINANT DIGITAL	0173181999	25-Feb-1999	4426096	20-Oct-2000
Japan	DESIGN (T)	0173171999	25-Feb-1999	4426095	20-Oct-2000
Japan	TRIPATH	909902900	17-Aug-1990	4312849	28-Dec-2001
Japan	TRIPATH TECHNOLOGY	0173161999	25-Feb-1999	4424789	24-Nov-2000
Korea	COMBINANT DIGITAL	1999-409	25-Feb-1999	1201	02-Jan-2000
Korea	DESIGN (T)	1999-408	25-Feb-1999	1202	02-Jan-2000
Korea	TRIPATH TECHNOLOGY	1999-410	25-Feb-1999	1139	01-Mar-2000
Singapore	COMBINANT DIGITAL	177499	25-Feb-1999		
Singapore	COMBINANT DIGITAL	177599	25-Feb-1999	T0001775D	31-Aug-1999
Singapore	DESIGN (T)	177299	25-Feb-1999	T5901772Z	11-Aug-1999
Singapore	DESIGN (T)	177399	25-Feb-1999	T9901773H	11-Aug-1999
Singapore	TRIPATH	T0008607Z	24-May-2000	T0008607Z	06-Dec-1999
Singapore	TRIPATH TECHNOLOGY	177699	25-Feb-1999		



COUNTRY	MARK	APPL. NO.	FILING DATE	REG. NO.	REG. DATE
Singapore	TRIPATH TECHNOLOGY	177199	23-Feb-1999		
Taiwan	COMBINANT DIGITAL	8808013	26-Feb-1999		16-Jan-2000
Taiwan	COMBINANT DIGITAL	8808012	26-Feb-1999	00133792	31-Dec-2000
Taiwan	DESIGN (T)	8808011	26-Feb-1999	00503008	01-Sep-2000
Taiwan	DESIGN (T)	8808010	26-Feb-1999	00150197	01-Jul-2000
Taiwan	TRIPATH	8804852	21-Aug-2000	984786	15-Feb-2002
Taiwan	TRIPATH TECHNOLOGY	8808014	26-Feb-1999	00915918	31-Dec-2000
Taiwan	TRIPATH TECHNOLOGY	8808015	26-Feb-1999	00127054	31-Aug-2000
U.S.	CLASS-T	76071870	20-Jan-2000	2,819,630	03-Feb-2004
U.S.	COMBINANT DIGITAL	75345470	31-Aug-1998		
U.S.	DESIGN (T)	75345863	31-Aug-1998		
U.S.	DIGITAL POWER PROCESSING	75386597	17-Nov-1998	2,526,256	01-Jan-2002
U.S.	DPP	75387530	12-Nov-1998	2,453,660	22-May-2001
U.S.	TIO	76056294	28-Jun-2000		
U.S.	TIO AND DESIGN	76056234	24-Jul-2000		
U.S.	TRIPATH				
U.S.	TRIPATH	75366017	08-Dec-1999	2,598,029	21-Oct-2000
U.S.	TRIPATH AND DESIGN (T)	76127810	31-Dec-2000	2,689,746	11-Feb-2003
U.S.	TRIPATH TECHNOLOGY	76301323	12-Jan-2001		