

**TRADEMARK ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
STRETCH, INC.		11/22/2010	CORPORATION:
<b>RECEIVING PARTY DATA</b>			
<b>Name:</b>	SILICON VALLEY BANK		
<b>Street Address:</b>	2400 HANOVER STREET		
<b>City:</b>	PALO ALTO		
<b>State/Country:</b>	CALIFORNIA		
<b>Postal Code:</b>	94304		
<b>Entity Type:</b>	CORPORATION: CALIFORNIA		
<b>PROPERTY NUMBERS Total: 3</b>			
<b>Property Type</b>	<b>Number</b>	<b>Word Mark</b>	
<b>Registration Number:</b>	3121371	STRETCH	
<b>Registration Number:</b>	3074231	EXTENDING THE POSSIBILITIES	
<b>Registration Number:</b>	2924167	STRETCH	
<b>CORRESPONDENCE DATA</b>			
<b>Fax Number:</b>	(404)962-6736		
	<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>		
<b>Phone:</b>	(404) 885-3038		
<b>Email:</b>	michael.brignati@troutmansanders.com		
<b>Correspondent Name:</b>	MICHAEL J. BRIGNATI, PH.D.		
<b>Address Line 1:</b>	TROUTMAN SANDERS LLP		
<b>Address Line 2:</b>	600 PEACHTREE STREET, N.E.		
<b>Address Line 4:</b>	ATLANTA, GEORGIA 30308-2216		
<b>ATTORNEY DOCKET NUMBER:</b>	220763.001096		
<b>NAME OF SUBMITTER:</b>	Michael J. Brignati, Ph.D.		

OP \$90.00 3121371

900176444

**TRADEMARK**  
**REEL: 004422 FRAME: 0195**

Signature:	/Michael J. Brignati 60,890/
Date:	11/29/2010
Total Attachments: 9 source=SVB-Stretch_Executed_IP_Agreement#page1.tif source=SVB-Stretch_Executed_IP_Agreement#page2.tif source=SVB-Stretch_Executed_IP_Agreement#page3.tif source=SVB-Stretch_Executed_IP_Agreement#page4.tif source=SVB-Stretch_Executed_IP_Agreement#page5.tif source=SVB-Stretch_Executed_IP_Agreement#page6.tif source=SVB-Stretch_Executed_IP_Agreement#page7.tif source=SVB-Stretch_Executed_IP_Agreement#page8.tif source=SVB-Stretch_Executed_IP_Agreement#page9.tif	

## INTELLECTUAL PROPERTY SECURITY AGREEMENT

THIS INTELLECTUAL PROPERTY SECURITY AGREEMENT is entered into as of November 22, 2010 by and between SILICON VALLEY BANK ("**Bank**") and STRETCH, INC. ("**Grantor**").

### RECITALS

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "**Loans**") in the amounts and manner set forth in that certain Loan and Security Agreement by and between Bank and Grantor dated the Effective Date (as the same may be amended, modified or supplemented from time to time, the "**Loan Agreement**"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works (as each term is described below) to secure the obligations of Grantor under the Loan Agreement.

B. Pursuant to the terms of that certain First Amendment to Loan and Security Agreement, of even date herewith (the "**First Amendment**"), by and between Bank and Grantor, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

### AGREEMENT

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its intellectual property (all of which shall collectively be called the "**Intellectual Property Collateral**"), including, without limitation, the following:

(a) Any and all copyright rights, copyright applications, copyright registrations and like protections in each work or authorship and derivative work thereof, whether published or unpublished and whether or not the same also constitutes a trade secret, now or hereafter existing, created, acquired or held, including without limitation those set forth on Exhibit A attached hereto (collectively, the "**Copyrights**");

(b) Any and all trade secrets, and any and all intellectual property rights in computer software and computer software products now or hereafter existing, created, acquired or held;

(c) Any and all design rights that may be available to Grantor now or hereafter existing, created, acquired or held;

(d) All patents, patent applications and like protections including, without limitation, improvements, divisions, continuations, renewals, reissues, extensions and continuations-in-part of the same, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the “**Patents**”);

(e) Any trademark and servicemark rights, whether registered or not, applications to register and registrations of the same and like protections, and the entire goodwill of the business of Grantor connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the “**Trademarks**”);

(f) All mask works or similar rights available for the protection of semiconductor chips, now owned or hereafter acquired, including, without limitation those set forth on Exhibit D attached hereto (collectively, the “**Mask Works**”);

(g) Any and all claims for damages by way of past, present and future infringements of any of the rights included above, with the right, but not the obligation, to sue for and collect such damages for said use or infringement of the intellectual property rights identified above;

(h) All licenses or other rights to use any of the Copyrights, Patents, Trademarks, or Mask Works and all license fees and royalties arising from such use to the extent permitted by such license or rights;

(i) All amendments, extensions, renewals and extensions of any of the Copyrights, Trademarks, Patents, or Mask Works; and

(j) All proceeds and products of the foregoing, including without limitation all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement and the First Amendment. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

[Signature Page Follows.]

IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.


Address of Grantor:

1322 Orleans Drive  
Sunnyvale, California 94089

Attn: \_\_\_\_\_

GRANTOR:

STRETCH, INC.

By:   
Name: CRAIG LYTLE  
Title: PRESIDENT + CHIEF  
EXECUTIVE OFFICER

Address of Bank:

2400 Hanover Street  
Palo Alto, California 94304

Attn: Matt Wright

BANK:

SILICON VALLEY BANK


By:   
Name: Matthew Wright  
Title: RM

EXHIBIT A

Copyrights

<u>Description</u>	Registration/ Application <u>Number</u>	Registration/ Application <u>Date</u>
None		

EXHIBIT B

Patents

<u>Title</u>	<u>Patent/Patent Application Number (Publication Number)</u>	<u>Issue/Filing Date</u>
SYSTEM, APPARATUS AND METHOD FOR DATA PATH ROUTING CONFIGURABLE TO PERFORM DYNAMIC BIT PERMUTATIONS	7,620,764	11/17/2009
SYSTEMS AND METHODS FOR SELECTING INPUT/OUTPUT CONFIGURATION IN AN INTEGRATED CIRCUIT	7,613,900	11/03/2009
PROGRAMMABLE LOGIC CONFIGURATION FOR INSTRUCTION EXTENSIONS	7,610,475	10/27/2009
EXTENSION ADAPTER	7,590,829	09/15/2009
SYSTEMS AND METHODS FOR SOFTWARE EXTENSIBLE MULTI-PROCESSING	7,581,081	08/25/2009
SYSTEM, APPARATUS AND METHOD FOR IMPLEMENTING MULTIFUNCTIONAL MEMORY IN RECONFIGURABLE DATA PATH PROCESSING	7,526,632	04/28/2009
INSTRUCTION SET FOR EFFICIENT BIT STREAM AND BYTE STREAM I/O	7,421,561	08/02/2008
LONG INSTRUCTION WORD PROCESSING WITH INSTRUCTION EXTENSIONS	7,418,575	08/26/2008
DEFINING INSTRUCTION EXTENSIONS IN A STANDARD PROGRAMMING LANGUAGE	7,373,642	05/13/2008
VIDEO PROCESSING SYSTEM WITH RECONFIGURABLE INSTRUCTIONS	7,284,114	10/16/2007
TRANSITIVE PROCESSING UNIT FOR PERFORMING COMPLEX OPERATIONS	7,269,616	09/11/2007

SYSTEM, APPARATUS AND METHOD FOR DATA PATH ROUTING CONFIGURABLE TO PERFORM DYNAMIC BIT PERMUTATIONS	7,237,055	06/26/2007
MULTI-SCALE PROGRAMMABLE ARRAY	7,062,520	06/13/2006
SYSTEM AND METHOD FOR EFFICIENTLY MAPPING HETEROGENEOUS OBJECTS ONTO AN ARRAY OF HETEROGENEOUS PROGRAMMABLE LOGIC RESOURCES	7,000,211	02/14/2006
RECONFIGURABLE INSTRUCTION SET COMPUTING	6,954,845	10/11/2005
APPARATUS AND METHOD FOR SELF TESTING PROGRAMMABLE LOGIC ARRAYS	6,874,110	03/29/2005
DESIGN METHODOLOGY FOR MERGING PROGRAMMABLE LOGIC INTO A CUSTOM IC	6,857,110	02/15/2005
PROGRAMMABLE LOGIC CORE ADAPTER	6,744,274	06/01/2004
MULTI-SCALE PROGRAMMABLE ARRAY	6,633,181	10/14/2003
CARRY LOOKAHEAD FOR PROGRAMMABLE LOGIC ARRAY	6,426,648	07/30/2002
EFFICIENT AND ROBUST RANDOM ACCESS MEMORY CELL SUITABLE FOR PROGRAMMABLE LOGIC CONFIGURATION CONTROL	6,418,045	07/09/2002
EFFICIENT AND ROBUST RANDOM ACCESS MEMORY CELL SUITABLE FOR PROGRAMMABLE LOGIC CONFIGURATION CONTROL	6,292,388	09/18/2001
PROGRAMMABLE LOGIC CONFIGURATION FOR INSTRUCTION EXTENSIONS	12/561,184	09/16/2009



BUILT-IN SELF TEST FOR A PROGRAMMABLE LOGIC DEVICE USING LINEAR FEEDBACK SHIFT REGISTERS AND HIERARCHICAL SIGNATURE GENERATION (Abandoned)	09/738,055	12/14/2000
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EXHIBIT C

Trademarks

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
STRETCH (Registered)	3,121,371	07/25/2006
EXTENDING THE POSSIBILITIES (Registered)	3,074,231	03/28/2006
STRETCH (Registered)	2,924,167	02/01/2005
STRETCH (Abandoned)	78/193,566	12/11/2002

EXHIBIT D

Mask Works

<u>Description</u>	Registration/ Application <u>Number</u>	Registration/ Application <u>Date</u>
Aruba S5000	MW0000017144	06/08/2004

Tyson01 433151v1 220763.001096